

## FEATURES

- Small size and weight
- High-reliability design
- Hermetically sealed
- High transient immunity
- Qualified to MIL-PRF-83726/22
- Reverse Polarity Protection


## PRINCIPLE TECHNICAL CHARACTERISTICS

Seal:Hermetic Tested per MIL-STD-883, Method 1014 Condition B, C
Finish: per MIL-T-10727
Terminals:
"A" (Tin Plate)
"W" (Tin Plate)
Weight
$1 \times 10^{-8} \mathrm{~atm}, \mathrm{~cm}^{3} / \mathrm{s}$ max
leakage
Tin Plate
Solder-lug
Plug-in PCB mountable
0.5 Ounce max.

APPLICATION NOTE:
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## DESCRIPTION

The TD-1412 Time Delay Relays are designed with thick film hybrid microelectronics timing circuits and are packaged in a hermetically sealed military style enclosure. The TD-1412 series are qualified to MIL-PRF83726/22 and designed to withstand severe environmental conditions encountered in military/aerospace applications. These relays are suited for use in power control, communication circuits and many other applications where power switching and high reliability are required over a wide temperature range.

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Data sheets are for initial product selection and comparison. Contact Esterline Power Systems prior to choosing a component.

| Input (Control) Parameters |  |
| :---: | :---: |
| Timing: <br> a. Operation, (Flasher) <br> b. Method <br> c. Range <br> d. Accuracy | Repeat Cycle Timer Fixed Cycle 1 to 600 Cycles/Min. [5] $\pm 10 \%$ [1] |
| Recycle Time | 10 ms , Max [4] |
| Operations: (X1-X2) <br> a. Input \& Control Voltage <br> b. Operating Current | $\begin{aligned} & 18-32 \mathrm{Vdc} \\ & 5 \mathrm{~mA}, \mathrm{Max} @+25^{\circ} \mathrm{C} \end{aligned}$ |
| Transients: MIL-STD-704A, Limit 1 <br> a. Spike Susceptibility <br> b. Self-Generated Spikes | +80 Volts Max - 600 Volts Max None |
| Electromagnetic Interference Per MIL-STD-461A | Class 1D [2] |
| Duty Rating | Continuous |
| Output (Load) Parameters |  |
| Contact Form Contact Rating: Voltage Drop | SPST 250 mA, Max. 2 Vdc, Max. |
| Dielectric Strength: <br> a. @ Sea Level, 60 Hz <br> b. @ $80,000 \mathrm{ft}$., 60 Hz | $\begin{aligned} & 1000 \text { Vrms [3] } \\ & 350 \text { Vrms } \end{aligned}$ |
| Insulation Resistance @ 500 Vdc | $1000 \mathrm{M} \Omega$ [3] |

## GENERAL CHARACTERISTICS

| Ambient Temperatures Range: | -55 to $+125^{\circ} \mathrm{C}$ |
| :--- | :--- |
| a. Operating |  |
| b. Non-Operating | -55 to $+125^{\circ} \mathrm{C}$ |
| Vibration: <br> a. Sinusoidal |  |
| $10-80 \mathrm{~Hz}$ | $0.06 \mathrm{\prime} \mathrm{\prime} \mathrm{DA}$ <br> $80-3000 \mathrm{~Hz}$ |
| b. Random: $50-2000 \mathrm{~Hz}$, MIL-STD-810 | $0.4 \mathrm{G} / \mathrm{Hz}$ |
| Shock, $0.5 \mathrm{MS}, 1 / 2$ Sine, 3 Axis | $1,100 \mathrm{G}$ |
| Acceleration, in any Axis | 100 G |
| Life at Rated Resistive Load; Minimum | $1,000,000$ operations |

## NUMBERING SYSTEM



1. Model Number or Basic "MIL-PRF" Series number.
2. Military "Slash" number.
3. Timing Cycle, Fixed: 100 milliseconds to 60 seconds. (See Note 5).
4. Mounting style and quality level (See Note 6).

W = Printed circuit mountable.
A = Flange mount with solder hook terminals.


## NOTES

[1] The accuracy specification applies for any combination of operating temperature and voltage.
[2] EMI test limits will not be exceeded during the timing interval or when continuously energized under steady state conditions, per paragraph 3.23, MIL-PRF-83726C.
[3] Terminals X1 and X2 must be connected together during the test. Dielectric withstanding voltage and insulation resistance are measured at sea level between all mutually insulated terminals and between all terminals and case.
[4] Recycle time is defined as the maximum time power must be removed from terminal X 1 to assure that a new cycle can be completed within the specified timing tolerance.
[5] A four digit number defines the length of one complete cycle, expressed in milliseconds. "On" time is $50 \%$ of each cycle. The first three digits are significant figures, used to define the the specific cycle. The fourth digit represents the number of zeros to follow the first three digits.
Examples:

- $1001=1$ cycle/second ( 1,000 milliseconds cycle)
- $2500=4$ cycle/seconds ( 25 milliseconds cycle)
- 6002 = 1 cycle/minute (60,000 milliseconds cycle)
[6] Quality level as specified in MIL-R-83726B, paragraph 3.1.1, 3.1.2 and 3.1.3.


## DERATING OF CONTACTS FOR DC VOLTAGES ABOVE NOMINAL RATING

To establish a standard for the derating of relay contacts is, at best, a subjective practice. Limitations are governed by the type of relay, contact gap, maximum voltage capabilities of the relay contact system, and the contact material.

The most common method is to derate the contacts by use of the Power Formula, using the known current and voltage.
This method is valid only for Resistive Loads, and is an approximation only; keeping in mind the limitations mentioned above.

$$
\begin{aligned}
& \text { Power }=\text { IE (Current } \times \text { Voltage }) \\
& \qquad I_{2} E_{2}=2 / 3 I_{1} E_{1}
\end{aligned}
$$

Example:
A designer is working with a 55 volt DC system and has a relay rated at 10 amps resistive at 28 volts DC.
What is the maximum current that can be switched at 55 Vdc .

$$
\begin{gathered}
\mathrm{I}_{1}=10 \text { Amperes } \\
\mathrm{E}_{1}=28 \mathrm{VDC} \\
\mathrm{E}_{2}=55 \mathrm{VDC} \\
\mathrm{I}_{2}=?(\text { Current ratings at } 55 \mathrm{VDC} \text { Resistive }) \\
\mathrm{I}_{2} \mathrm{E}_{2}=2 \mathrm{I}_{1} \mathrm{E}_{1} / 3 \\
\mathrm{I}_{2}=2 \mathrm{I}_{1} \mathrm{E}_{1} / \mathrm{E}_{2} 3 \\
=2(10 \times 28) / 55 \times 3 \\
=560 / 165
\end{gathered}
$$

$$
\mathrm{I}_{2}=3.4 \text { Amperes at } 55 \mathrm{VDC}
$$

In addition, the user should always be concerned about the following:

1. Derating contacts that are rated for less than 10 Amperes at nominal voltage.
2. Derating contacts for use in system voltages above 130 Volts DC
