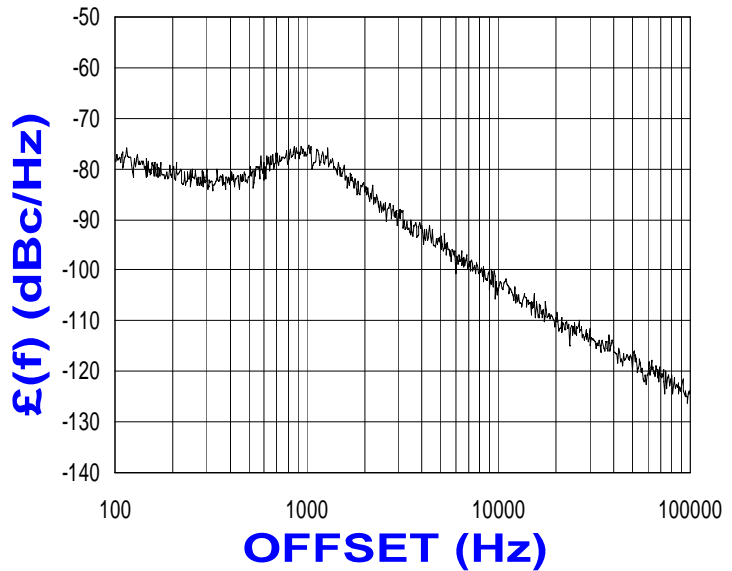


PHASE NOISE (1 Hz BW, typical)



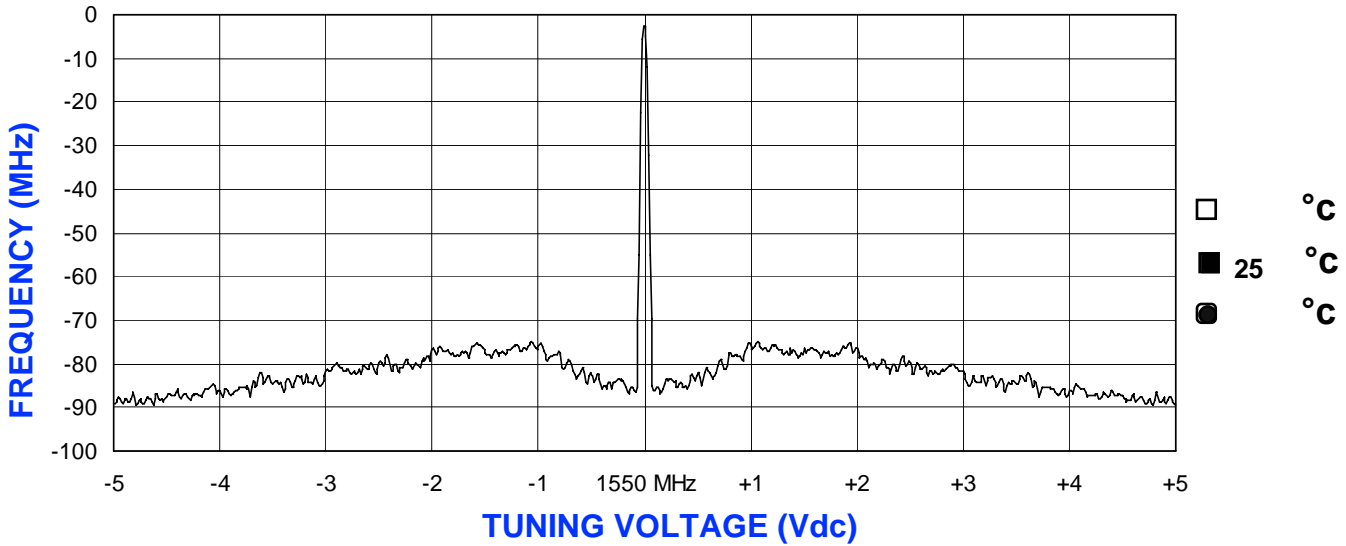
FEATURES
• Frequency Range: 1500 - 1600 MHz
• Step Size: 1000 KHz
• cPLL - Style Package
APPLICATIONS
• Fixed Wireless Broadband Equipment
• Point-to-Point Microwave Radio
• Satellite Communication Systems

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	1500 - 1600	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-103	dBc/Hz
Harmonic Suppression (2nd, typ.)	-20	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	1.5±2.5	dBm
Load Impedance	50	Ω
Step Size	1000	KHz
Charge Pump Output Current	1250	μ A
Switching Speed (typ., adjacent channel)	2	mSec
Startup Lock Time (typ.)	3	mSec
Operating Temperature Range	-40 to 85	$^{\circ}$ C
Package Style	cPLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	37	mA

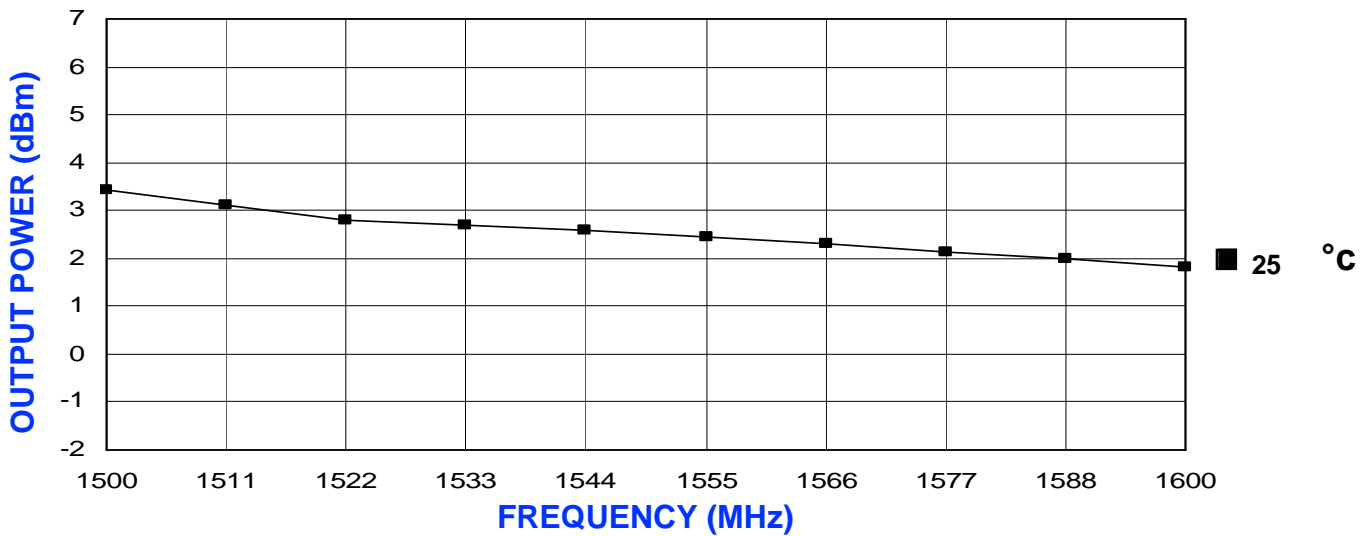
All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES
• AN-107 : How to Solder Z-COMM VCOs / PLLs
• AN-200 : Mounting and Grounding of Z-COMM PLLs
• AN-201 : PLL Fundamentals AN-202 : PLL Functional Description
NOTES:
Reference Oscillator Signal: $5 \text{ MHz} < f_{\text{osc}} < 100 \text{ MHz}$
Frequency Synthesizer: Analog Devices - ADF4113

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

