

### Endicott Research Group, Inc.

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# Specifications and Applications Information

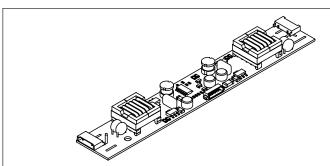
04/29/04 Preliminary

The ERG DMD42934 (DMD Series) DC to AC inverter features onboard connectors and can be easily dimmed using the onboard PWM Dimming or an external PWM generator.

Powered by a regulated +12 Volt DC source, the DMD42934 is designed to power the backlight of the Sharp LQ095Y5DR01.

#### **Product Features**

- ✓ Small Package Size, less than 13mm in height.
- ✓ High Dimming Ratio
- ✓ High Efficiency
- ✓ Made in U.S.A.



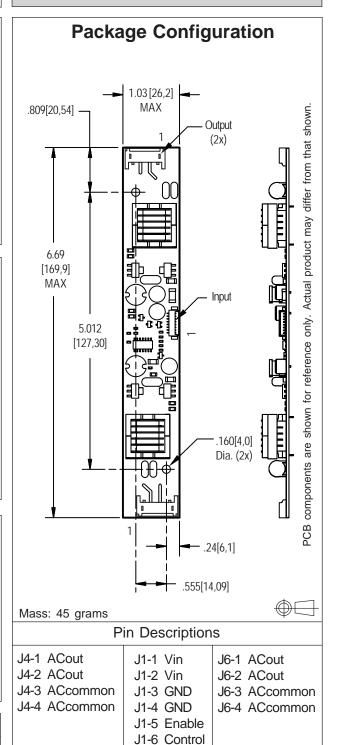
PCB components are shown for reference only. Actual product may differ from that shown.

**DMD** Package

Connectors			
Output	Input		
JST SM04(4.0)B-BHS-1-TB	Molex 53261-0890		

## **DMD42934**

# Four Tube DC to AC Inverter



J1-7 GND J1-8 GND



# Absolute Maximum Ratings (Note 1)

Rating	Symbol	Value	Units
Input Voltage	V <sub>in</sub>	-0.3 to +13.2	Vdc
Operating Temperature	T <sub>a</sub>	-0 to +85	°C
Storage Temperature	T <sub>s</sub>	-40 to +85	°C

#### **Recommended Operating Conditions**

Rating	Symbol	Value	Units
Input Voltage	V <sub>in</sub>	+10.8 to 12.6	Vdc
Operating Temperature (Note 2)	T <sub>a</sub>	0 to +50	°C

#### **Electrical Characteristics**

Unless otherwise noted Vin = 12.00 Volts dc and Ta = 25°C

Characteristic	Symbol	Min	Тур	Max	Units
Input Current	l in	-	.88	1.05	A <sub>DC</sub>
Input Ripple Current	l rip	-	20	-	mA <sub>pk-pk</sub>
Operating Frequency	F <sub>o</sub>	31	36	41	KHz
Efficiency	h	-	78	-	%
Output Voltage (no load)	V <sub>start</sub>	1650	-	-	V
Output Voltage (with lamp)	V <sub>out</sub>	-	490	-	V
Output Current (per tube)	l out	-	4.2	-	mArms
Enable (pin J1-5)					
Turn-off Threshold	V thoff	-	-	1	V
Turn-On Threshold	V thon	2.5	-	-	V
Impedance to Vin	R <sub>Disable</sub>	4.8	5.1	5.4	V

<sup>(</sup>Note 1) Reliable and predictable operation of the device is not guaranteed with applied stresses at or beyond those listed in "Absolute Maximum Ratings". Operation at these limits may reduce device reliability and is therefore not recommended. Please refer to "Recommended Operating Conditions" for reliable operation of the device.

(Note 2) Reliable operation above 50°C is possible if airflow is provided.

Input voltage specification modified for clarity on 4/2004.



#### **Onboard PWM**

Unless otherwise noted Vin = 12.00 Volts DC ,  $T_a = 25$  °C and unit has been running for 5 minutes.

Characteristic	Symbol	Min	Тур	Max	Units
Frequency	f <sub>pwm</sub>	-	160	-	$H_Z$
Control Input Bias Current	I chias	-	-	10	uA

#### **Pin Descriptions**

Vin Input voltage to the inverter. The four pins should be connected for optimum reliability and efficiency.

**GND** Inverter ground. The two pins should be connected for optimum reliability and efficiency.

Control Analog voltage input to the onboard pulse width modulator. Decreasing this voltage increases the ON

time of the onboard PWM resulting in increased brightness. The inverter is full ON when this voltage is

near inverter ground.

**Enable** Inverter Enable. Pull this pin low to disable inverter operation. This pin must be high to enable the

inverter. The onboard PWM is always utilized.

#### **Application information**

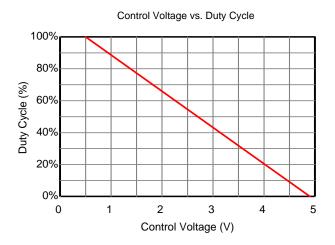
The DMD series of inverters is designed to power up to four cold cathode fluorescent tubes. An external analog control interfaces with an onboard pulse width modulator to provide dimming control. The DMD inverter can reliably dim to less than 5% duty cycle.

External shutdown of the inverter is accomplished using the Enable pin. Pulling this pin low (below Vthoff) disables the inverter. Enabling the inverter is accomplished by pulling this pin high (above Vthon).

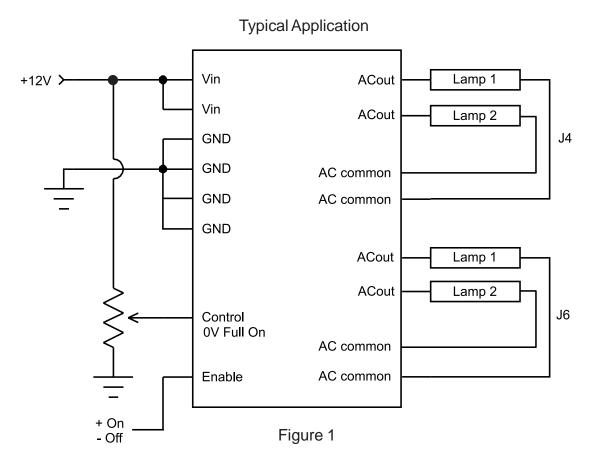
If analog voltage dimming is required, the analog voltage is applied to the Control pin. Figure 1 shows how to connect the inverter for onboard PWM operation. Graph 1 shows the relationship of PWM duty cycle to input control voltage.

If an external PWM is used, simply connect the Enable pin to the PWM source and connect the Control pin to inverter Ground. If the onboard PWM is used, connect the analog voltage to the Control pin.





Graph 1





Endicott Research Group, Inc. (ERG) reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by ERG is believed to be accurate and reliable. However, no responsibility is assumed by ERG for its use.