

Description

The ACE2303 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and Battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

Features

- -30V/-2.6A, $R_{DS(ON)}=130m\Omega@V_{GS}=-10V$
- -30V/-2.0A, R_{DS(ON)}=180m Ω @V_{GS}=-4.5V
- Super high density cell design for extremely low R_{DS(ON)}
- Exceptional on-resistance and maximum DC current capability
- SOT-23-3L package design

Application

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

Absolute Maximum Ratings

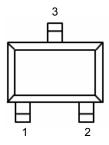
(T_A=25° Unless otherwise noted)

Parameter	Symbol Typical		Unit		
Drain-Source Voltage	V_{DSS}	-30	V		
Gate-Source Voltage	V_{GSS}	±20	V		
	T _A =25°ℂ	I _D	-3.0	Α	
	T _A =70°C	טי	-2.0		
Pulsed Drain Current	I_{DM}	-10	Α		
Continuous Source Current (Diode Cor	Is	-1.25	Α		
Power Dissipation	T _A =25°℃	P_{D}	1.25	W	
i owei Dissipation	T _A =70°C	ı D	0.8		
Operating Junction Temperature		T_J	150	$^{\circ}\mathbb{C}$	
Storage Temperature Range		T _{STG}	-55/150	$^{\circ}\mathbb{C}$	
Thermal Resistance-Junction to Ambient		$R_{\theta JA}$	R _{θ JA} 100		

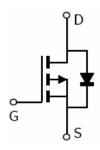


Packaging Type

SOT-23-3

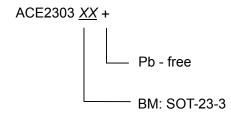


Pin	Description		
1	Gate		
2	Source		
3	Drain		



Ordering information

Selection Guide





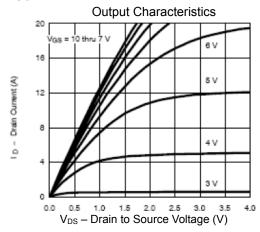
Electrical Characteristics

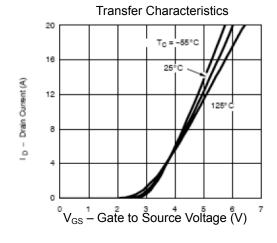
(T_△=25°C. Unless otherwise noted)

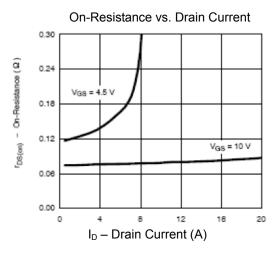
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} =0V, I_D =-10uA	-30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1.0		-3.0	
Gate Leakage Current	I _{GSS}	V_{DS} =0.V, V_{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V			-1	uA
		V_{DS} =-30V, V_{GS} =0V T_J =55 $^{\circ}$ C			-10	
On-State Drain Current	I _{D(ON)}	$V_{DS} \leq -5V$, $V_{GS} = -10V$	-6			Α
Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-2.6A		0.095	0.130	
		V_{GS} =-4.5V, I_{D} =-2.0A		0.125	0.180	Ω
Forward Transconductance	gfs	V _{DS} =-10V, I _D =-1.7A		2.4		S
Diode Forward Voltage	V_{SD}	I _S =-1.25A, V _{GS} =0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Q_g	V _{DS} =-15V, V _{GS} =-10V,		5.8	10	
Gate-Source Charge	Q_{gs}			8.0		nC
Gate-Drain Charge	Q_{gd}	I _D ≡-1.7A		1.5		
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, f=1MHz		226		
Output Capacitance	C _{oss}			87		pF
Reverse Transfer Capacitance	C_{rss}			19		
t _{d(on)}	$t_{d(on)}$	V _{DD} =-15V, R _L =15Ω		9	20	
				9	20	
	$t_{d(off)}$			18	35	ns
	K ^G =077		6	20		

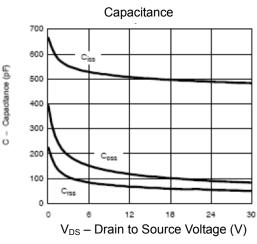


Typical Characteristics





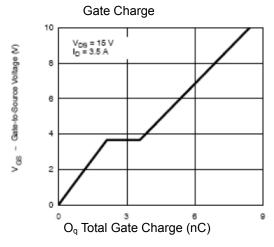


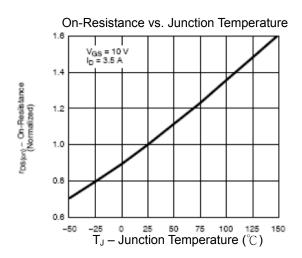


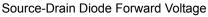


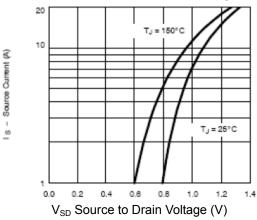
ACE2303

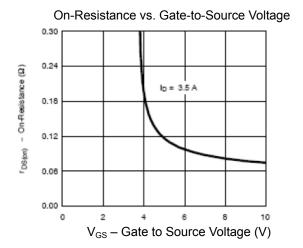
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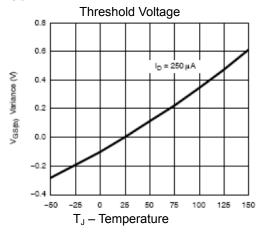


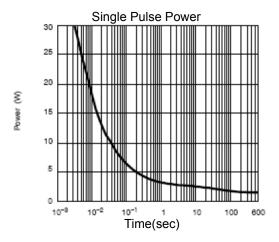


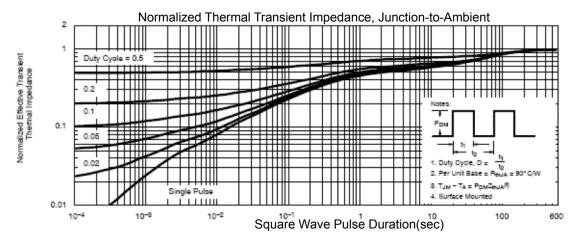




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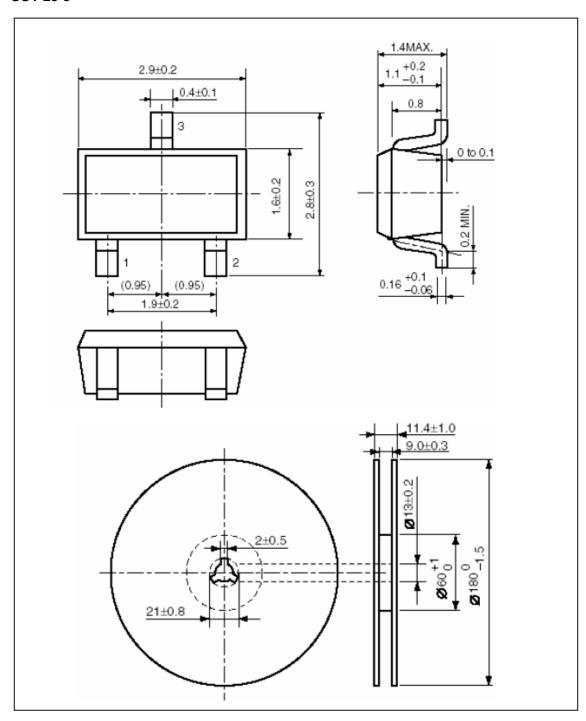






Packing Information

SOT-23-3





Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and shoes failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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