

Memory Module Specification

KVR800D2S5/1G 1GB 128M x 64-Bit DDR2-800 CL5 200-Pin SODIMM

DESCRIPTION:

This document describes ValueRAM's 128M x 64-bit 1GB (1024MB) DDR2-800 CL5 SDRAM (Synchronous **DRAM**) memory module. The components on this module include sixteen 64M x 8-bit DDR2-800 SDRAM in FBGA packages. This 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

- Clock Cycle Time (tCK) CL=5
- \checkmark Row Cycle Time (tRC)
- Refresh to Active/Refresh Command Time (tRFC)
- \mathbf{V} Row Active Time (tRAS)
- Single Power Supply of
- Dever 2
- **UL** Rating

2.5ns (min.) / 8ns (max.) 57.5ns (min.) 105ns 45ns (min.) / 70,000ns (max.) +1.8V (+/- .1V) 1.800 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 30mm.

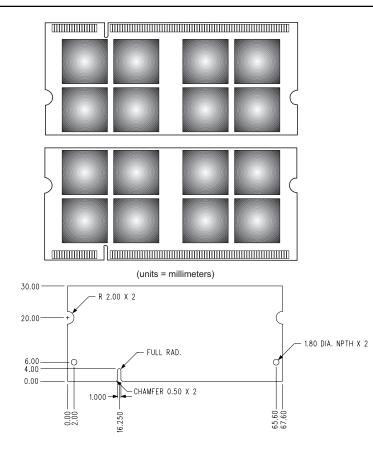


Figure 1: KVR800D2S5/1G 200-Pin SODIMM

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