

Memory Module Specification

## KVR400D2N3/1G 1GB 128M x 64-Bit DDR2-400 CL3 240-Pin DIMM

## **DESCRIPTION:**

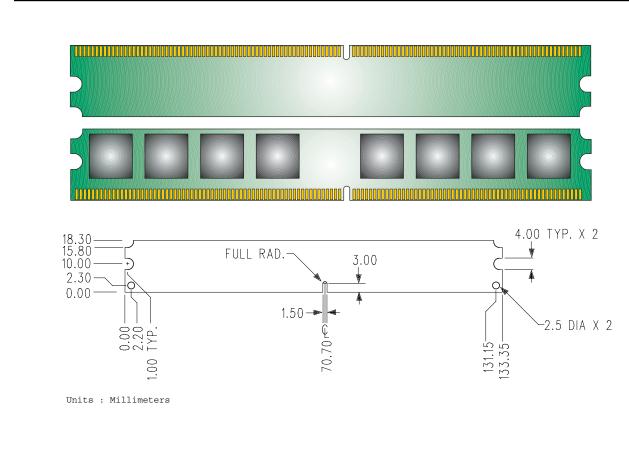
This document describes ValueRAM's 128M x 64-bit (1GB) DDR2-400 CL3 SDRAM (Synchronous **DRAM**) memory module. The components on this module include eight 128M x 8-bit DDR2-400 SDRAM in FBGA packages. This 240-pin DIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

## FEATURES:

- ☑ Clock Cycle Time (tCK) CL=3
- $\checkmark$  Row Cycle Time (tRC)
- Refresh to Active/Refresh Command Time (tRFC)
- $\mathbf{V}$  Row Active Time (tRAS)
- Single Power Supply of
- Dever Power
- **UL** Rating

5ns (min.) / 8ns (max.) 55ns (min.) 127.5ns 40ns (min.) / 70,000ns (max.) +1.8V (+/- .1V) 1.025 W (operating) 94 V - 0

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here.



## Figure 1: KVR400D2N3/1G 240-Pin DIMM