

Description

The function of this low reset Type IC is to accurately reset systems after detecting the supply voltage at the time of switching power on and instantaneous power off in various CPU and other logic system. Further, this IC, with its super low consumption current is most suited as a voltage check circuit for a number of products which use batteries.

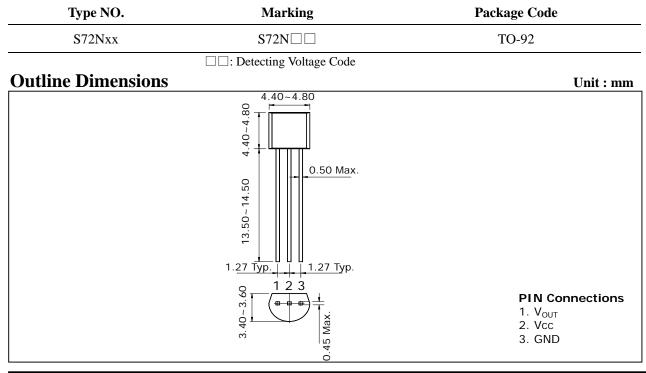
Features

- Super low current consumption (I_{CCH} = 1.0uA Typ.)
- High current of output transistor ($I_{OL} = 20mA$ Typ.)
- Hysteresis circuit built in ($\triangle V_S = 100mV$ Typ.)
- It has on delay function to supplement the constant of outer C and R.

Applications

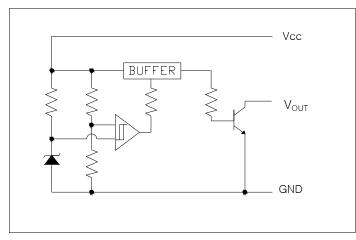
- Reset circuits for microcomputers, CPU and MPU.
- Reset circuit for logic circuitry.
- Battery voltage check circuit.
- Circuit for changing over to backup battery.
- Level detecting circuit.

Ordering Information



KSD-I0A010-000

Equivalent Circuit Diagram



Maximum ratings

Maximum ratings			(Ta=25°C)	
Characteristic	Symbol	Ratings	Unit	
Supply Voltage	V _{CC}	-0.3 ~ +10	V	
Power Dissipation	P _D	500	mW	
Output Voltage	V _{OUT}	-0.3 ~ +10	V	
Operating Temperature Range	T _{OPR}	-20 ~ +75	°C	
Storage Temperature Range	T _{STG}	-40 ~ +125	°C	

Electrical Characteristics

 $(V_{CC}=5V, Ta=25^{\circ}C)$

Characteristic	Symbol	Test Circuit	Test Condition Min		Тур.	Max.	Unit	
Detecting Voltage	Vs	1	* See Table 1					
Hysteresis Voltage	ΔV_{S}	1	$R_L = 470 \Omega, V_{CC} = L \rightarrow H \rightarrow L$		100	300	mV	
Temperature Coefficient of Detecting Voltage	$V_S\!/\DeltaT$	1	R_L = 470 Ω , Ta= -20~75 $^\circ \! \mathbb{C}$	-	±0.01	-	%/°C	
Low Level Output voltage	V _{OL}	1	$R_L = 470 \Omega$, $V_{CC} = V_S Min-0.05 V$	-	0.1	0.4	v	
Output Leakage Current	I_{LEAK}	1	$V_{OUT} = 10V$	-	-	0.1	μΑ	
Circuit Current at ON	I _{CCL}	1	$V_{CC} = V_S Min - 0.05 V$	-	100	180	μΑ	
Circuit Current at OFF	I _{CCH}	1	V _{CC} = 5.25V -		1.0	2.5	μΑ	
Operating Voltage	V _{OPR}	1	R_{L} =4.7 kΩ, $V_{OL} {\leq} 0.4 V$ -		1.4	1.6	v	
Output Current at ON I	I _{OL} I	1	$R_L = 0 \Omega$, $V_{CC} = V_S$ Min	10	20	-	mA	
Output Current at ON II	I _{OL} II	1	$ \begin{array}{c} R_{L} = 0 \Omega , V_{CC} = V_{S} Min, \\ Ta = -20 {\sim} 75 ^{\circ} C $		-	-	mA	
L→H Transmission delay time	t _{PLH}	2	$R_L = 4.7 \text{ k}\Omega, C_L = 100 \text{ pF}$	-	100	500	μs	
H→L Transmission delay time	t _{PHL}	2	$R_L = 4.7 \text{ k}\Omega, C_L = 100 \text{ pF}$	-	10	20	μs	

V_S: Standard Detection Voltage

* Table 1

Electrical Characteristics

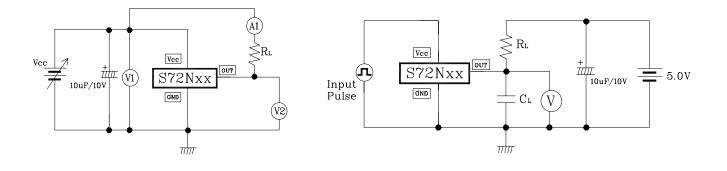
(V_{CC}=5V, Ta=25°C)

Characteristic	Symbol	Test Circuit	Test Condition		Min.	Тур.	Max.	Unit
Detecting voltage		1	$\begin{array}{c} R_L = 470 \Omega \\ V_{CC} = H \rightarrow L \\ V_{OL} \leq 0.4 V \end{array}$	S72N45	4.30	4.50	4.70	v
				S72N42	4.00	4.20	4.40	
				S72N39	3.70	3.90	4.10	
				S72N36	3.40	3.60	3.80	
	Vs			S72N33	3.10	3.30	3.50	
	v _s			S72N31	2.90	3.10	3.30	
				S72N29	2.75	2.90	3.05	
				S72N27	2.55	2.70	2.85	
				S72N25	2.35	2.50	2.65]
				S72N23	2.15	2.30	2.45	1

S72Nxx

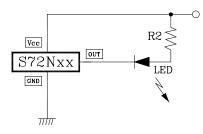
Test Circuit 1

Test Circuit 2



Application Circuit

(1) Battery Low Indicator

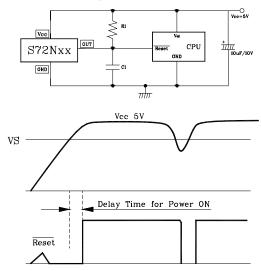


Note 1.

: Connecting of LED and R2

obtains a voltage drop indicator.

(2) Resetting for CPU

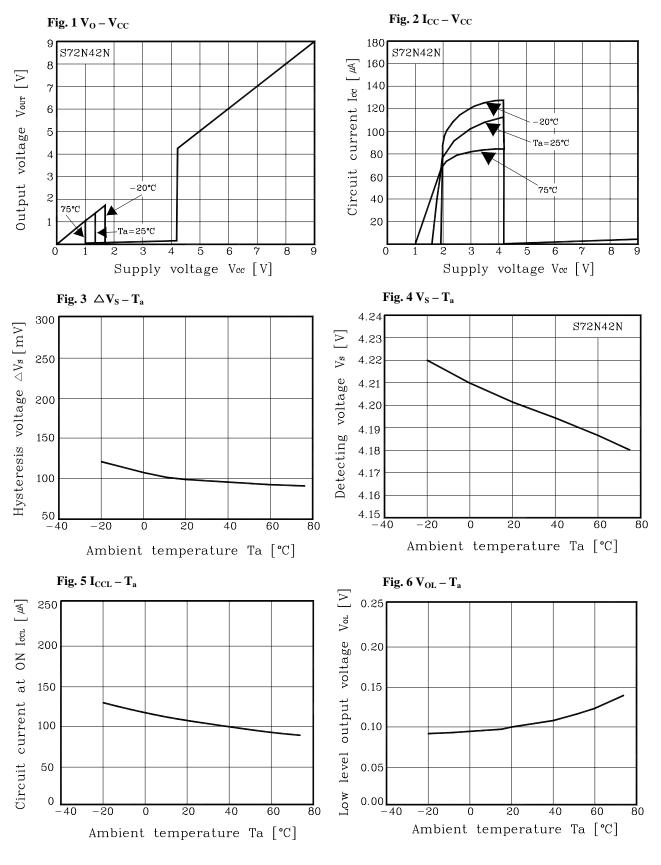


Note 2.

: Connecting of C1 and selection of time constant with C1 and R1 set the power on delay time.

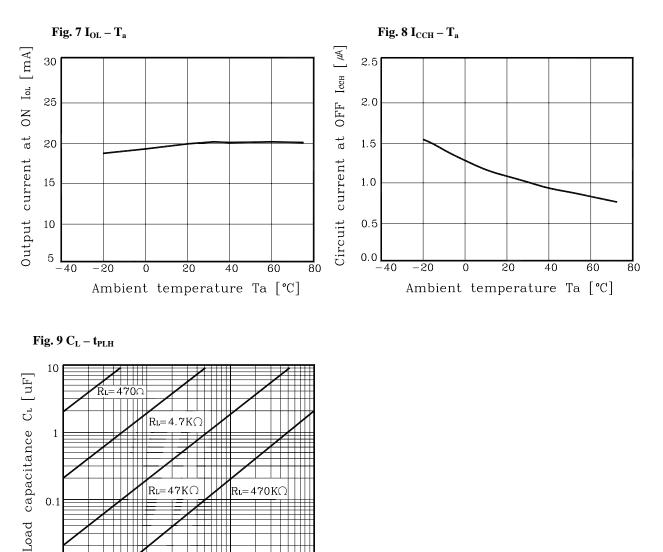
S72Nxx

Electrical Characteristic Curves



KSD-I0A010-000

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Specifications mentioned in this publication are subject to change without notice.

KSD-I0A010-000

 $R_L = 47 K\Omega$

L->H Transmission delay time tplH [ms]

10

0.1

0.01

 $R_{L}=470KC$

1000

100