DECT Power Amplifier 1880 - 1930 MHz



M/A-COM Products Rev. V1

Features

- Ideal for DECT Applications
- Power Set Pin for Adjustable Output Power High Power Mode: 25 dBm Low Power Mode: 15 dBm
- Power Gain: 25 dB Typical
- Voltage Supply Compensation
- Ramp Power Control: 1.7 V Enable
- Micro-Amp Shutdown Current
- Operates from 1.8 V to 3.6 V
- Lead-Free 3 mm 12-Lead PQFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAAPSS0113 is a three stage power amplifier designed for Digitally Enhanced Cordless Telephone applications. The power amplifier is available in a lead-free 3 mm 12-lead PQFN plastic package.

The MAAPSS0113 features an integrated power enable pin (5) for accurate ramp control and a separate power mode pin (2) for current savings in a low power mode state. Both control pins operate with 1.7 V logic. A voltage compensation circuit is also included for applications where the amplifier supply voltage is not regulated and is directly connected to the battery.

Ordering Information ^{1,2}

Part Number	Package	
MAAPSS0113TR-3000	3000 piece reel	
MAAPSS0113SMB	Sample Board 1880 - 1930 MHz tuning	

1. Reference Application Note M513 for reel size information.

2. All sample boards include 5 loose parts.

Functional Schematic



Pin Configuration

Pin No.	Pin Name	Description	
1	RF_{IN}	RF Input	
2	V _M	Power Mode	
3	GND	Ground	
4	N/C	No Connection	
5	V _{EN}	Power Enable	
6	PFC	Power Flatness Control	
7	$\mathrm{RF}_{\mathrm{OUT}}$ / $\mathrm{V}_{\mathrm{CC3}}$	RF Output, 3rd Stage Supply	
8	$\mathrm{RF}_{\mathrm{OUT}}$ / $\mathrm{V}_{\mathrm{CC3}}$	RF Output, 3rd Stage Supply	
9	$\mathrm{RF}_{\mathrm{OUT}}$ / $\mathrm{V}_{\mathrm{CC3}}$	RF Output, 3rd Stage Supply	
10	V _{CC2}	2nd Stage Supply	
11	GND	Ground	
12	V _{CC1}	1st Stage Supply	
Pad ³	GND	RF & DC Ground	

3. The exposed pad centered on the package bottom must be connected to RF and DC ground.

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications: F = 1880 - 1930 MHz, $T_A = 25 \text{ °C}$, $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Input Power		dBm		0	—
Input Return Loss	V _M = 0 V V _M = 1.7 V	dB dB		11 12	_
P _{OUT,} High Power Mode (HPM)	$V_{M} = 1.7 \text{ V}, V_{CC} = 3.0 \text{ V}$ $V_{CC} = 2.4 \text{ V}$ $V_{CC} = 2.0 \text{ V}$ $V_{CC} = 1.8 \text{ V}$		 22 	26 25.5 24 23.5	27 — —
P _{OUT} vs. Temperature, HPM	$T_A = 0$ °C to 50 °C, $V_{CC} = 2.4$ V	dB	_	0.8	—
P _{OUT} vs. V _{EN}	V_{EN} = 1.6 t o 1.9 V , V_{CC} = 2.4 V	dB		1	—
Current, HPM	$V_{M} = 1.7 V, V_{CC} = 3.0 V$	mA		350	500
P _{OUT,} Low Power Mode (LPM)	$V_{\rm M}$ = 0 V, $V_{\rm CC}$ = 2.4 V	dBm	12	15	—
Current, LPM	$V_{M} = 0 V, V_{CC} = 2.4 V$	mA	—	95	150
Current, Shutdown	V_{CC} = 3.0 V, V_{EN} = 0.5 V	μA	—	1	10
Control Pins	$\begin{array}{c} V_{\text{EN}}, V_{\text{M}} \text{ low} \\ V_{\text{EN}}, V_{\text{M}} \text{ high} \\ V_{\text{M}} \text{ current} \\ V_{\text{EN}} \text{ current} \end{array}$		0 1.6 —	 1.7 0.5 1	0.5 1.9 1 2
Harmonics	$V_{M} = 1.7 V, V_{CC} = 2.4 V $ 2f _o 3f _o	dBc dBc	_	-33 -35	_
Forward Isolation	$V_{EN} = 0 V, V_{CC} = 2.4 V$	dB		46	_
Stability	+1.5 V < V _{CC} < +3.5 V, P _{OUT} = HPM & LPM, VSWR < 6:1 -20°C < T_A < +70°C	All spurs < -60 dBc			
Turn on/off time	t _{on} : RF burst to (Avg Power – 1 dB) t _{off} : (Avg Power – 1 dB) to RF off		_	3 3	_

Absolute Maximum Ratings ^{4,5,6}

Parameter	Absolute Maximum	
Input Power	5 dBm	
Operating Supply Voltage	4.0 Volts	
Operating Control Voltage	3.0 Volts	
Operating Temperature	-20 °C to +85 °C	
Channel Temperature	150 °C	
Storage Temperature	-40 °C to +150 °C	

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

5. M/A-COM does not recommend sustained operation near these survivability limits.

6. For operation above 4V as required for 3 cell systems, a dropping diode is required. See application note page 6.

Logic Table ⁷

V _{EN}	V _M	State
0	0	OFF
0	1	OFF
1	0	LPM
1	1	HPM

7. Logic 1 = 1.6 V to 1.9 V, Logic 0 = 0 V to 0.5 V.

Operating the MAAPSS0113

The MAAPSS0113 is sensitive to electrostatic Use proper ESD control discharge (ESD). techniques when handling this device. To operate the MAAPSS0113, follow these steps. Ramp down or shut down in reverse order.

- 1. Apply V_{CC} (2.4 V).
- Apply V_M (0 or 1.7 V).
- 3. Apply P_{IN} (-2 to 2 dBm).
- 4. Ramp V_{EN} from 0 to 1.7 V.

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Evaluation Board Schematic



Transmission Line Dimensions, 0.20 mm thick FR4

Designator	Length (mm)	Width (mm)	
T1	10.67	.254	
T2	2.22	.380	
Т3	1.80	.380	
T4	0.735	.380	
Т5	4.275	.380	
Т6	3.13	.254	

Recommended PCB Configuration



Parts List

Component	Value	Foot Print	Manufacturer
C1 - C5	1 µF	0402	Murata
C6	68 pF	0402	Murata
C7	18 pF	0402	Murata
C8	330 pF	0402	Murata
C9	3 pF	0402	Murata
C10	10 pF	0402	Murata
C11	15000 pF	0402	Murata
L1	10 nH	0402	Coilcraft
R1	158 Ω	0402	Panasonic
R2	15 KΩ	0402	Panasonic

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High Power Mode Typical Performance, $V_M = 1.7 V$ (using the supplied sample board BOM)



P_{OUT} vs. Temperature @ 1900 MHz, P_{IN} = 0 dBm



P_{OUT}, 2f_o, 3f_o vs. P_{IN} @ 2.4 V



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Icc vs. Temperature @ 1900 MHz, P_{IN} = 0 dBm



 P_{OUT} , PAE, I_{CC} vs. Frequency @ $V_{CC} = 2.4$ V, $P_{IN} = 0$ dBm



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Low Power Mode Typical Performance, $V_M = 0 V$ (using the supplied sample board BOM)



P_{OUT} vs. Temperature @ 1900 MHz, P_{IN} = 0 dBm



Pout vs. V_{EN} @ $V_{CC} = 2.4$ V, VM = 1.7 V, $P_{IN} = 0$ dBm



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 I_{CC} vs. Temperature @ 1900 MHz, $P_{IN} = 0$ dBm



S11 vs. Frequency, State @ V_{cc} = 2.4V



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High Voltage Operation (3 Cells)

The MAAPSS0113 amplifier product is designed for use in phone systems using 2 battery cells for mobile units (3.6V charging, 2.4V nom.) or up to 3.6V regulated for base-station units. For operation with higher supply voltages such as in 3 cell mobile units (4.5V charging), a dropping diode is required to reduce the voltage to the amplifier. An example schematic is shown in Figure 1. A series diode is placed between the battery supply and the three stages of the amplifier. An example diode is the Vishay ES1A surface mount diode. This diode has a maximum forward current of 1A and a forward voltage drop of 1V.



Figure 1.

Supply schematic for 3 cell operation showing external dropping diode, ex. Vishay ES1A

Figure 2 shows the measured affect of the dropping diode on the supply voltage (V_{CC}) to the amplifier. Because of the approximate 1V forward drop of the diode, the maximum supply to the amplifier is 3.6V for a charging condition of 4.5V. Figures 3 and 4 show the power and current versus a 3 cell battery range, respectively. The 3 cell performance with the dropping diode is very similar to the electrical specification at 2.4V shown on page 2. The key to matching this data is choosing a diode with approximately 1V forward drop. Because of this forward drop, the maximum voltage from the battery supply can increase from 4V to 5V.

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Figure 3. Pout vs. 3 cell battery voltage



Figure 4. Current vs. 3 cell battery voltage

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Lead-Free 3 mm 12-lead PQFN[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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