



Amplifier, Power, 1.6W 12.75-15.35 GHz

MAAP-000071-PKG003 Rev — Advance Information

#### **Features**

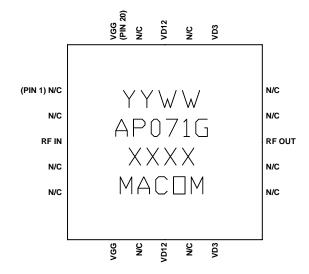
- ◆ 1.6 Watt Saturated Output Power Level
- ◆ Variable Drain Voltage (6-10V) Operation
- MSAG<sup>™</sup> Process

#### **Description**

The MAAP-000071-PKG0003 is a 4-stage 1.6 W power amplifier with on-chip bias networks in a 20 lead MLP package, allowing easy assembly. This product is fully matched to 50 ohms on both the input and output. It can be used as a power amplifier stage or as a driver stage in high power applications.

Each device is 100% RF tested to ensure performance compliance. The part is fabricated using M/A-COM's GaAs Multifunction Self-Aligned Gate (MSAG) Process.

M/A-COM's MSAG™ process features robust silicon-like manufacturing processes, planar processing of ion implanted transistors and multiple implant capability enabling power, low-noise, switch and digital FETs on a single chip. The use of refractory metals and the absence of platinum in the gate metal formulation prevents hydrogen poisoning when employed in hermetic packaging.



#### **Primary Applications**

- ♦ Point-to-Point Radios
  - 13 and 15 GHz Bands

#### Also Available in:

Description	Die	Ceramic Package	Die Sample Board	Die Mechanical Sample	Packaged Sample Board
Part Number	MAAPGM0071-DIE	MAAPGM0071	MAAP-000071-SMB004	MAAP-000071-MCH000	MAAP-000071-SMB003 (Lead Free)

### Electrical Characteristics: $T_B = 30^{\circ}C^1$ , $Z_0 = 50 \Omega$ , $V_{DD} = 8V$ , $I_{DQ} = 900 mA^2$ , $P_{in} = 10 dBm$ , $R_G = 100 \Omega$

Parameter	Symbol	Typical	Units	
Bandwidth	f	12.75-15.35	GHz	
Output Power	P <sub>out</sub>	32	dBm	
1-dB Compression Point	P1dB	31	dBm	
Power Added Efficiency	PAE	19	%	
Small Signal Gain	G	24	dB	
Input VSWR	VSWR	1.4:1		
Output VSWR	VSWR	2.7:1		
Gate Current	$I_{GG}$	10	mA	
Drain Current	I <sub>DD</sub>	1400	mA	
Output Third Order Intercept	TOI	40	dBm	
Third Order Intermod, Pout = 26 dBm (DCL)	IM3	33	dBc	

- 1. T<sub>B</sub> = MMIC Base Temperature
- 2. Adjust  $V_{\text{GG}}$  between -2.6 and -1.2V to achieve specified Idq.

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## **Maximum Ratings**<sup>3</sup>

Parameter	Symbol	Absolute Maximum	Units	
Input Power	P <sub>IN</sub>	15.0	dBm	
Drain Supply Voltage	$V_{DD}$	+12.0	V	
Gate Supply Voltage	$V_{GG}$	-3.0	V	
Quiescent Drain Current (No RF)	I <sub>DQ</sub>	1.42	А	
Quiescent DC Power Dissipated (No RF)	P <sub>DISS</sub>	14.2	W	
Junction Temperature	T <sub>J</sub>	170	°C	
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C	

<sup>3.</sup> Operation beyond these limits may result in permanent damage to the part.

### Recommended Operating Conditions<sup>4</sup>

Characteristic	Symbol	Min	Тур	Max	Unit
Drain Voltage	$V_{DD}$	6.0	8.0	10.0	V
Gate Voltage	$V_{GG}$	-2.6	-2.0	-1.2	V
Input Power	P <sub>IN</sub>		10.0	14.0	dBm
Thermal Resistance	$\Theta_{JC}$		10.8		°C/W
MMIC Base Temperature	Тв			Note 5	°C

<sup>4.</sup> Operation outside of these ranges may reduce product reliability.

### **Operating Instructions**

This device is static sensitive. Please handle with care. To operate the device, follow these steps.

- 1. Apply  $V_{GG} = -2.7 \text{ V}$ ,  $V_{DD} = 0 \text{ V}$ .
- 2. Ramp V<sub>DD</sub> to desired voltage, typically 8.0 V.
- 3. Adjust  $V_{GG}$  to set  $I_{DQ}$ , (approximately @ -2.0 V).
- 4. Set RF input.
- Power down sequence in reverse. Turn V<sub>GG</sub> off last.



<sup>5.</sup> MMIC Base Temperature = 170°C —  $\Theta_{JC}^* V_{DD} * I_{DQ}$ 

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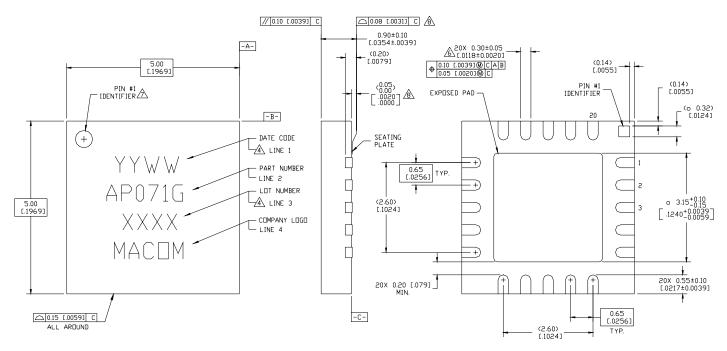
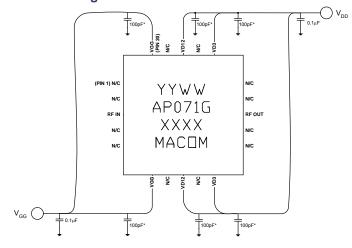


Figure 1. 5x5 mm 20-Lead MLP.



\* Place 100pF capacitors as close to the package as possible

Figure 2. Recommended Bias Configuration.

Note: The exposed pad centered on the package bottom must be connected to RF and dc ground for proper electrical and thermal operation.

Refer to M/A-COM Application Note *Surface Mounting Instructions for PQFN Packages #S2083*\* for assembly guidelines.

Additional Precaution: All parts must receive a bake-out of 125°C for 24 hours prior to any solder reflow operation.

\*Application Notes can be found by going to the Site Search Page of M/A-COM's web page (http://www.macom.com/Application%20Notes/index.htm) and searching for the required Application Note.

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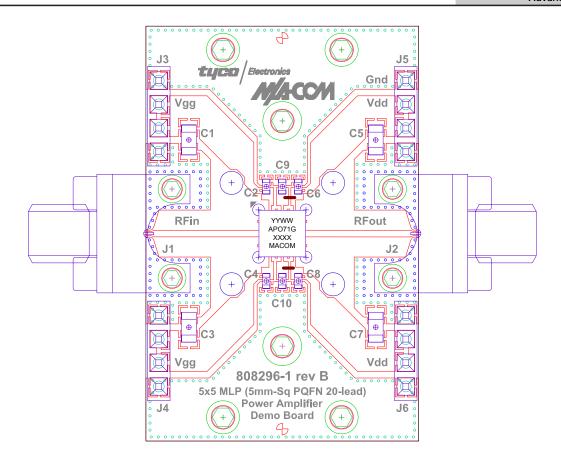


Figure 3. Demonstration Board PN MAAP-000071-SMB003 (available upon request).

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