

MAQ5 4 0

item can replace **BFQ540**



Approved by:
Checked by:
Issued by:

SPECIFICATION

PRODUCT: NPN 9.0GHz wideband transistor

MODEL: MAQ540 S OT89

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FEATURES

- High gain
- High output voltage
- Low noise
- Gold metallization ensures excellent reliability
- Low thermal resistance.

APPLICATIONS

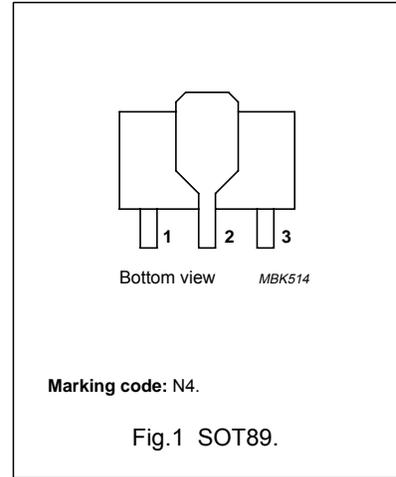
- VHF, UHF and CATV amplifiers.

DESCRIPTION

NPN wideband dual transistor in a plastic SOT89 package.

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–	20	V
V_{CES}	collector-emitter voltage	$R_{BE} = 0$	–	–	15	V
V_{EBO}	collector-base voltage	open collector	–	–	2	V
I_C	collector current (DC)		–	–	120	mA
P_{tot}	total power dissipation	$T_s \leq 60 \text{ }^\circ\text{C}$; note 1	–	–	1.2	W
h_{FE}	DC current gain	$I_C = 40 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	60	120	250	
f_T	transition frequency	$I_C = 40 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $f = 1 \text{ GHz}$; $T_{amb} = 25 \text{ }^\circ\text{C}$	–	9	–	GHz
$ S_{21} ^2$	insertion power gain	$I_C = 40 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $f = 900 \text{ MHz}$; $T_{amb} = 25 \text{ }^\circ\text{C}$	12	13	–	dB
F	noise figur	$I_C = 40 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $f = 900 \text{ MHz}$; $\Gamma_S = \Gamma_{opt}$	–	1.9	2.4	dB

Note

1. T_s is the temperature at the soldering point of the collector pin.

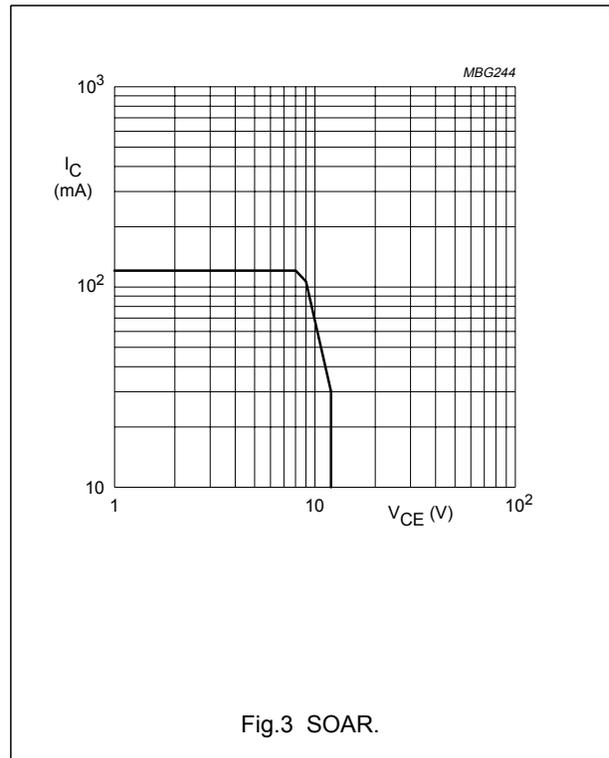
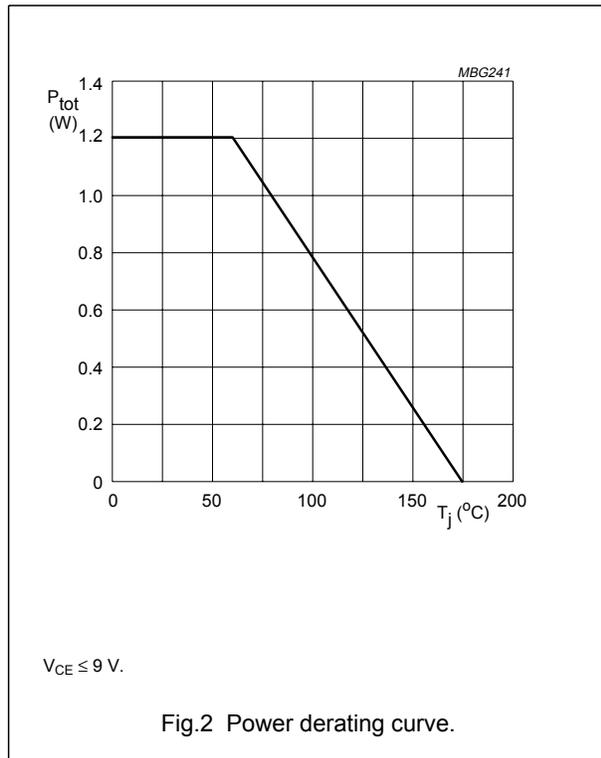
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	-	20	V
V_{CES}	collector-emitter voltage	$R_{BE} = 0$	-	15	V
V_{EBO}	emitter-base voltage	open collector	-	2	V
I_C	collector current (DC)		-	120	mA
P_{tot}	total power dissipation	$T_s \leq 60\text{ }^\circ\text{C}$	-	1.2	W
T_{stg}	storage temperature		-65	+150	$^\circ\text{C}$
T_j	operating junction temperature		-	175	$^\circ\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-s}$	thermal resistance from junction to soldering point	$T_s \leq 60\text{ }^\circ\text{C}; P_{tot} = 1.2\text{ W}$	95	K/W



CHARACTERISTICS

T_j = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage	open emitter; I _C = 10 μA; I _E = 0	20	–	–	V
V _{(BR)CES}	collector-emitter breakdown voltage	R _{BE} = 0; I _C = 40 μA	15	–	–	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0	2	–	–	V
I _{CBO}	collector-base leakage current	V _{CB} = 8 V; I _E = 0	–	–	50	nA
I _{EBO}	emitter-base leakage current	V _{CB} = 1 V; I _C = 0	–	–	200	nA
h _{FE}	DC current gain	I _C = 40 mA; V _{CE} = 8 V	60	120	250	
f _T	transition frequency	I _C = 40 mA; V _{CE} = 8 V; f _m = 1 GHz	–	9	–	GHz
C _e	emitter capacitance	I _C = i _e = 0; V _{EB} = 0.5 V; f = 1 MHz	–	2	–	pF
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 8 V; f = 1 MHz	–	0.9	–	pF
S ₂₁ ²	insertion power gain	I _C = 40 mA; V _{CE} = 8 V; f = 900 MHz; T _{amb} = 25 °C	12	13	–	dB
V _o	output voltage	note 1	–	500	–	mV
		note 2	–	350	–	mV
d ₂	second order intermodulation distortion	note 3	–	–	–53	dB
F	noise figur	I _C = 40 mA; V _{CE} = 8 V; f = 900 MHz; Γ _S = Γ _{opt}	–	1.9	2.4	dB

Notes

- d_{im} = –60 dB (DIN45004B); V_{CE} = 8 V; I_C = 40 mA; R_L = 50 Ω;
V_p = V_o; V_q = V_o –6 dB; V_r = V_o –6 dB;
f_p = 795.25 MHz; f_q = 803.25 MHz; f_r = 805.5 MHz;
measured at f_p + f_q – f_r = 793.25 MHz.
- d_{im} = –60 dB (DIN 45004B); I_C = 40 mA; V_{CE} = 8 V; R_L = 50 Ω;
V_p = V_q = V_o; f_p = 806 MHz; f_q = 810 MHz;
measured at 2f_p – f_q = 802 MHz.
- I_C = 40 mA; V_{CE} = 8 V; R_L = 50 Ω;
V_p = V_q = 225 mV; f_p = 250 MHz; f_q = 560 MHz;
measured at f_p + f_q = 810 MHz.

