

Applications

- IEEE802.11b DSSS WLAN
- IEEE802.11g OFDM WLAN
- Embedded, SiP modules

Features

- Dual Mode IEEE802.11b & IEEE802.11g
- Integrated PA, digital bias control, 50Ω input and output match, 3.2GHz TX Filter.
- Integrated harmonic filter.
- Integrated load insensitive Power Detector, with <1dB error at 2:1 mismatch
- 20 dBm Output Power, 802.11b, 11 Mbps, ACPR
 <-30 dBc
- 18dBm @ 3.0 % EVM, 802.11g, 54 Mbps
- 2.3 V to 5.0 V direct to battery supply
- Lead free, Halogen free, ROHS compliant, 2 x2x0.9 mm QFN package, MSL 1

Ordering Information

Part No.	Package	Remark
SE2574L	8 pin QFN	Samples
SE2574L-R	8 pin QFN	Tape and Reel
SE2574L-EK1	N/A	Evaluation kit

Product Description

The SE2574L is a complete 802.11 b/g WLAN discrete power amplifier. The device provides all the functionality of the power amplifier, power detector, filter, associated input, inter-stage and output matching in an ultra compact 2mm x 2mm x 0.9mm form factor.

The SE2574L is designed for ease of use, with all the critical input and output matching integrated. The SE2574L includes a transmitter power detector with 20 dB of dynamic range and a digital Enable for power on/off control. Harmonic filters and an input 3.2GHz LO rejection filter are integrated on-chip. The power ramp rise/fall time is 0.7 µs typical.

Functional Block Diagram

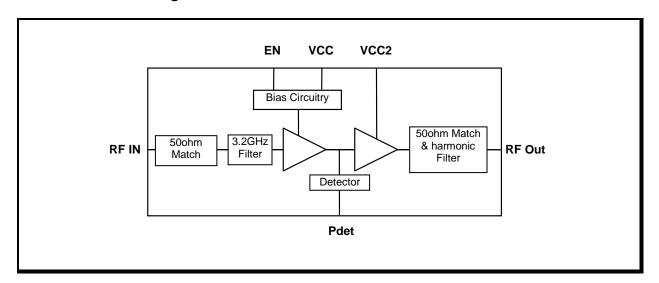


Figure 1: Functional Block Diagram



Pin Out Diagram

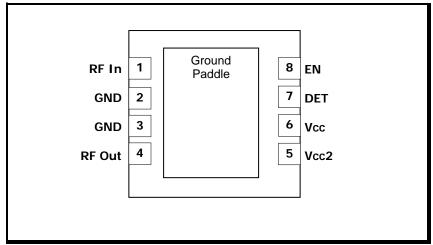


Figure 2: SE2574L Pin Out (Top View Through Package)

Pin Out Description

Pin No.	Name	Description
1	RF In	RF Input (No DC voltage on the pin, but DC short to ground)
2	GND	Ground
3	GND	Ground
4	RF Out	RF Output (No DC voltage on the pin, DC open to ground)
5	VCC2	Final Stage Supply Voltage (May attach directly to battery)
6	VCC	First Stage Supply Voltage (May attach directly to battery)
7	DET	Power Detector Output
8	EN	Power Amplifier Enable
Die paddle	GND	Ground



Absolute Maximum Ratings

These are stress ratings only. Exposure to stresses beyond these maximum ratings may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Definition	Min.	Max.	Unit
VCC	Supply Voltage on VCC	-0.3	5.5	V
EN	DC input on EN	-0.3	4.0	V
TX	RF Input Power. ANT terminated in 50Ω match	-	12.0	dBm
TA	Operating Temperature Range	-40	85	°C
Тѕтс	Storage Temperature Range	-40	150	°C
ESD _{HBM}	JEDEC JESD22-A114 all pins	-	500	V

Recommended Operating Conditions

Symbol	Parameter	Min.	Тур.	Max.	Unit
TA	Ambient temperature	-40	25	85	°C
	Supply voltage, nominal operation	2.7	3.3	5.0	
VCC	Supply voltage, output power reduced by 2dB typ	2.3	2.7	-	V

DC Electrical Characteristics

Conditions: VCC = 3.3V (default) or VCC = 5.0V (as noted), EN = 3.3V, $T_A = 25$ °C, as measured on SiGe Semiconductor's SE2574L-EK1 evaluation board, all unused ports terminated with 50 ohms, unless

otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
lcc-g	Total Supply Current	54 Mbps OFDM signal, 64QAM 18dBm, VCC = 3.3V 20.5dBm, VCC = 5.0V	-	135 150	-	mA
Ісс-н	Total Supply Current	802.11n, MCS7 17dBm, VCC = 3.3V 19dBm, VCC = 5.0V	-	115 130	-	mA
Ісс-в	Total Supply Current	11 Mbps CCK signal, BT = 0.45 20dBm, VCC = 3.3V 22dBm, VCC = 5.0V	-	160 175	-	mA
Icq	Total Supply Current	No RF	-	90 100	-	mA
Icc_off	Total Supply Current	EN = 0 V, No RF Applied	-	1	10	μA



Logic Characteristics

Conditions: VCC = 3.3V (default) or VCC = 5.0V (as noted), EN = 3.3V, $T_A = 25$ °C, as measured on SiGe Semiconductor's SE2574L-EK1 evaluation board, all unused ports terminated with 50 ohms, unless

otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
VENH	Logic High Voltage (Module On)	-	1.8	-	3.6	V
VENL	Logic Low Voltage (Module Off)	-	0	-	0.4	V
lenh	Input Current Logic High Voltage	-	-	2	10	μΑ
IENL	Input Current Logic Low Voltage	-	-	2	10	μА



AC Electrical Characteristics

802.11g/n Transmit Characteristics

Conditions: VCC = 3.3V (default) or VCC = 5.0V (as noted), EN = 3.3V, T_A = 25 °C, as measured on SiGe Semiconductor's SE2574L-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Со	ndition	Min.	Тур.	Max.	Unit
Fin	Frequency Range		-	2400	-	2500	MHz
		54Mbps, OFDM,	64 QAM, EVM = 3%	-	18	-	
	Outrat Davis a 0.01/	11Mbps, CCK, B	T = 0.45, Mask	-	20	-	
	Output Power, 3.3V	802.11n, HT20, a	all data rates, Mask	-	22	-	
Dovet		802.11n, HT40, a	all data rates, Mask	-	20	-	-ID
Pout		54Mbps, OFDM,	64 QAM, EVM = 3%	-	20.5	-	dBm
	504	11Mbps, CCK, B	T = 0.45, Mask	-	22	-	
	Output Power, 5.0V	802.11n, HT20, a	all data rates, Mask	-	24	-	
		802.11n, HT40, a	all data rates, Mask	-	22	-	
P _{1dB}	P1dB	-		-	25.0	-	dBm
S ₂₁	Small Signal Gain	-		25	28	29	dB
ΔS21	Small Signal Gain Variation	Gain variation over single 20MHz channel		-	0.5	-	dB
		Gain Variation over band		-	-	1.1	
S ₂₁ 3.2	Gain @ limit at Ref- vco spur frequency	3206 to 3312 MH	z	-	-	15	dB
2f		===:/	20dBm, 3.3V 22dBm, 5.0V	-	-50	-45	dBm/MHz
3f	Harmonics	1 Mbps, BPSK,	20dBm, 3.3V	_	-50	-45	dBm/MHz
- 31			22dBm, 5.0V	_	-48	-43	dDIII/IVII IZ
tdr, tdf	Delay & rise/fall Time	50 % of VEN edge and 90/10 % of final output power level		-	0.7	-	μs
S ₁₁	Input Return Loss	-		7	10	-	dB
STAB	Stability	CW, Pout = 20 dBm, VCC = 3.3V 0.1 GHz - 20 GHz Load VSWR = 10:1		All non-ha than -42 o	rmonically i dBm/MHz	related outp	outs less
RU	Ruggedness	P _{IN} = 12dBm, VC Load VSWR = 10		No perma	nent damaç	је	



Power Detector Characteristics

Conditions: VCC = 3.3V (default) or VCC = 5.0V (as noted), EN = 3.3V, $T_A = 25$ °C, as measured on SiGe

Semiconductor's SE2574L-EK1 evaluation board, all unused ports terminated with 50 ohms, unless

otherwise noted.

Symbol	Parameter	Condition	VCC = 3.3V		VCC = 5V			Unit	
			Min.	Тур.	Max.	Min.	Тур.	Max.	
Fouт	Frequency Range	-	2400	-	2500	2400	-	2500	MHz
PDR	Power detect range, CW	Measured at ANT	0	-	23	0	-	23	dBm
PDZsrc	DC source impedance on PD_OUT	-	-	1	-	-	1	-	kΩ
PDV _{NoRF}	Output Voltage, Pout = No RF	Measured into 1MΩ	-	0.12	-	-	0.12	-	V
PDV _{p18}	Output Voltage, Pout = 18 dBm CW	Measured into 1MΩ	-	0.60	-	-	0.55	-	٧
PDV _{p20}	Output Voltage, Pout = 20 dBm CW	Measured into 1MΩ	-	0.75	-	-	0.70	-	٧
PDV _{p23}	Output Voltage, Pout = 23 dBm CW	Measured into 1MΩ	1	NA	-	1	1.00	-	V
LPF-3dB	Power detect low pass filter -3dB corner frequency	Measured into 1MΩ	260	290	400	270	290	400	kHz

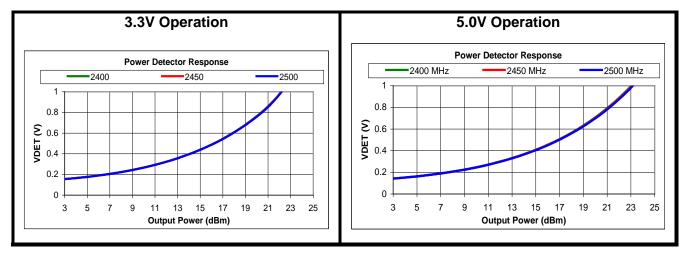


Figure 3: SE2574L Power Detector Characteristics



Package Diagram

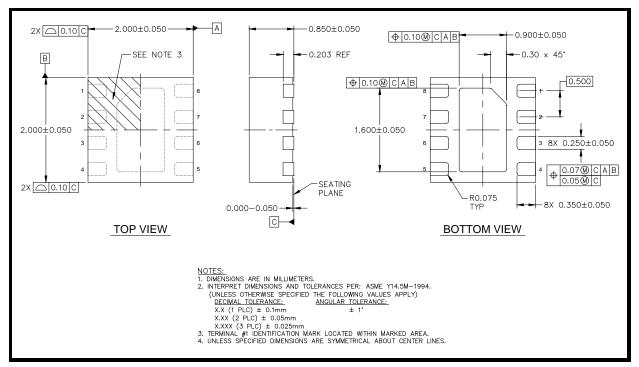


Figure 4: SE2574L Package Diagram

Recommended Land Pattern

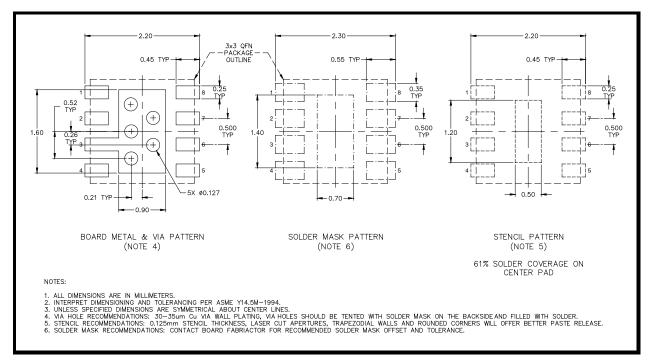


Figure 5: SE2574L Package Diagram



Branding Information

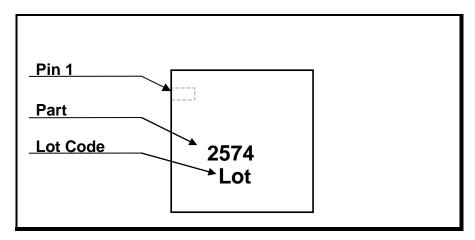


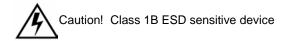
Figure 6: SE2574L Branding and Pin 1 Location (Top View)



Package Handling Information

Because of its sensitivity to moisture absorption, instructions on the shipping container label must be followed regarding exposure to moisture after the container seal is broken, otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly. The SE2574L is capable of withstanding a Pb free solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is manually attached, precaution should be taken to insure that the device is not subjected to temperatures above its rated peak temperature for an extended period of time. For details on both attachment techniques, precautions, and handling procedures recommended by SiGe, please refer to:

- SiGe's Application Note: "QFN solder reflow and rework information application note", Document Number QAD-00045
- SiGe's Application Note: "Handling, packing, shipping and use of moisture sensitive QFN application note", Document Number QAD-00044



Tape and Reel Information

Parameter	Value
Devices Per Reel	3000
Reel Diameter	7 inches
Tape Width	12 millimeters

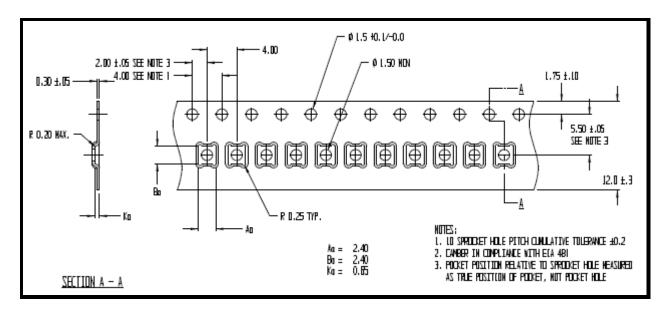


Figure 8: SE2574L-R Tape and Reel Information



Document Change History

Revision	Date	Notes
1.0	12/4/2009	Created
1.1	4/30/2010	Updated Package Marking Diagram
1.2	6/10/2010	Updated tape and reel information
1.3	6/22/2010	Updated gain specification MIN limit from 25dB to 24dB
1.4	11/01/2010	Updated ESD specification.
1.5	12/18/2010	Added 802.11n Mask Compliant Power Rating
1.6	1/31/2011	Added 802.11N to ICC table



PAGE LEFT INTENTIONALLY BLANK



http://www.sige.com

Email: sales@sige.com

Customer Service Locations:

North America: 1050 Morrison Drive, Suite 100 Ottawa ON K2H 8K7 Canada

Phone: +1 613 820 9244 Fax: +1 613 820 4933 Hong Kong

Phone: +852 3428 7222 Fax: +852 3579 5450

San Diego

Phone: +1 858 668 3541 (ext. 226)

Fax: +1 858 668 3546

United Kingdom

Phone: +44 1279 464217 Fax: +44 1279 464201

Product Preview

The datasheet contains information from the product concept specification. SiGe Semiconductor, Inc. reserves the right to change information at any time without notification.

Preliminary Information

The datasheet contains information from the design target specification. SiGe Semiconductor, Inc. reserves the right to change information at any time without notification.

Production testing may not include testing of all parameters.

Information furnished is believed to be accurate and reliable and is provided on an "as is" basis. SiGe Semiconductor, Inc. assumes no responsibility or liability for the direct or indirect consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license or indemnity is granted by implication or otherwise under any patent or other intellectual property rights of SiGe Semiconductor, Inc. or third parties. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SiGe Semiconductor, Inc. products are NOT authorized for use in implantation or life support applications or systems without express written approval from SiGe Semiconductor, Inc.

Copyright 2011 SiGe Semiconductor, Inc. All Rights Reserved

