PCM-9562

Intel[®] Atom[™] N450/D510 EBX SBC with 3 LAN/6 COM/3 SATA/8 USB2.0/2 Watchdog



Features

- Supports Intel[®] Atom[™] N450/D510 dual core processor
- Design complies with UL60601 on LAN3 and COM6 port isolation
- 3 Intel GbE Ethernet, 2 Watch Dog timer support
- Power off protection and Software I²C API support
- Supports embedded software APIs and Utilities

Software APIs:				*/	*		1/0
	Watchdog	I ² C	SMBus	H/W Monitor	Brightness	GPIO	Backlight On/Off
Utility:	BIOS flash	eSOS	Monitoring	Flash Lock	Embedded Security ID		

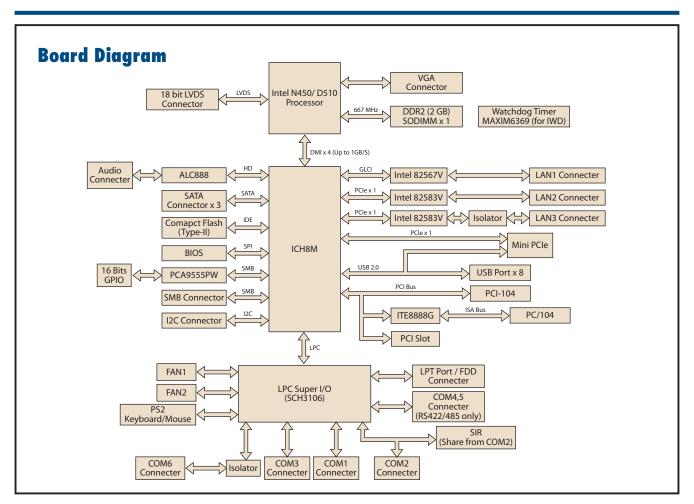
Windows 🔬 💽

Specifications

Max. Speed 1.67 GHz 1.67 GHz Processor System L2 Cache 512 KB 1 MB Chipset ICH8M ICH8M BIOS AMI 16 Mbit AMI 16 Mbit Memory Technology DDR2 667 DDR2 800 Max. Capacity 2 GB Socket 1 x 200-pin SODIMM		CPU	Intel Atom N450	Intel Atom D510					
Processor System L2 Carlie S12 KB 1.NB Diport CHMM CHMM CHMM BIOS AMI 16 Muit AMI 16 Muit AMI 16 Muit BIOS CHM DDR2 607 DDR2 600 Marco Capacity 2 68 Compacifiash Compacifiash S20 Compacifiash Card Type I, Type II Compacifiash Compacifiash S20 Compacifiash Card Type I, Type II Compacifiash Compacifiash R5.23/242/465 2 ObeLIN R5-422 By optional COMB is with isolation R5-23/242/465 Card Type I, Type II R5.24/242/465 2 Compacifiash F2 KM 1 Compacifiash R5.24/242/465 2 Compacifiash Compacifiash Compacifiash Compacifiash R5.24/242/465 CARD (AL CASB 2Code CLIN=in, LIN=-Out, Mic-In, Speaker Out (RAL) Compacifiash Compacifiash R010 164/04 Compacifiash Compacifiash Compacifiash Charnel South (AL CASB 2Code CLIN=in, LIN=-Out (RAL) Compacifiash Compacifiash R040 Charnel Sout									
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NV Interface RS-22/42/455 2 (Default RS-422/4265, RS-232 by optional request) VV Interface 1 VV Interface 3 v1SB 2.0 VV Interface 8 v1SB 2.0 VV Interface 8 v1SB 2.0 VV Interface 6 v10 v1 V v14 2.2 V Speaker for Speaker-out) 6 v10 v1 V v14 2.2 V Speaker for Speaker-out) 6 v10 v14		LPT	1 (FDD is Optional)						
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AD\ANTECH **Embedded Single Board Computers** Downloaded from product specifications are subject to phonoe without protocor

PCM-9562



Ordering Information

Model CPU	U C	RT	LVDS	Giga LAN1	Giga LAN2	Giga LAN3 UL60601	HD Audio	USB 2.0	SATAII	RS-232	RS-422/485	PC/104-Plus	Mini PCle	CF	Thermal
PCM-9562N-S6A1E Ator	m N450 1		1, 18-bit	1	1	Optional	Yes	8	3	3	2	Yes	1	1	Passive
PCM-9562D-S6A1E Ator	m D510 1		1, 18-bit	1	1	Optional	Yes	8	3	3	2	Yes	1	1	Active

* For PCM-9562 with 3 LAN and 6 COM sku, pls contact with field sales rep. Minimum Order quantity is required.

(PCM-9562 has 3 LAN and 6COM sku with LAN3 and COM6 designed in for UL60601.)

Optional Accessories

Part No.	Description
PCM-10586-9562E	Wiring kit for PCM-9562
1703100260	USB cable
CF-HDD-ADP	CompactFlash 50-pin to IDE 44-pin adapter
170304015K	AT Cable 4P x 2/4200-H-4P 15 cm

Packing List

Part No.	Description	Quantity
	PCM-9562 SBC	1
9689000002	Mini Jumper Pack	1
	Startup Manual	1
	Utility CD	1
1700015741	ATX 5VSB CABLE	1

Value-Added Software Services

Software API: An interface that defines the ways by which an application program may request services from libraries and/or operating systems. Provides not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speeds development, enhances security and offers add-on value for Advantech platforms. It plays the role of catalyst between developer and solution, and makes Advantech embedded platforms easier and simpler to adopt and operate with customer applications.

Software APIs

Control



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. It allows users to monitor the level of signal input or set the output status to switch on/off a device. Our API also provides Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.



SMBus is the System Management Bus defined by Intel® Corporation in 1995. It is used in personal computers and servers for low-speed system management communications. The SMBus API allows a developer to interface a embedded system environment and transfer serial messages using the SMBus protocols, allowing multiple simultaneous device control.



I²C is a bi-directional two wire bus that was developed by Philips for use in their televisions in the 1980s. The I²C API allows a developer to interface with an embedded system environment and transfer serial messages using the I²C protocols, allowing multiple simultaneous device control.

Display



Control

The Brightness Control API allows a developer to interface with an embedded device to easily control brightness.



The Backlight API allows a developer to control the backlight (screen) on/off in an embedded device.

Backlight

Software Utilities



The BIOS Flash utility allows customers to update the flash ROM BIOS version, or use it to back up current BIOS by copying it from the flash chip to a file on customers' disk. The BIOS Flash utility also provides a command line version and API for fast implementation into customized applications.



The embedded application is the most important property of a system integrator. It contains valuable intellectual property, design knowledge and innovation, but it is easily copied! The Embedded Security ID utility provides reliable security functions for customers to secure their application data within embedded BIOS.



The Monitoring utility allows the customer to monitor system health, including voltage, CPU and system temperature and fan speed. These items are important to a device; if critical errors happen and are not solved immediately, permanent damage may be caused.

Monitor



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own. A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.



The Hardware Control API allows developers to set the PWM (Pulse Width Modulation) value to adjust fan speed or other devices; it can also be used to adjust the LCD brightness.

Power Saving



Make use of Intel SpeedStep technology to reduce power consumption. The system will automatically adjust the CPU Speed depending on system loading.



Throttling

Refers to a series of methods for reducing power consumption in computers by lowering the clock frequency. APIs allow the user to lower the clock from 87.5% to 12.5%.



The eSOS is a small OS stored in BIOS ROM. It will boot up in case of a main OS crash. It will diagnose the hardware status, and then send an e-mail to a designated administrator. The eSOS also provides remote connection: Telnet server and FTP server, allowing the administrator to rescue the system.



Flash Lock is a mechanism that binds the board and CF card (SQFlash) together. The user can "Lock" SQFlash via the Flash Lock function and "Unlock" it via BIOS while booting. A locked SQFlash cannot be read by any card reader or boot from other platforms without a BIOS with the "Unlock" feature.