VOICE CONTROL PROCESSOR LE79232

PRODUCT PREVI

Features

- → Separate test-in and test-out busses allow simultaneous test-in and test-out access.
- The voltage sense is connected before the test-out relay, such that the impedance generation is preserved during test-in access or during self-test performed with the loop disconnected
- → The test-out relay can be used to disconnect the loop during test-in access, in-service calibration, self-test, or to measure sensing offsets
- The LCAS is inside the impedance feedback loop and doesn't degrade the longitudinal balance
- → In-service calibration can be performed using a central circuit connected to the test-in bus
- Self-test is performed using the calibration circuit or using a single test load resistor located on the test-in bus

Foreign Voltage Testing

- → Voltage range at the tip and ring port is ±200 V with typical secondary protection
- The LCAS break switches are used such that the SLIC leakage doesn't affect the results
- → Measurement offsets can be calibrated by using the testout relay to disconnect the loop, or by applying a low impedance to ground on the test-in bus

Foreign Current Testing

→ Current range is ±60 mA total common-mode current and ±80 mA differential and common mode current per individual lead

- → Voltage range at the tip and ring port is VBH to ground, minus the saturation voltage at peak common-mode
- → The current sensing offset can be calibrated out by operating the test-out relay

3-Element Resistance Testing

- → Current range is ±60 mA total common-mode current and ±80 mA differential and common mode current per individual lead
- Voltage range at the tip and ring port is VBH to ground, minus the saturation voltage at peak common-mode current
- ➤ The fixed part of the current sensing offset is cancelled in the test procedure by the two point measurement
- → The variable part of the current sensing offset can be calibrated out by operating the test-out relay

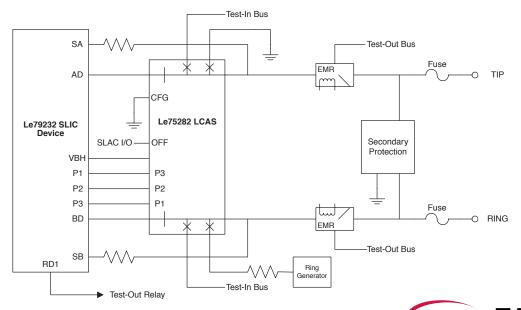
Fuse Test

→ The fuse test can be done by executing a 3-element capacitance test in high gain mode with and without the test-out relay activated

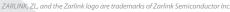
Loop Resistance Test

→ The voltage sensing offset and the feed current offset are cancelled out by doing a positive and negative polarity measurement at each current level

Le79232 SLIC and External Ringing with Le75282 LCAS, Test-In Bus and Test-Out Bus



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