

SIOV metal oxide varistors

Housed varistors, Fail-safe varistor, SFS14 series

 Series/Type:
 B72214F2**1K101

 Date:
 April 2011

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Fail-safe varistor, SFS14 series

Construction

- Plastic housing protected varistor
- Terminals: tinned copper wire
- Housing: heat-resistant and flame-retardant to UL 94 V-0

Features

- No flame or rupture under specified test conditions (see "Reliability data", "Overvoltage test")
- No harm to other components nearby on printed circuit board (PCB)

Approvals

- UL 🔳
- CSA
- IEC
- VDE

Applications

- Consumer electronics
- Power supply

Delivery mode

Bulk (standard)

General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to IEC 61051	-40 + 85	°C
Storage temperature		-40 + 85 -40 +125	°C
Electric strength	to IEC 61051	≥ 2.5	kV _{RMS}
Insulation resistance	to IEC 61051	≥ 100	MΩ
Response time		< 25	ns

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Electrical specifications and ordering codes

Maximum ratings (T_A = 85 °C)

Ordering code	Туре	V _{RMS}	V _{DC}	i _{max}	W _{max}	P _{max}
-	(untaped)			(8/20 µs)	(2 ms)	
	SIOV-	V	V	А	J	W
B72214F2171K101	SFS14K175E2	175	225	6000	70	0.6
B72214F2211K101	SFS14K210E2	210	270	6000	80	0.6
B72214F2231K101	SFS14K230E2	230	300	6000	90	0.6
B72214F2251K101	SFS14K250E2	250	320	6000	100	0.6
B72214F2271K101	SFS14K275E2	275	350	6000	110	0.6
B72214F2301K101	SFS14K300E2	300	385	6000	125	0.6
B72214F2321K101	SFS14K320E2	320	420	6000	136	0.6
B72214F2381K101	SFS14K385E2	385	505	5000	136	0.6

Characteristics (T_A = 25 $^{\circ}$ C)

Ordering code	Туре	V _v	ΔV_v	V _{c,max}	i _c	C _{typ}
-	(untaped)	(1 mA)	(1 mA)	(i _c)		(1 kHz)
	SIOV-	V	%	V	А	pF
B72214F2171K101	SFS14K175E2	270	±10	455	50	490
B72214F2211K101	SFS14K210E2	330	±10	545	50	410
B72214F2231K101	SFS14K230E2	360	±10	595	50	380
B72214F2251K101	SFS14K250E2	390	±10	650	50	350
B72214F2271K101	SFS14K275E2	430	±10	710	50	320
B72214F2301K101	SFS14K300E2	470	±10	775	50	300
B72214F2321K101	SFS14K320E2	510	±10	845	50	280
B72214F2381K101	SFS14K385E2	620	±10	1025	50	240

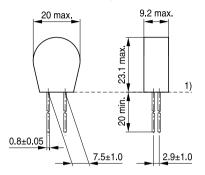




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Dimensional drawings



1) Seating plane in accordance with IEC 60717 VAR0675-N-E

Weight

Nominal diameter	V _{RMS}	Weight
mm	V	g
14	175 385	4.9 6.5



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 $\bigcap_{i=1}^{n}$

Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called V_v (1 mA _{DC} @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current ($8/20 \ \mu s$) applied.	To meet the specified value
Endurance at upper category temperature	1000 h at UCT After having continuously applied the maximum allowable AC voltage at UCT ± 2 °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V _v shall be measured.	ΙΔV/V (1 mA)Ι ≤10%
Surge current derating, 8/20 μs	10 surge currents (8/20 μ s), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 μ s.	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms	$ \Delta V/V (1 \text{ mA}) \le 10\%$ (measured in direction of surge current) No visible damage
Electric strength	IEC 61051-1, test 4.9.2 Metal balls method, 2500 V_{FMS} , 60 s The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown



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Test	Test methods/conditions	Requirement
Climatic sequence	The specimen shall be subjected to: a) dry heat at UCT, 16 h, IEC 60068-2-2, test Ba b) damp heat, 1st cycle: 55 °C, 93% r. H., 24 h, IEC 60068-2-30, test Db c) cold, LCT, 2 h, IEC 60068-2-1, test Aa d) damp heat, additional 5 cycles: 55 °C/25 °C, 93% r. H., 24 h/cycle, IEC 60068-2-30, test Db	IΔV/V (1 mA)I ≤10% R _{ins} ≥100 MΩ
	Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_V shall be measured. Thereafter, insulation resis- tance R_{ins} shall be measured at V = 500 V.	
Fast temperature cycling	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	l∆V/V (1 mA)l ≤5% No visible damage
Damp heat, steady state	IEC 60068-2-78, test Ca The specimen shall be subjected to $40 \pm 2 ^{\circ}$ C, 90 to 95% r. H. for 56 days without load / with 10% of the maximum continuous DC operating voltage V _{DC} . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V _V shall be measured. Thereafter, insulation resis- tance R _{ins} shall be measured at V = 500 V (insulated varistors only).	ΙΔV/V (1 mA)I ≤10% R _{ins} ≥100 MΩ



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Test	Test methods/conditions	Requirement
Test Solderability	Test methods/conditions IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	Requirement The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s: Each lead shall be dipped into a solder bath having a temperature of 260 \pm 5 °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for 10 \pm 1 s and then be stored at room temperature and normal humidity for 1 to 2 h. The change of V _v shall be measured and the specimen shall be visually examined.	l∆V/V (1 mA)l ≤5% No visible damage
Tensile strength	IEC 60068-2-21, test Ua1 After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage. Force for wire diameter: 0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N	I∆V/V (1 mA)I ≤5% No break of solder joint, no wire break



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Test	Test methods/conditions	Requirement
Vibration	IEC 60068-2-6, test Fc, method B4	l∆V/V (1 mA)l ≤5%
	Frequency range: 10 55 Hz Amplitude: 0.75 mm or 98 m/s ² Duration: 6 h ($3 \cdot 2$ h) Pulse: sine wave After repeatedly applying a single harmonic vibration according to the table above. The change of V _v shall be measured and the specimen shall be visually examined.	No visible damage
Bump	IEC 60068-2-29, test Eb Pulse duration: 6 ms Max. acceleration: 400 m/s ² Number of bumps: 4000 Pulse: half sine	I∆V/V (1 mA)I ≤5% No visible damage
Fire hazard	IEC 60695-11-5 (needle flame test) Severity: vertical 10 s	5 s max.
$ \begin{array}{ll} \hline \label{eq:constraint} Overvoltage test & The varistor should be subjected to \\ V_{\text{RMS}} test (V_{\text{RMS}} = 0.85 \ V_{\text{V}} \ (1 \ \text{mA})) \ \text{in series} \\ & \text{with a quick-acting 5 A fuse and a 5 } \Omega \\ & \text{resistor until the fuse fails. (based on S14 \\ & \text{series}). \end{array} $		

Note:

UCT = Upper category temperature

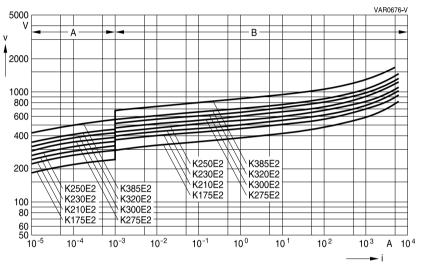
LCT = Lower category temperature

R_{ins} = Insulation resistance



v/i characteristics

v = f (i) for explanation of the characteristics refer to "General technical information", chapter 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances



SIOV-SFS14K175E2 ... K385E2





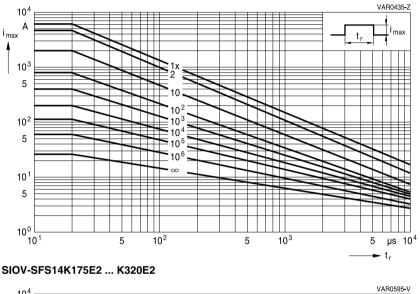
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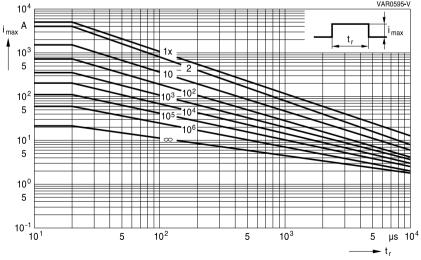
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Derating curves

Maximum surge current $i_{max} = f(t_r, pulse train)$

For explanation of the derating curves refer to "General technical information", section 1.8.1





SIOV-SFS14K385E2

Please read *Cautions and warnings* and *Important notes* at the end of this document.



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Cautions and warnings

General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

1. Store SIOVs only in original packaging. Do not open the package before storage.

2.	Storage conditions in original packaging:		
	Storage temperature:	−25 °C +45 °C,	
	Relative humidity:	<75% annual average,	
		<95% on maximum 30 days a year.	
	Dew precipitation:	is to be avoided.	

- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered within the time specified:

SIOV-S, -Q, -LS, -B, -SFS	24 months
ETFV	12 months.

Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.





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Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions.Contact with any liquids and solvents should be prevented.



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Symbols and terms

Symbol	Term
С	Capacitance
C _{typ}	Typical capacitance
i	Current
i _c	Current at which $V_{c, max}$ is measured
I _{leak}	Leakage current
i _{max}	Maximum surge current (also termed peak current)
I _{max}	Maximum discharge current to IEC 61643-1
I _{nom}	Nominal discharge current to IEC 61643-1
LCT	Lower category temperature
L _{typ}	Typical inductance
P _{max}	Maximum average power dissipation
R _{ins}	Insulation resistance
R _{min}	Minimum resistance
T _A	Ambient temperature
t _r	Duration of equivalent rectangular wave
UCT	Upper category temperature
v	Voltage
V _{clamp}	Clamping voltage
V _{c, max}	Maximum clamping voltage at specified current $i_{\rm c}$
V _{DC}	DC operating voltage
V_{jump}	Maximum jump start voltage
V _{max}	Maximum voltage
V _{op}	Operating voltage
V _{RMS}	AC operating voltage, root-mean-square value
$V_{\text{RMS, op, max}}$	Root-mean-square value of max. DC operating voltage incl. ripple current
V _{surge}	Super imposed surge voltage
Vv	Varistor voltage
ΔV_V	Tolerance of varistor voltage
W _{LD}	Maximum load dump
W _{max}	Maximum energy absorption
e	
	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.

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