

HT82V735

## 330mW Stereo Audio Power Amp With Shutdown

## Features

- Operating voltage: 2.4V~6.0V
- Very low standby current 0.5μA (Typ.)
- High signal-to-noise ratio
- High slew rate
- Output power 330mW at 10% THD+N into 8Ω
- · Excellent power supply ripple rejection

- Low power consumption
- Wide temperature operating range
- Direct drive speaker
- Shutdown function
- 8-pin SOP package

## Applications

- CD ROM DVD player
- Notebook/Desktop PC
- Portable Audio Device

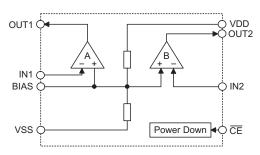
### • Headphone Amplifier

- Microphone Pre-amplifier
- Discman/MP3

## **General Description**

HT82V735 is a class AB stereo earphone driver designed for portable digital audio application. Pin assignments and application circuits are compatible with LM4880 which is suitable for effective low cost applications. The HT82V735 can deliver a maximum of 330mW

## **Block Diagram**



# **Pin Assignment**

provides 8-SOP package.

IN1 🗖 2						
BIAS 🗖 3	6 🗖 IN2					
VSS 🗆 4						
HT82V735 - 8 SOP-A						

Output power to an  $8\Omega$  load with less than 10%

(THD+N) from a 5V power supply. The very low standby

current in shutdown mode contributes to the reduction of

power consumption of battery-powered equipments. It

# **Pin Description**

Pin No.	Pin Name	I/O	Description
1	OUT1	0	Channel 1 output pin
2	IN1	I	Channel 1 Audio input
3	BIAS	I	Supports a voltage driver for internal bias
4	VSS	_	Negative power supply, ground
5	CE	Т	Power down mode when held high, I (power down) =1µA
6	IN2	I	Channel 2 Audio input
7	OUT2	0	Channel 2 output pin
8	VDD	—	Positive power supply



# **Absolute Maximum Ratings**

Supply Voltage	V <sub>SS</sub> –0.3V to V <sub>SS</sub> +6V	Storage Temperature	.–50°C to 125°C
Input Voltage	$V_{SS}$ –0.3V to $V_{DD}$ +0.3V	Operating Temperature	–20°C to 70°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

## **Electrical Characteristics**

 $V_{SS}$ =0V; f<sub>i</sub>=1kHz; R<sub>L</sub>=32 $\Omega$ ; Ta=25°C

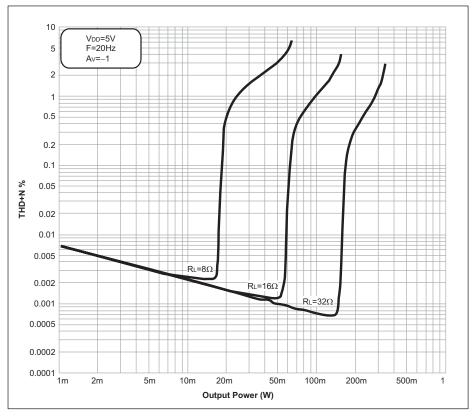
Symphol	Demonster	Test Conditions		NAT:	<b>T</b>	Mart		
Symbol	Parameter	V <sub>DD</sub>	Conditions	Min.	Тур.	Max.	Unit	
Supplies								
V <sub>DD</sub>	Supply Voltage	_	_	2.4	_	6	V	
I <sub>DD</sub>	Supply Current	5V	No load	_	10		mA	
I <sub>PD</sub>	Power-down Current	5V		_	1	_	μA	
D.C. Charac	cteristics							
V <sub>I(OS)</sub>	Input Offset Voltage	5V	_	_	10		mV	
I <sub>O</sub>	Maximum Output Current	5V	(THD+N)/S<0.1%	_	62		mA	
R <sub>0</sub>	Output Resistance	5V	_	_	230		mΩ	
			R <sub>L</sub> =8Ω	0.66		3	V	
Vo	Output Voltage Swing	5V	R <sub>L</sub> =16Ω	0.38	_	3.15	V	
			R <sub>L</sub> =32Ω	0.18	_	3.29	V	
PSRR	Power Supply Rejection Ratio	5V	C <sub>S</sub> =2.2μF, V <sub>RIPPLE</sub> =200V <sub>RMS</sub> , f=120Hz	_	66	_	dB	
X <sub>TALK</sub>	Channel Separation	5V	P <sub>O</sub> =200mW, R <sub>L</sub> =8Ω, C <sub>B</sub> =2.2μF		85	_	dB	
A.C. Charac	cteristics	_						
			P <sub>O</sub> =200mW, R <sub>L</sub> =8Ω, f=1kHz	_	0.03	_	_	
		s 5V	$P_O$ =120mW, R <sub>L</sub> =16 $\Omega$ , f=1kHz	_	0.01	_		
	Total Harmonic Distortion Plus Noise to Signal Ratio		P <sub>O</sub> =75mW, R <sub>L</sub> =32Ω, f=1kHz	_	0.01	_	%	
	3	3.3V	P <sub>O</sub> =30mW, R <sub>L</sub> =32Ω, f=1kHz	_	0.01	_		
P <sub>0</sub>	Output Power	5V	(THD+N)/S=0.1%, f=1kHz	_		_		
			$R_L=8\Omega$	_	240	_	- mW	
			$R_L=16\Omega$	_	160			
			R <sub>L</sub> =32Ω	_	90	_		
			(THD+N)/S=10%, f=1kHz	_		_		
			R <sub>L</sub> =8Ω	_	330	_	7	
			R <sub>L</sub> =16Ω		200	_		
			R <sub>L</sub> =32Ω	_	110			



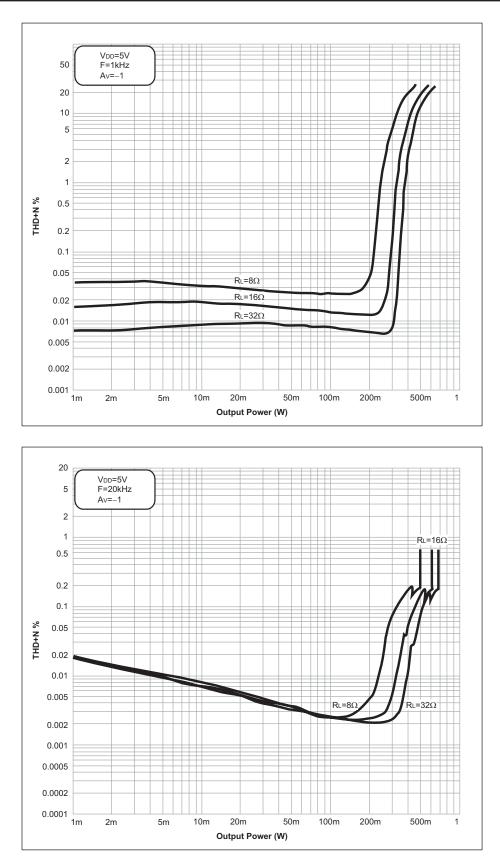
Symbol	Devementer	Test Conditions		Min.	Тур.	Max.	Unit
Symbol	Symbol Parameter		Conditions	wiin.			
S/N	Signal to Noise Ratio	5V	$V_{IN}$ =1 <sub>VRMS</sub> , R <sub>L</sub> =8 $\Omega$		92		dB
SR	Slew Rate	5V			3		V/µs
ATT	Power-down Attenuation	5V	1kHz, 0dB	_	70	_	dB

# **Typical Performance Characteristics**

# THD+N vs. Output Power



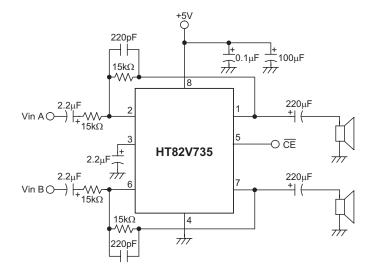




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# **Application Circuits**



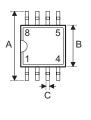
Rev. 1.10

February 18, 2004



# **Package Information**

8-pin SOP (150mil) Outline Dimensions





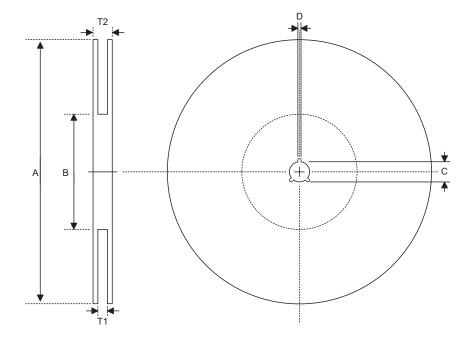


Cymhol				
Symbol	Min.	Nom.	Max.	
A	228		244	
В	149		157	
С	14		20	
C'	189		197	
D	53		69	
E	_	50	_	
F	4		10	
G	22	_	28	
н	4		12	
α	0°		10°	



# **Product Tape and Reel Specifications**

# **Reel Dimensions**



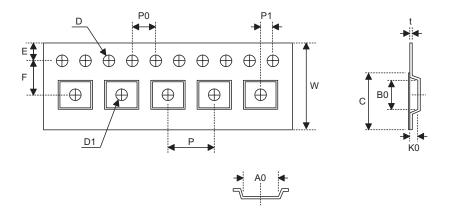
## SOP 8N

Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	330±1.0
В	Reel Inner Diameter	62±1.5
с	Spindle Hole Diameter	13.0+0.5 _0.2
D	Key Slit Width	2.0±0.5
T1	Space Between Flange	12.8+0.3 _0.2
T2	Reel Thickness	18.2±0.2

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## **Carrier Tape Dimensions**



## SOP 8N

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	12.0+0.3 0.1
Р	Cavity Pitch	8.0±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	5.5±0.1
D	Perforation Diameter	1.55±0.1
D1	Cavity Hole Diameter	1.5+0.25
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.1
A0	Cavity Length	6.4±0.1
B0	Cavity Width	5.20±0.1
K0	Cavity Depth	2.1±0.1
t	Carrier Tape Thickness	0.3±0.05
С	Cover Tape Width	9.3



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