

□ MN101C47 Series

Type	MN101C47C	MN101C47D	MN101CF46F
Internal ROM type	Mask ROM		FLASH
ROM (byte)	48K	64K	96K
RAM (byte)	1.5K	2K	3K
Package (Lead-free)	LQFP064-P-1414, SDIP042-P-0600C		LQFP064-P-1414 (ES (Engineering Sample) available), SDIP042-P-0600C (ES (Engineering Sample) available)
Minimum Instruction Execution Time	279 ns (3.0 V to 3.6 V, 14.32 MHz)		

■ Interrupts

- External (6 lines)
- Internal (8 lines) : Timer × 3, A/D, OSD, I²C, Remote control, Watchdog

■ Timer Counter

- 8-bit timer × 3
- Watchdog timer : system clock fs 1/2¹⁶, 1/2¹⁸, 1/2²⁰ triple selection

■ Serial interface

- I²C × 1 : for multimaster mode, bus line (output) has 2 systems

■ Caption / Teletext Decoder

- Built-in sync separator × 2

■ I/O Pins

- I/O 35 : Common use : 29

■ A/D converter

- 5-bit × 8-ch. (with S/H)

■ PWM

- 8-bit × 6-ch. , 14-bit × 1-ch.

■ Special Ports

- Remote control reception

■ CRTC

- 1-layer display (graphics, characters)

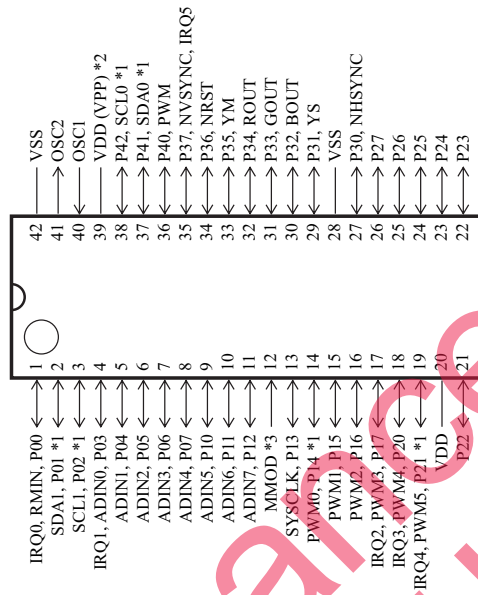
■ Notes

- Remote control input discriminant circuit built-in

■ Development tools

- In-circuit Emulator
PX-ICE101C/D + PX-PRB101C46-SDIP042-P-0600-M (Under development)

■ Pin Assignment
SDIP042-P-0600C



- Note) *1: 5 V dielectric N-ch. open drain output pin
 *2: Mask ROM = VDD, Flash memory built-in type = VPP
 *3: MMOD = H (fixed) (Set the test mode pin to the normal mode.)

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