

□ MN101C48 Series

Type	MN101C485	MN101C487	MN101CP487
Internal ROM type	Mask ROM		EPROM
ROM (byte)	8K	16K	
RAM (byte)	0.5K		
Package (Lead-free)	LQFP064-P-1414, TQFP064-P-1010B		
Minimum Instruction Execution Time	0.10 μs (at 4.5 V to 5.5 V, 20 MHz) 0.25 μs (at 2.7 V to 5.5 V, 8 MHz) 125 μs (at 2.0 V to 5.5 V, 32 kHz)* *: The lower limit for operation guarantee for EPROM built-in type is 2.3 V.		

■ Interrupts

RESET. Watchdog. External 0 to 2. External 4. Timer 2 to 5. Time base. Serial 0. A/D conversion finish

■ Timer Counter

8-bit timer × 2

Timer 2Square-wave/8-bit PWM output. Event count. Synchronous output event

Timer 3Square-wave output. Event count. Remote control carrier output. Serial 0 baud rate timer

Timer 2, 3 can be cascade-connected

16-bit timer × 1

Timer 4

Time base timer: One-minute count setting. Independently operable 8-bit timer 5

Watchdog timer × 1

■ Serial interface

Synchronous type/Simple UART (half-duplex) × 1: Serial 0

■ I/O Pins

I/O 36 : Common use. Specified pull-up resistor available. Input/output selectable (bit unit). Specified pull-down resistor partially selectable

Input 11 : Common use. Specified pull-up resistor available. Specified pull-down resistor partially selectable

■ A/D converter

10-bit × 8 channels (with S/H)

■ Display control function

LCD: 25 segments × 4 commons (Static, 1/2, 1/3, or 1/4 duty)

■ Special Ports

Buzzer output. Remote control carrier output. High-current drive port

■ Electrical Characteristics (Supply current)

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating supply current	IDD1	fosc = 8 MHz. VDD = 5 V		10	25	mA
	IDD2	fx = 32 kHz. VDD = 3 V		15	100	μA
Supply current at HALT	IDD3	fx = 32 kHz. VDD = 3 V. Ta = 25 °C		4	8	μA
	IDD4	fx = 32 kHz. VDD = 3 V. Ta = -40 °C to +85 °C			30	μA
Supply current at STOP	IDD5	VDD = 5 V. Ta = 25 °C			1	μA
		VDD = 5 V. Ta = -40 °C to +85 °C			25	μA

■ Development tools

In-circuit Emulator

PX-ICE101C/D + PX-PRB101C48-TQFP064-P-1010B

PX-ICE101C/D + PX-PRB101C48-LQFP064-P-1414

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