

□ MN101C57 Series

Type	MN101C57C	MN101C57D	MN101CF57D
Internal ROM type	Mask ROM		FLASH
ROM (byte)	48K	64K	
RAM (byte)	2K		
Package (Lead-free)	QFP100-P-1818B		
Minimum Instruction Execution Time	0.1 μs (at 4.5 V to 5.5 V, 20 MHz) 0.25 μs (at 2.7 V to 5.5 V, 8 MHz) 62.5 μs (at 2.0 V to 5.5 V, 32 kHz)* *: The lower limit for operation guarantee for flash memory built-in type is 2.5 V.		

■ Interrupts

RESET. Watchdog. External 0 to 3. External 4 (key interrupt selectable). External 5 (key interrupt dedicated). External 6. External 7. Remote control. Timer 0 to 3. Timer 6. Timer 7 (2 systems). Timer 8 (2 systems). Time base. Serial 0 (2 systems). Serial 2. A/D conversion finish

■ Timer Counter

8-bit timer × 5

- Timer 0Square-wave/8-bit PWM output. Event count. Remote control carrier output. Simple pulse width measurement. Square-wave/PWM output to large current terminal P50 possible
- Timer 1Square-wave output. Event count. Synchronous output event
- Timer 2Square-wave output. Added pulse (2-bit) type PWM output. Event count. Synchronous output event. Simple pulse width measurement. Square-wave/PWM output to large current terminal P52 possible
- Timer 3Square-wave output. Event count. Remote control carrier output. Serial 0 baud rate timer
- Timer 68-bit freerun timer
- Timer 0, 1 can be cascade-connected
- Timer 2, 3 can be cascade-connected

16-bit timer × 2

- Timer 7Square-wave output. Square-wave/16-bit PWM output (cycle/duty continuous variable). Event count. Synchronous output event. Pulse width measurement. Input capture. Square-wave/PWM output to large current terminal P51 possible
- Timer 8Square-wave/16-bit PWM output (duty continuous variable). Event count. Pulse width measurement. Input capture. Square-wave/PWM output to large current terminal P53 possible
- Timer 7, 8 can be cascade-connected: Square-wave output, PWM, input capture, pulse width measurement is possible as a 32-bit timer

Time base timer: One-minute count setting

Watchdog timer × 1

■ Serial interface

Synchronous type/UART (full-duplex) × 1: Serial 0

Synchronous type × 1: Serial 2

■ Remote Control Interface

Remote control output: Based on the timer 0 and timer 3 output, a remote control carrier with duty cycle of 1/2 or 1/3 can be output
 Remote control reception: Queued reception by low speed clock. Compatible with the Kaseikyo format (Setup can be adjusted to optional format)

■ I/O Pins

I/O 77 : Common use. Specified pull-up resistor available. Input/output selectable (bit unit)

Input 6 : Common use. Specified pull-up resistor available

■ A/D converter

10-bit × 16 channels (with S/H)

■ Display control function

LCD: 47 segments × 4 commons (Static, 1/2, 1/3, or 1/4 duty)

LCD power supply separated from VDD (usable if VLCD ≤ VDD)

LCD power shunt resistance contained

■ Special Ports

Buzzer output. Remote control carrier output. High-current drive port

■ Electrical Characteristics (Supply current)

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating supply current	IDD1	fosc = 20 MHz. VDD = 5 V		15	30	mA
	IDD2	fosc = 8 MHz. VDD = 5 V		8	16	mA
	IDD3	fx = 32 kHz. VDD = 3 V		30	60(70)	μA
Supply current at HALT	IDD4	fx = 32 kHz. VDD = 3 V. Ta = 25 °C		4	8	μA
	IDD5	fx = 32 kHz. VDD = 3 V. Ta = -40 °C to +85 °C			30	μA
Supply current at STOP	IDD6	VDD = 5 V. Ta = 25 °C			2	μA
	IDD7	VDD = 5 V. Ta = -40 °C to +85 °C			50	μA

Note) (): Flash memory built-in type

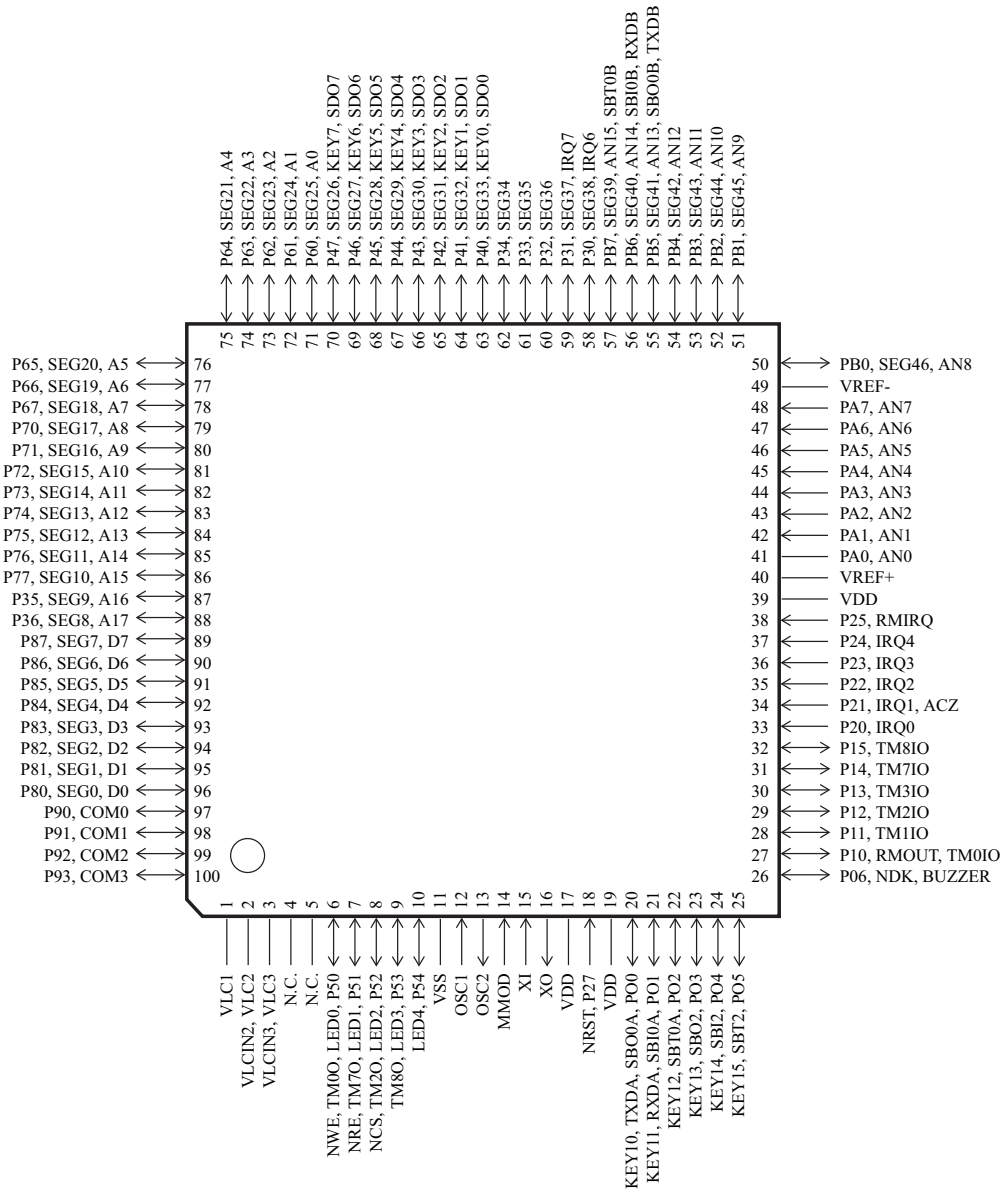
■ Development tools

In-circuit Emulator

PX-ICE101C/D + PX-PRB101C57-QFP100-P-1818B-M

■ Pin Assignment

QFP100-P-1818B



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