

# NPN SILICON EPITAXIAL TRANSISTOR

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### DESCRIPTION

The 2SD1286-Z is designed for Switching, especially in Hybrid Integrated Circuits.

#### **FEATURES**

• High hFE = 2000 to 30000

Complement to 2SB963-Z

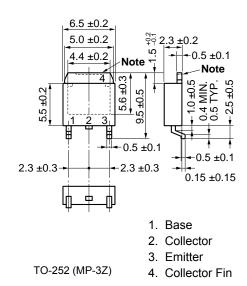
### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Collector to Base Voltage	Vсво	60	V
Collector to Emitter Voltage	VCEO	60	V
Base to Emitter Voltage	Vebo	8	V
Collector Current (DC)	IC(DC)	1	А
Collector Current (pulse) Note 1	IC(pulse)	2	А
Total Power Dissipation $(T_A = 25^{\circ}C)^{Note 2}$	Рт	2.0	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

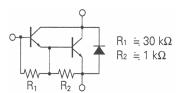
#### **Notes 1.** PW $\leq$ 10 ms, Duty Cycle $\leq$ 50%

**2.** When mounted on ceramic substrate of 7.5  $\text{cm}^2 \times 0.7$  mm

## PACKAGE DRAWING (Unit: mm)



**Note** The depth of notch at the top of the fin is from 0 to 0.2 mm.



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The mark <R> shows major revised points.

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## ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

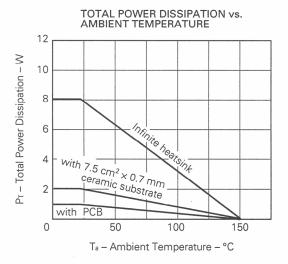
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Collector Cutoff Current	Ісво			10	μA	$V_{CB} = 60 V, I_E = 0$	
Emitter Cutoff Current	Іево			1.0	mA	VEB = 5.0 V, IC = 0	
DC Current Gain	hfe1*	1 000				Vce = 2.0 V, lc = 0.2 A	
DC Current Gain	hFE2*	2 000		30 000		Vce = 2.0 V, lc = 0.5 A	
Collector Saturation Voltage	VCE(sat)*			1.5	V	lc = 500 mA, lb = 0.5 mA	
Base Saturation Voltage	VBE(sat)*			2.0	V	lc = 500 mA, lв = 0.5 mA	
Turn-on Time	ton		0.5		μs	lc = 0.5 A, RL = 100 Ω	
Storage Time	tstg		1.0		μs	IB1 = -IB2 = 0.1 mA	
Fall Time	tr		1.0		μs	Vcc = 50 V	

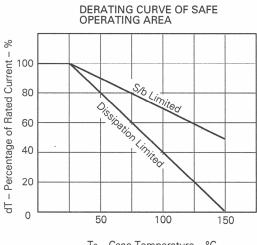
\* Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2 %

#### **hFE Classification**

MARKING	М	L	K
hFE2	2 000 to 5 000	4 000 to 10 000	8 000 to 30 000

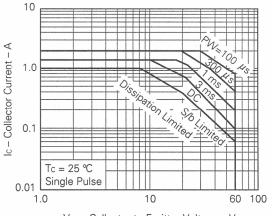
#### TYPICAL CHARACTERISTICS (T<sub>a</sub> = 25 °C)



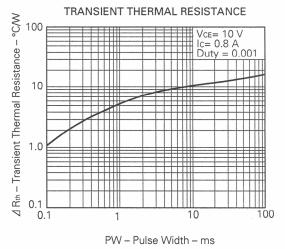


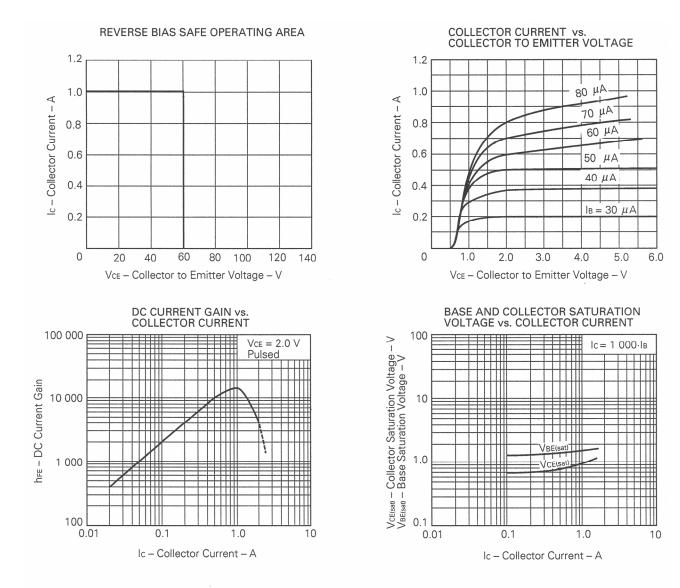
Tc – Case Temperature – °C

SAFE OPERATING AREA

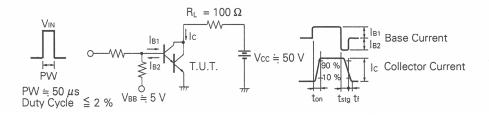


Vce – Collector to Emitter Voltage – V





SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT



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