Medium Power Transistor (50V,0.5A)

2SD1949/2SD1484K

Features

1) High current.(Ic=5A)

2) Low saturation voltage, typically VcE(sat)=0.1V at Ic / IB=150mA / 15mA.

●Absolute maximum rationgs (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _c	0.5	A
Collector power dissipation	Pc	0.2	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{CBO}	50	-	-	V	I _c =100μA
Collector-emitter breakdown voltage	BV _{CEO}	50	-	-	V	I _C =1mA
Emitter-base breakdown voltage	BVEBO	5	-	-	V	I _E =100μA
Collector outoff current	I _{CBO}	-	-	0.5	μΑ	V _{CB} =30V
Emitter cutoff current	EBO	-	-	0.5	μA	V _{EB} =4V
DC current rransfer ratio	h _{FE}	120	-	390	-	V _{CE} /I _C =3V/0.01A
Collector-emitter saturation voltage	V _{CE(sat)}	-	-	0.4	V	I _C /I _B =150mA/15mA
Transition frequency	f _T	-	250	-	MHz	V_{CE} =5V , I_{E} = -20mA , f=100MHz
Output capacitance	Cob	-	6.5	-	pF	V _{CB} =10V , I _E =0A , f=1MHz

Packaging specifications and h_{FE}

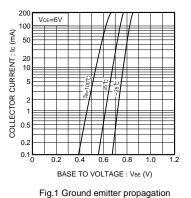
Туре	2SD1949	2SD1484K
Package	UMT3	SMT3
h _{FE}	QR	QR
Marking	Y*	Y*
Code	T106	T146
Basic ordering unit (pleces)	3000	3000

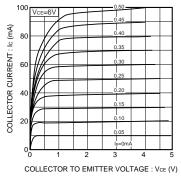
* Danotes hre

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Transistors

Electrical characteristic curves





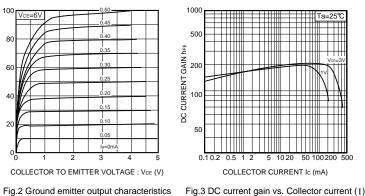
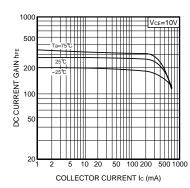


Fig.3 DC current gain vs. Collector current (I)



characteristics

Fig.4 DC current gain vs. Collector currnet (II)

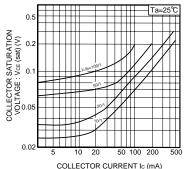


Fig.5 Collector-emitter saturation voltage

Ta=25℃

vs. Collector current

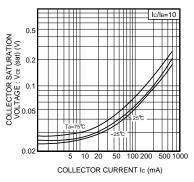
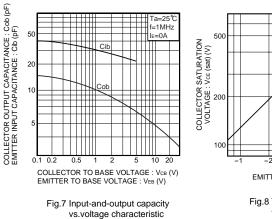


Fig.6 Collector-emitter saturation voltage vs. collector current



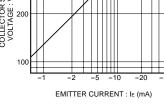


Fig.8 Transition frequency vs.emitter current

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2SD1949/2SD1484K

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