AUTOMOTIVE GRADE

PD - 96340

ISULATED GATE BIPOLAR TRANSISTOR

Features

- Standard: optimized for minimum saturation voltage and low operating frequencies (< 1kHz)
- Lead-Free, RoHS Compliant

International

Automotive Qualified *

Benefits

 Typical Applications: PTC Heater, Discharge Switch & Relay Replacements AUIRG4BC30S-S AUIRG4BC30S-SL Standard Speed IGBT $V_{CES} = 600V$ $V_{CE(on) typ.} = 1.4V$ $@V_{GE} = 15V, I_C = 18A$

D²Pak TO-2 AUIRG4BC30S-S AUIRG4BC

10-262
AUIRG4BC30S-SL

G	C	E
Gate	Collector	Emitter

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified

	Parameter	Max.	Units
V _{CES}	Collector-to-Emitter Breakdown Voltage	600	V
I _C @ T _C = 25°C	Continuous Collector Current	34	
$I_C @ T_C = 100^{\circ}C$	Continuous Collector Current	18	A
I _{CM}	Pulsed Collector Current ①	68	
I _{LM}	Clamped Inductive Load Current 2	68	
V _{GE}	Gate-to-Emitter Voltage	±20	V
E _{ARV}	Reverse Voltage Avalanche Energy ③	10	mJ
P _D @ T _C = 25°C	Maximum Power Dissipation	100	w
$P_D @ T_C = 100^{\circ}C$	Maximum Power Dissipation	42	
TJ	Operating Junction and	-55 to +150	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm) from case)	1

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{0JC}	Junction-to-Case		1.2	
R _{0CS}	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
R _{0JA}	Junction-to-Ambient, typical socket mount		40	1
Wt	Weight	1.44		g (oz)

* When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.

International **tor** Rectifier

Electrical Characteristics @ $T_J = 25^{\circ}C$ (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions	
V _{(BR)CES}	Collector-to-Emitter Breakdown Voltage	600	—	_	V	$V_{GE}=0V,\ I_C=250\mu A$	
V _{(BR)ECS}	Emitter-to-Collector Breakdown Voltage ④	18	-	—	V	$V_{GE} = 0V, I_{C} = 1.0A$	
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.75	—	V/°C	$V_{GE} = 0V, I_C = 1.0mA$	
		—	1.40	1.6		I _C = 18A	$V_{GE} = 15V$
V _{CE(ON)}	Collector-to-Emitter Saturation Voltage	—	1.84	—	v	I _C = 34A	See Fig. 2, 5
			1.45	_	ľ	$I_{C} = 18A$, $T_{J} = 150^{\circ}C$	
V _{GE(th)}	Gate Threshold Voltage	3.0	-	6.0		$V_{CE} = V_{GE}, I_C = 250 \mu A$	
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	—	-11	_	mV/°C	$V_{CE} = V_{GE}, I_C = 250 \mu A$	
g fe	Forward Transconductance (5)	6.0	11	—	S	$V_{CE} = 100V, I_C = 18A$	
I _{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{GE} = 0V, V_{CE} = 600V$	
ICES	Zero date voltage collector current		—	2.0	μ	$V_{GE}=0V,V_{CE}=10V,T_J$	= 25°C
		—	-	1000		$V_{GE} = 0V, V_{CE} = 600V, T$	J = 150°C
I _{GES}	Gate-to-EmitterLeakageCurrent	—	—	±100	nA	$V_{GE} = \pm 20V$	

Switching Characteristics @ $T_J = 25^{\circ}C$ (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
Qg	Total Gate Charge (turn-on)	—	50	75		I _C = 18A
Q _{ge}	Gate - Emitter Charge (turn-on)	_	7.3	11	nC	V _{CC} = 400V See Fig. 8
Q _{gc}	Gate - Collector Charge (turn-on)	—	17	26		V _{GE} = 15V
t _{d(on)}	Turn-On Delay Time	_	22	_		
tr	Rise Time	—	18	—	ns	$T_J = 25^{\circ}C$
t _{d(off)}	Turn-Off Delay Time	—	540	810	113	$I_{C} = 18A, V_{CC} = 480V$
t _f	Fall Time	—	390	590		$V_{GE} = 15V, R_G = 23\Omega$
Eon	Turn-On Switching Loss	—	0.26	_		Energy losses include "tail"
E _{off}	Turn-Off Switching Loss	—	3.45	_	mJ	See Fig. 9, 10, 14
E _{ts}	Total Switching Loss	—	3.71	5.6		
t _{d(on)}	Turn-On Delay Time	_	21	_		$T_{J} = 150^{\circ}C,$
tr	Rise Time	_	19	_	ns	$I_{C} = 18A, V_{CC} = 480V$
t _{d(off)}	Turn-Off Delay Time	_	790	_	115	$V_{GE} = 15V, R_G = 23\Omega$
t _f	Fall Time	_	760	_		Energy losses include "tail"
Ets	Total Switching Loss	_	6.55	_	mJ	See Fig. 11, 14
LE	Internal Emitter Inductance	—	7.5	—	nH	Measured 5mm from package
Cies	Input Capacitance	_	1100	_		$V_{GE} = 0V$
Coes	OutputCapacitance	_	72	—	pF	V _{CC} = 30V See Fig. 7
C _{res}	Reverse Transfer Capacitance	—	13	—		f = 1.0MHz

Notes:

- \odot Repetitive rating; V_{GE} = 20V, pulse width limited by max. junction temperature (See fig. 13b).
- O V_{CC} = 80%(V_{CES}), V_{GE} = 20V, L = 10 $\mu H,$ R_G = 23 $\Omega,$ (See fig. 13a).
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width \leq 80µs; duty factor \leq 0.1%.
- S Pulse width 5.0µs, single shot.

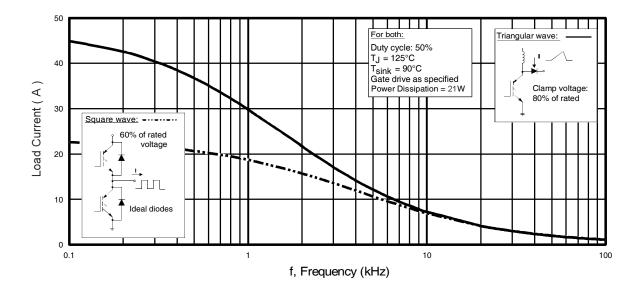
Qualification Information[†]

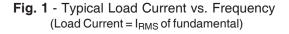
		Automotive				
		(per AEC-Q101) ^{††}				
Qualification L	evel	Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualificatior level is granted by extension of the higher Automotive level.				
		D ² PAK	MSL1 ^{†††}			
Moisture Sens	itivity Level		(per IPC/JEDEC J-STD-020)			
		TO-262	N/A			
	Machine Model	Class M4 (400V)				
		AEC-Q101-002				
500	Human Body Model	Class H1C (2000V)				
ESD			AEC-Q101-001			
	Charged Device Model	Class C5 (1000V)				
			AEC-Q101-005			
RoHS Compliant			Yes			

 \dagger Qualification standards can be found at International Rectifier's web site: <u>http://www.irf.com</u>

†† Exceptions to AEC-Q101 requirements are noted in the qualification report.







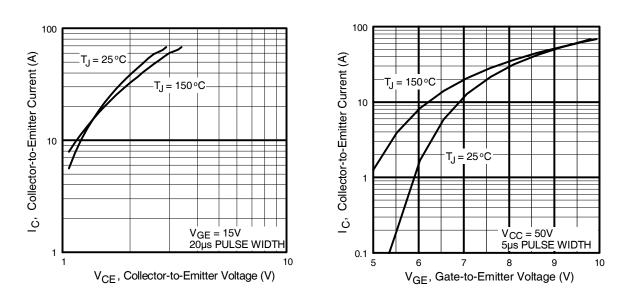


Fig. 2 - Typical Output Characteristics 4

Fig. 3 - Typical Transfer Characteristics www.irf.com

International

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36 A

I_C= 18A

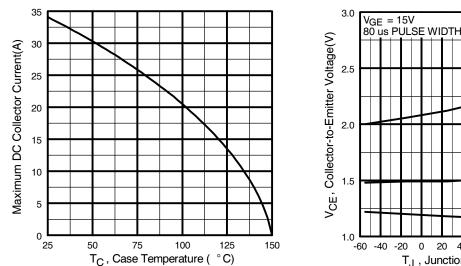


Fig. 4 - Maximum Collector Current vs. Case Temperature

 $J_{1.0}^{\bullet}$ = 0.0 A I_{C}^{\bullet} = 9.0 A $I_$

Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

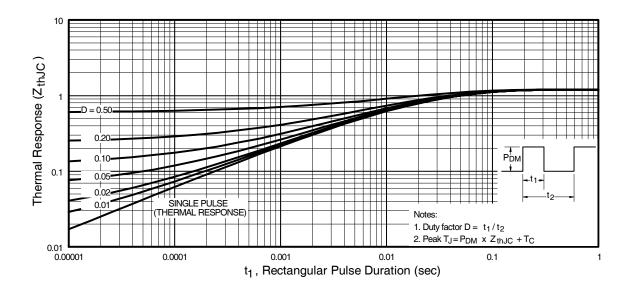
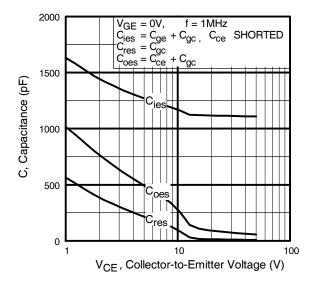
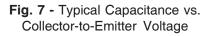


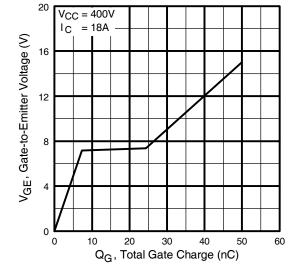
Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



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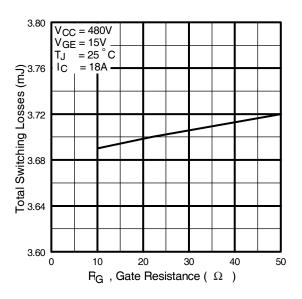


Fig. 9 - Typical Switching Losses vs. Gate Resistance

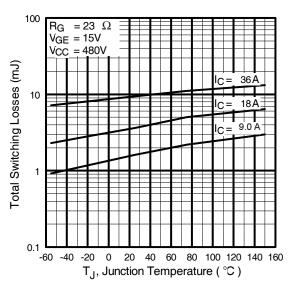
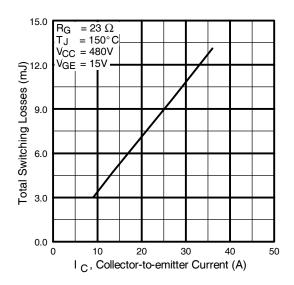
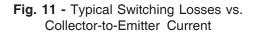


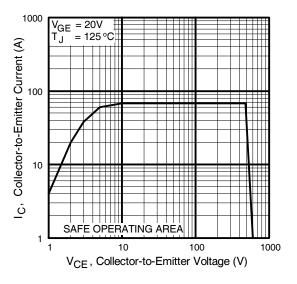
Fig. 10 - Typical Switching Losses vs. Junction Temperature

International **tor** Rectifier

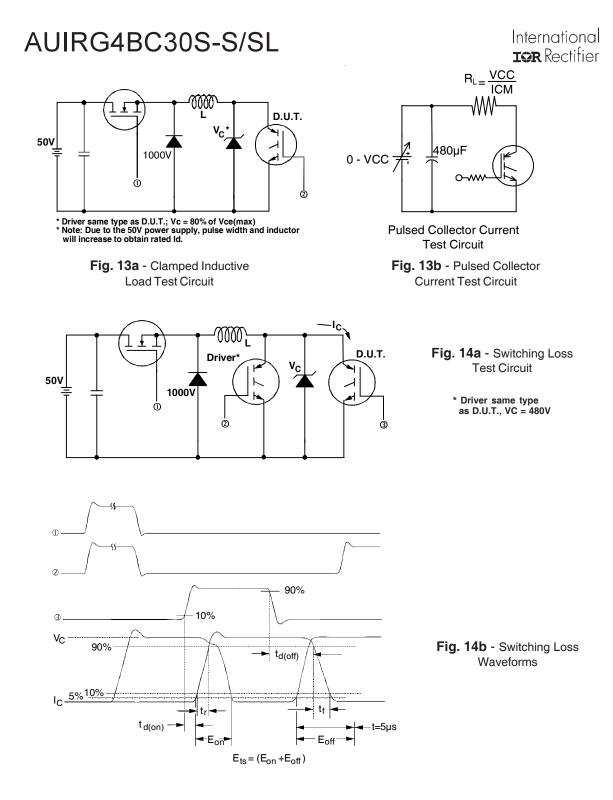




AUIRG4BC30S-S/SL





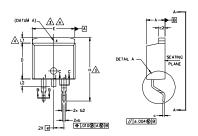


International **TOR** Rectifier

AUIRG4BC30S-S/SL

D²Pak (TO-263AB) Package Outline

Dimensions are shown in millimeters (inches)



H YER A-A



1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994

2 DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]

3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED 0.127 [.0.05"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.

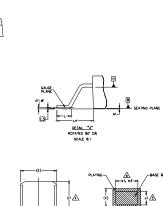
4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.

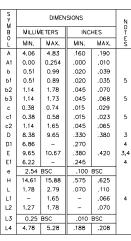
5. DIMENSION 61 AND c1 APPLY TO BASE METAL ONLY.

6. DATUM A & B TO BE DETERMINED AT DATUM PLANE H.

7, CONTROLLING DIMENSION: INCH.

8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-263AB





LEAD ASSIGNMENTS

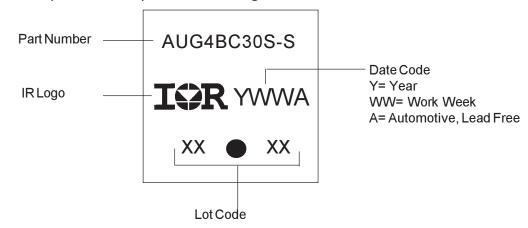
HE XFE T 1.- GATE 2. 4.- DRAIN 3.- SOURCE

IGBTs. CoPACK 1.- GATE 2. 4.- COLLECTOR 3.- EMITTER

DIODES 1.- ANODE * 4.- CATHODE 3.- ANODE

* PART DEPENDENT.

D²Pak (TO-263AB) Part Marking Information



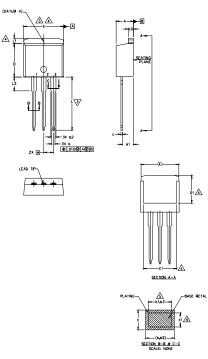
Note: For the most current drawing please refer to IR website at http://www.irf.com/package/ www.irf.com

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International **TOR** Rectifier

TO-262 Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994

2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

3 DIMENSION D & E DO NOT INCLUDE WOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.

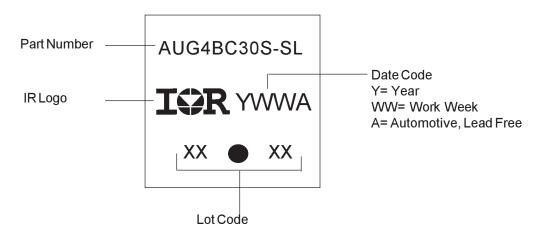
A. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1. 5. DIMENSION 61 AND c1 APPLY TO BASE METAL ONLY.

6. CONTROLLING DIMENSION; INCH.

7.- OUTLINE CONFORM TO JEDEC TO-262 EXCEPT A1(max.), b(min.) AND D1(min.) WHERE DIMENSIONS DERIVED THE ACTUAL PACKAGE OUTLINE.

S Y M		DIMEN	SIONS		N	
B	MILLIM	ETERS	INC	HES	NOTES	
0 L	MIN.	MAX.	MIN.	MAX.	L S	
A	4.06	4.83	.160	.190		
A1	2.03	3.02	.080	.119		
ь	0.51	0.99	.020	.039		
b1	0.51	0,89	.020	.035	5	LEAD ASSIGNMENTS
b2	1.14	1.78	.045	.070		LEAD ASSIGNMENTS
ь3	1,14	1.73	.045	.068	5	
c	0.38	0.74	.015	.029		HEXFET
c1	0.38	0.58	.015	.023	5	1 GATE
c2	1,14	1.65	.045	.065		2 DRAIN
D	8,38	9.65	.330	.380	3	3 SOURCE 4 DRAIN
D1	6,86	-	.270	-	4	4 DRAIN
Ε	9.65	10.67	.380	.420	3,4	
E1	6.22	-	.245		4	IGBTS, CoPACK
e	2.54	BSC	.100	BSC		1 GATE
L	13.46	14.10	.530	.555		1 GATE 2 COLLECTOR
L1	-	1.65	-	.065	4	3 EMITTER
L2	3.56	3.71	.140	.146		4 COLLECTOR

TO-262 Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

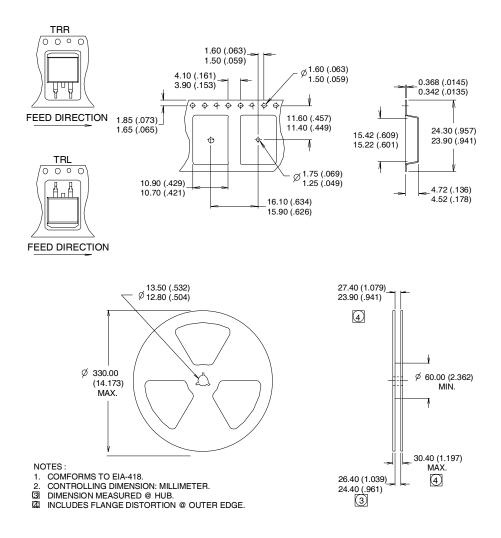
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International **IOR** Rectifier

AUIRG4BC30S-S/SL

D²Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)



International **tor** Rectifier

Ordering Information

Base part number	Package	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRG4BC30S-SL	TO-262	Tube	50	AUIRG4BC30S-SL
AUIRG4BC30S-S	D2Pak	Tube	50	AUIRG4BC30S-S
		Tape and Reel Left	800	AUIRG4BC30SSTRL
		Tape and Reel Right	800	AUIRG4BC30SSTRR



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