TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type

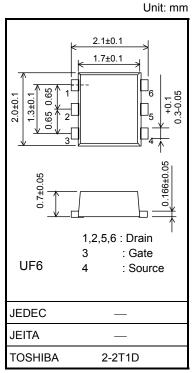
# SSM6K407TU

- DC-DC Converter, Relay Drive and Motor Drive Applications
- 4V drive
- Low ON-resistance : $R_{on} = 440 \text{m}\Omega \text{ (max) (@V_{GS} = 4 V)}$

 $:R_{on} = 300 \text{m}\Omega \text{ (max) (@V_{GS} = 10 V)}$ 

### Absolute Maximum Ratings (Ta = 25°C) (Note)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	60	V	
Gate–source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC	ΙD	2	А	
	Pulse	I <sub>DP</sub>	6		
Drain power dissipation		P <sub>D</sub> (Note1)	500	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature		T <sub>stg</sub>	<b>−55~150</b>	°C	



Weight: 7mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board  $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t, Cu Pad: } 645 \text{ mm}^2)$ 



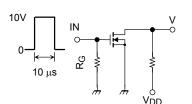
## Electrical Characteristics (Ta = 25°C)

Chara	ecteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cutoff current	t	IDSS	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source break	kdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60	_	_	V
Gate threshold volt	age	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V
Drain-source ON-resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 1 A (Note2)	_	0.33	0.44	Ω
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1 A (Note2)	_	0.22	0.30	
Forward transfer a	dmittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 A (Note2)	1.0	2.0	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V f = 1 MHz	_	150	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	25	-	
Output capacitance		Coss		_	70	_	
Switching time	Turn-on time	t <sub>on</sub>	V <sub>DD</sub> = 30 V, I <sub>D</sub> = 1 A	_	30	-	ns
	Turn-off time	t <sub>off</sub>	$V_{GS} = 0 \sim 10 \text{ V}, R_G = 50 \Omega$	_	150	_	
Total Gate Charge		Qg		_	6.0	_	nC
Gate-Source Charge		Q <sub>gs</sub>	V <sub>DD</sub> = 48 V, V <sub>GS</sub> = 10 V I <sub>D</sub> = 2 A	_	4.6	_	
Gate-Drain Charge		Q <sub>gd</sub>	1.5	_	1.4	_	
Drain-source forward voltage		V <sub>DSF</sub>	$I_D = -2 \text{ A}, V_{GS} = 0 \text{ V}$ (Note2)	_	-1.0	-1.5	٧

Note 2: Pulse test

### **Switching Time Test Circuit**

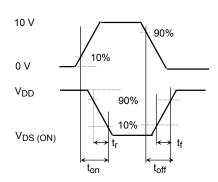
### (a) Test Circuit



### (b) V<sub>IN</sub>

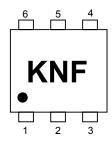


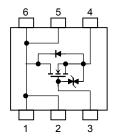
(c) V<sub>OUT</sub>



### Marking

### **Equivalent Circuit (top view)**





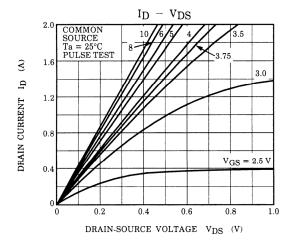
### **Notice on Usage**

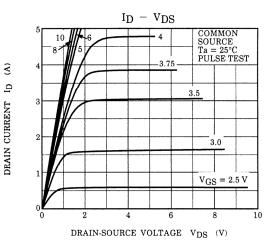
V<sub>th</sub> can be expressed as the voltage between gate and source when the low operating current value is I<sub>D</sub> = 1 mA for this product. For normal switching operation, VGS (on) requires a higher voltage than Vth and VGS (off) requires a lower voltage than V<sub>th.</sub> (The relationship can be established as follows: V<sub>GS</sub> (off) < V<sub>th</sub> < V<sub>GS</sub> (on).)

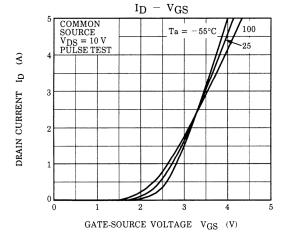
Take this into consideration when using the device.

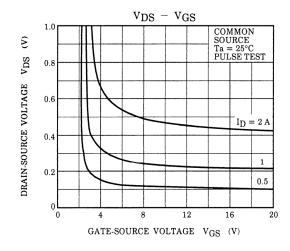
### **Handling Precaution**

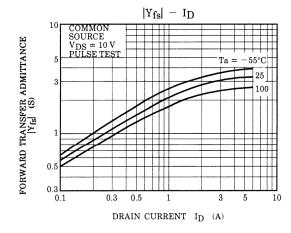
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

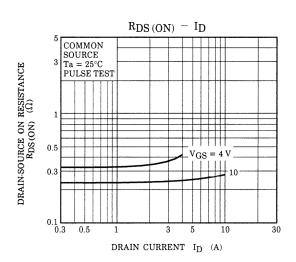




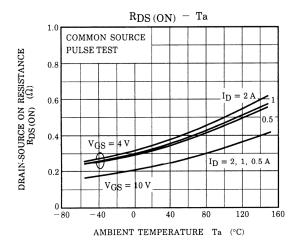


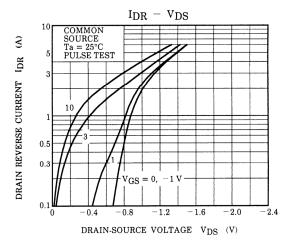


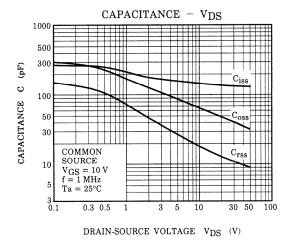


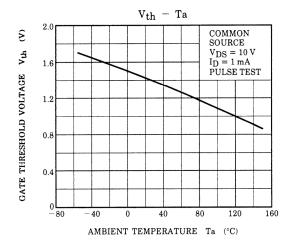


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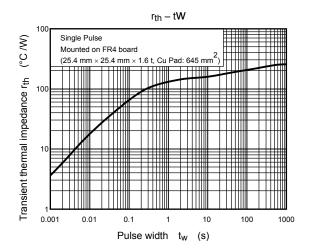


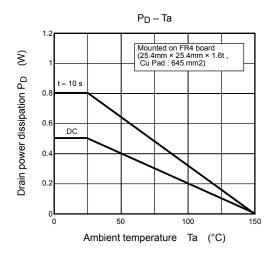






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### **RESTRICTIONS ON PRODUCT USE**

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