

New Product

Si7943DP

RoHS COMPLIANT

Vishay Siliconix

Dual P-Channel 30-V (D-S) MOSFET

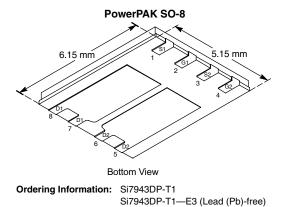
PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω) I _D (A)			
	0.025 at V _{GS} = - 10 V	- 9.4		
- 30	0.030 at $V_{GS} = -4.5 V$	- 8.6		
	0.045 at V_{GS} = – 2.5 V	- 7.0		

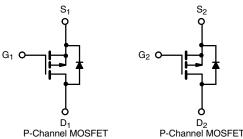
FEATURES

- TrenchFET[®] Power MOSFET
- New Low Thermal Resistance PowerPAK® Package with Low 1.07-mm Profile

APPLICATIONS

- 1–2 Cell Li-Ion Battery Switch
- Bus Load Switch for Notebook/Desktop Computers





G2 C	
	D ₂ P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	「 _A = 25 °C, unles	ss otherwise r	noted			
Parameter		Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	- 9.4	- 6.0		
Continuous Drain Current $(T_j = 150 \text{ C})$	T _A = 70 °C		- 7.5	- 4.8	А	
Pulsed Drain Current		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		ا _S	- 2.9	- 1.2		
Maximum Dawar Dissinctional	T _A = 25 °C	P _D	3.5	1.4	W	
Maximum Power Dissipation ^a	T _A = 70 °C		2.2	0.9		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	– 55 to 150		°C	
Soldering Recommendations (Peak Temperature) ^{b,c}			260		U	

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Mariana lugation to Ambient	$t \le 10 \text{ sec}$	R _{thJA}	26	35	°C/W
Maximum Junction-to-Ambient ^a	Steady State		60	85	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	2.2	2.7	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. See Solder Profile (*http://www.vishay.com/ppg?73257*). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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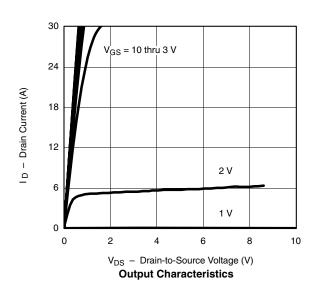


SPECIFICATIONS $T_J = 25$	°C, unless	s otherwise noted					
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Static		-		•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.60		- 1.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$	= ± 12 V		± 100	nA	
Zero Gate Voltage Drain Current	L	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
	IDSS	V_{DS} = -30 V, V_{GS} = 0 V, T_{J} = 55 °C			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq -5$ V, $V_{GS} = -10$ V	- 30			А	
		$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -9.4 \text{ A}$		0.020	0.025	Ω	
Drain-Source On-State Resistance ^a	r _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -8.6 \text{ A}$		0.024	0.030		
		$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -3.0 \text{ A}$		0.037	0.045	l	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -9.4 \text{ A}$		15		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S} = -2.9$ A, $V_{\rm GS} = 0$ V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			24	36		
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -9.4 \text{ A}$		8.5		nC	
Gate-Drain Charge	Q _{gd}	1		5.0			
Gate Resistance	Rg			2.9		Ω	
Turn-On Delay Time	t _{d(on)}			18	27		
Rise Time	t _r	V_{DD} = - 15 V, R _L = 15 Ω		40	60		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -1$ A, $V_{GEN} = -10$ V, $R_G = 6$ Ω		100	150	ns	
Fall Time	t _f			60	90		
Source-Drain Reverse Recovery Time t_{rr} $I_F = -2.9 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		I _F = - 2.9 A, di/dt = 100 A/μs		50	90		

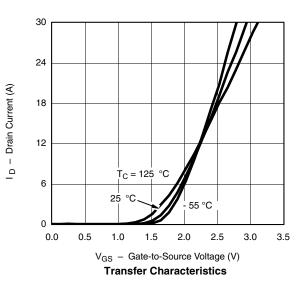
Notes a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

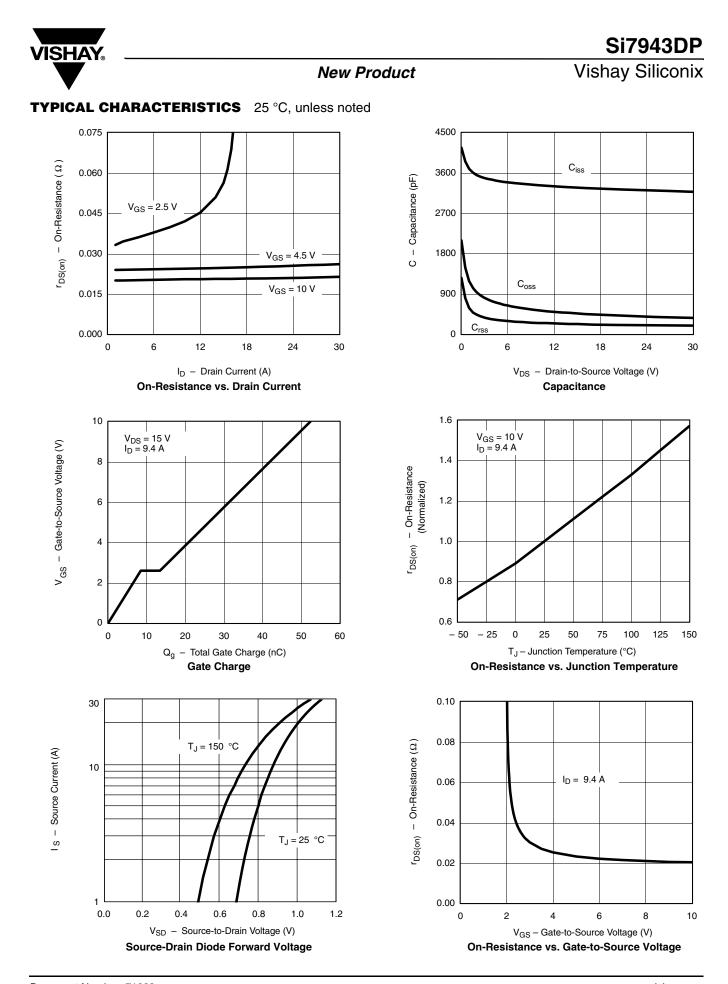
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless noted



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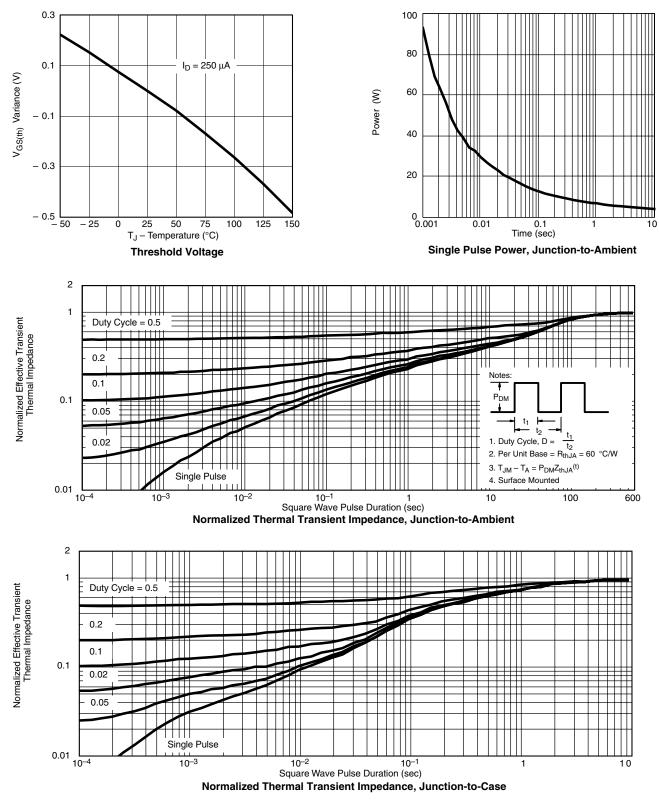


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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71629.



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