2N3903 is a Preferred Device

Symbol

VCEO

V_{CBO}

VEBO

 I_{C}

 P_D

 P_D

T_J, T_{sta}

Symbol

 $R_{\theta JA}$

 $R_{\theta JC}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the

Recommended Operating Conditions may affect device reliability.

1. Indicates Data in addition to JEDEC Requirements.

Value

40

60

6.0

200

625

5.0

1.5

12

-55 to +150

Max

200

83.3

Unit

Vdc

Vdc

Vdc

mAdc

mW

mW/°C

W

mW/°C

°C

Unit

°C/W

°C/W

General Purpose Transistors

NPN Silicon

MAXIMUM RATINGS

Collector-Emitter Voltage

Collector Current - Continuous

Collector-Base Voltage

Total Device Dissipation

Derate above 25°C

Total Device Dissipation

Derate above 25°C

Operating and Storage Junction

THERMAL CHARACTERISTICS (Note 1)

Characteristic

Thermal Resistance, Junction-to-Ambient

Thermal Resistance, Junction-to-Case

@ T_A = 25°C

@ $T_{C} = 25^{\circ}C$

Temperature Range

Emitter-Base Voltage

Features

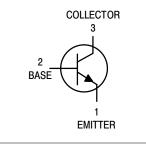
Pb–Free Packages are Available*

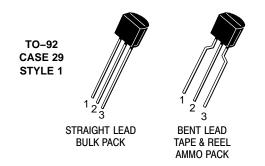
Rating



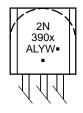
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http://onsemi.com





MARKING DIAGRAMS



= 3 or 4

- = Assembly Location А = Wafer Lot L
 - = Year

х

and best overall value.

- Υ W = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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1

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic			Symbol	Min	Max	Unit
OFF CHARACTER						
Collector – Emitter Breakdown Voltage (Note 2) ($I_C = 1.0 \text{ mAdc}, I_B = 0$)			V _{(BR)CEO}	40	_	Vdc
Collector – Base Breakdown Voltage ($I_C = 10 \ \mu Adc, I_E = 0$)			V _{(BR)CBO}	60	_	Vdc
Emitter – Base Breakdown Voltage (I _E = 10 μ Adc, I _C = 0)			V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current ($V_{CE} = 30$ Vdc, $V_{EB} = 3.0$ Vdc)			I _{BL}	-	50	nAdc
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)			ICEX	_	50	nAdc
ON CHARACTERI	STICS	L				
DC Current Gain (I ($I_C = 0.1 \text{ mAdc}, V_C$		2N3903	h _{FE}	20	_	-
$(I_C = 1.0 \text{ mAdc}, V_C)$	_E = 1.0 Vdc)	2N3904 2N3903 2N3904		40 35 70		
$(I_C = 10 \text{ mAdc}, V_{CI})$	_E = 1.0 Vdc)	2N3903 2N3904		50 100	150 300	
$(I_C = 50 \text{ mAdc}, V_{CI})$	_E = 1.0 Vdc)	2N3903 2N3904		30 60		
$(I_{C} = 100 \text{ mAdc}, V_{C})$	_{CE} = 1.0 Vdc)	2N3903 2N3904		15 30	-	
Collector – Emitter $(I_C = 10 \text{ mAdc}, I_B = (I_C = 50 \text{ mAdc}, I_B = 10 \text{ mAdc})$	Saturation Voltage (Note 2) = 1.0 mAdc) = 5.0 mAdc		V _{CE(sat)}		0.2 0.3	Vdc
Base – Emitter Saturation Voltage (Note 2) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)			V _{BE(sat)}	0.65 -	0.85 0.95	Vdc
SMALL-SIGNAL	CHARACTERISTICS			1	1	
Current-Gain – Bandwidth Product ($I_C = 10 \text{ mAdc}, V_{CE} = 20 \text{ Vdc}, f = 100 \text{ MHz}$)		2N3903 2N3904	f _T	250 300		MHz
Output Capacitance (V_{CB} = 5.0 Vdc, I_E = 0, f = 1.0 MHz)			C _{obo}	_	4.0	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}$, $I_C = 0$, $f = 1.0 \text{ MHz}$)			C _{ibo}	-	8.0	pF
Input Impedance ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, f = 1.0 kHz)		2N3903 2N3904	h _{ie}	1.0 1.0	8.0 10	kΩ
Voltage Feedback Ratio ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, f = 1.0 kHz)		2N3903 2N3904	h _{re}	0.1 0.5	5.0 8.0	X 10 ⁻
Small–Signal Current Gain ($I_C = 1.0$ mAdc, $V_{CE} = 10$ Vdc, f = 1.0 kHz)		2N3903 2N3904	h _{fe}	50 100	200 400	-
Output Admittance ($I_C = 1.0$ mAdc, $V_{CE} = 10$ Vdc, f = 1.0 kHz)			h _{oe}	1.0	40	μmhos
Noise Figure (I _C = 100 μ Adc, V _{CE} = 5.0 Vdc, R _S = 1.0 k Ω , f = 1.0 kHz)		2N3903 2N3904	NF		6.0 5.0	dB
SWITCHING CHAI	RACTERISTICS			·	•	•
Delay Time	(V _{CC} = 3.0 Vdc, V _{BE} = 0.5 Vdc,		t _d	-	35	ns
Rise Time	$I_{\rm C} = 10 \text{ mAdc}, I_{\rm B1} = 1.0 \text{ mAdc})$		t _r	-	35	ns
Storage Time	$(V_{CC} = 3.0 \text{ Vdc}, I_C = 10 \text{ mAdc}, I_{B1} = I_{B2} = 1.0 \text{ mAdc})$	2N3903 2N3904	t _s		175 200	ns

2. Pulse Test: Pulse Width \leq 300 µs; Duty Cycle \leq 2%.

50

_

t_f

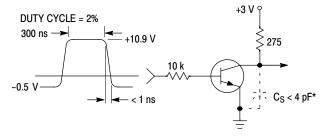
ns

Fall Time

ORDERING INFORMATION

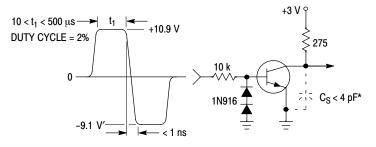
Device	Package	Shipping [†]	
2N3903RLRM	TO-92	2000 / Ammo Pack	
2N3904	TO-92	5000 Units / Bulk	
2N3904G	TO-92 (Pb-Free)	5000 Units / Bulk	
2N3904RLRA	TO-92	2000 / Tape & Reel	
2N3904RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel	
2N3904RLRM	TO-92	2000 / Ammo Pack	
2N3904RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack	
2N3904RLRP	TO-92	2000 / Ammo Pack	
2N3904RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack	
2N3904RL1G	TO–92 (Pb–Free)	2000 / Tape & Reel	
2N3904ZL1	TO-92	2000 / Ammo Pack	
2N3904ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



* Total shunt capacitance of test jig and connectors

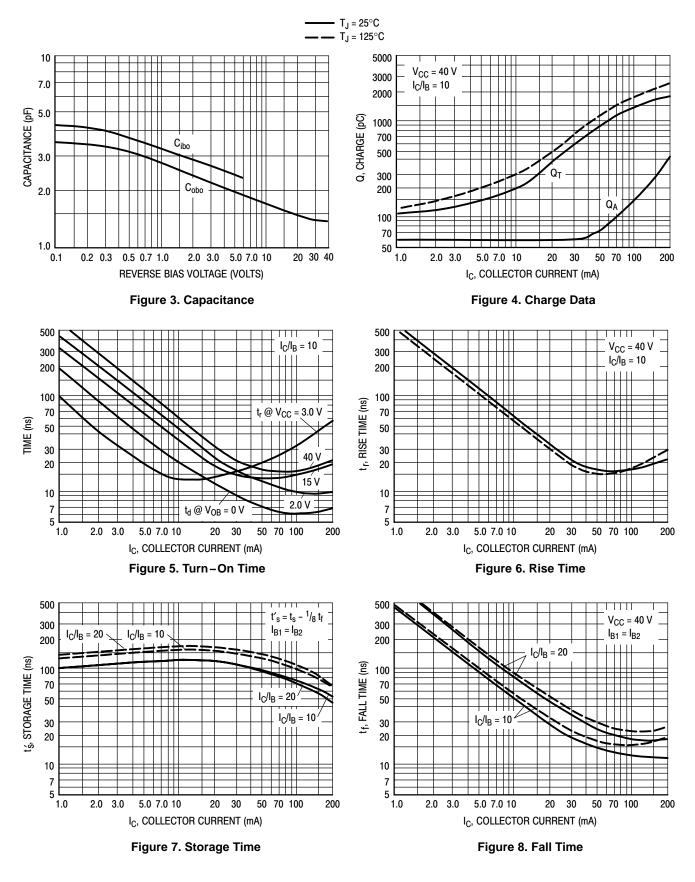
Figure 1. Delay and Rise Time Equivalent Test Circuit



* Total shunt capacitance of test jig and connectors

Figure 2. Storage and Fall Time Equivalent Test Circuit

TYPICAL TRANSIENT CHARACTERISTICS



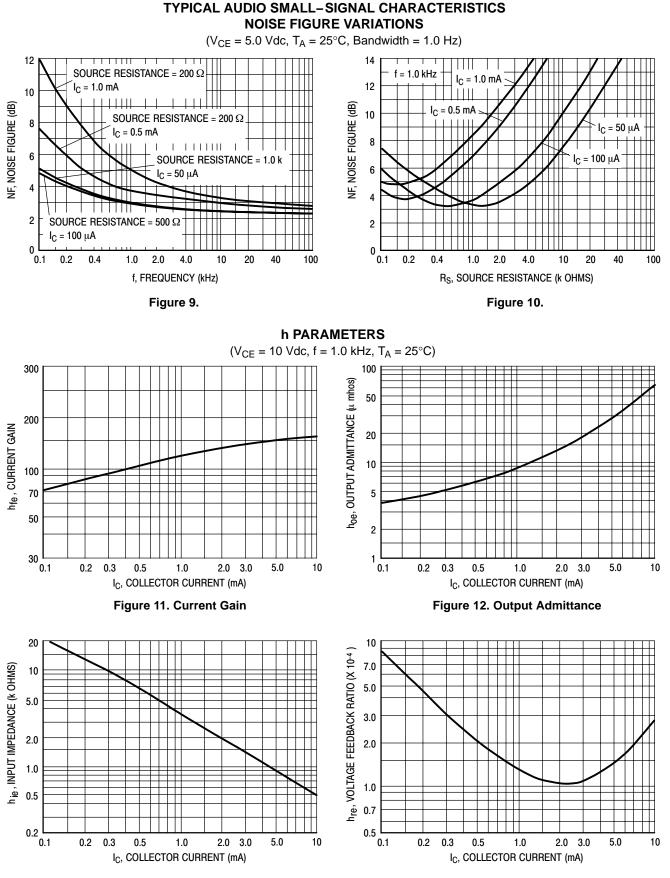
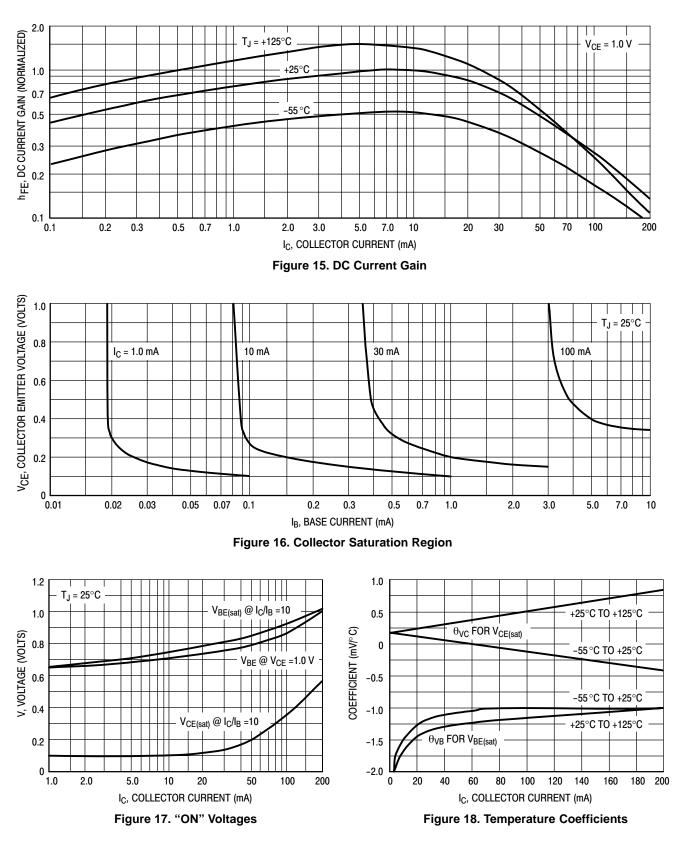


Figure 13. Input Impedance

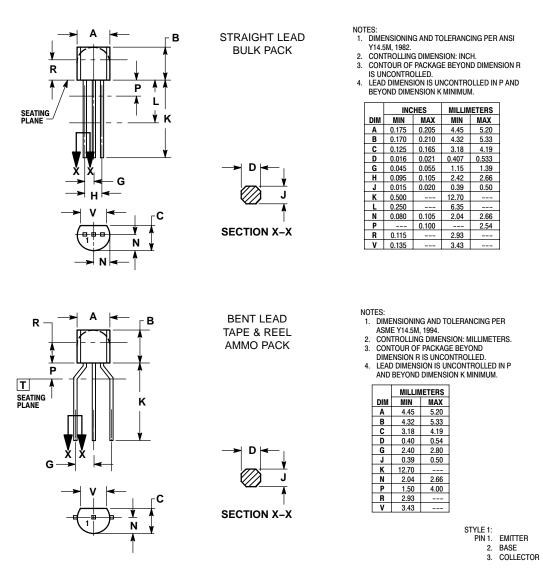
Figure 14. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS



PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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PUBLICATION ORDERING INFORMATION

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