

SILICON POWER TRANSISTOR 2SC3632-Z

NPN SILICON EPITAXIAL TRANSISTOR

DESCRIPTION

The 2SC3632-Z is designed for High Voltage Switching, especially in Hybrid Integrated Circuits.

FEATURES

- High Voltage VcEo = 600 V
- High Speed $t_f < 0.5 \mu s$
- · Complement to 2SA1413-Z

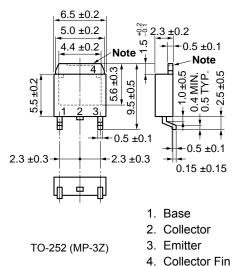
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Collector to Base Voltage	Vсво	600	V
Collector to Emitter Voltage	Vceo	600	V
Emitter to Base Voltage	VEBO	7	V
Collector Current (DC)	Ic(DC)	1	Α
Collector Current (pulse) Note 1	Ic(pulse)	2	Α
Total Power Dissipation (T _A = 25°C) Note 2	Рт	2.0	W
Junction Temperature	T_{j}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Notes 1. PW \leq 10 ms, Duty Cycle \leq 50%

2. When mounted on ceramic substrate of 7.5 cm 2 × 0.7 mm

<R> PACKAGE DRAWING (Unit: mm)



4. Collector Fin

Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

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ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

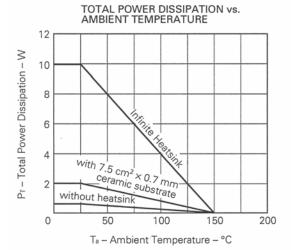
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	Ісво			10	μΑ	VCB = 600 V, IE = 0
Emitter Cutoff Current	ІЕВО			10	μΑ	VEB = 7.0 V, IC = 0
DC Current Gain	hFE1*	30	55	120		VcE = 5.0 V, Ic = 100 mA
DC Current Gain	hFE2*	5	7			VcE = 5.0 V, lc = 500 mA
Collector Saturation Voltage	VCE(sat)*		0.35	1.0	V	Ic = 400 mA, IB = 80 mA
Base Saturation Voltage	VBE(sat)*		0.9	1.2	V	Ic = 400 mA, I _B = 80 mA
Gain Bandwidth Product	fт		30		MHz	VcE = 5.0 V, IE = -50 mA
Output Capacitance	Соь		14		pF	VcB = 10 V, IE = 0, f = 1.0 MHz
Turn-on Time	ton		0.1	0.5	μs	Ic = 0.5 A, RL = 500 Ω
Storage Time	tstg		4.0	5.0	μs	B1 = −B2 = 0.1 A
Fall Time	tf		0.2	0.5	μs	Vcc = 250 V

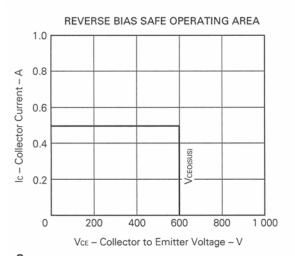
^{*} Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2 %

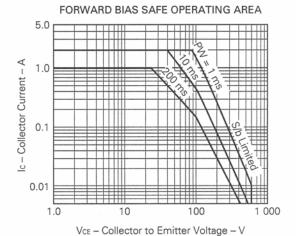
hfe Classification

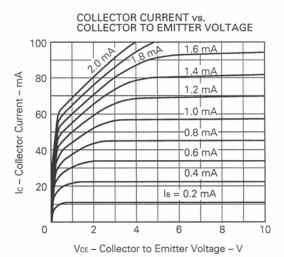
MARKING	М	L	K
hFE1	30 to 60	40 to 80	60 to 120

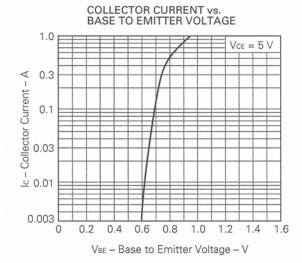
TYPICAL CHARACTERISTICS (T_a = 25 °C)

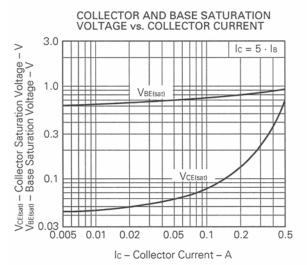


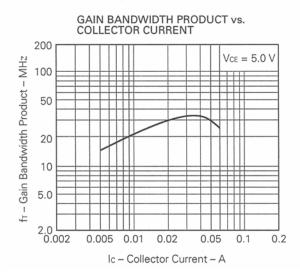


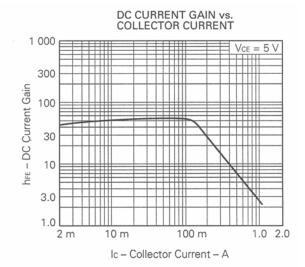


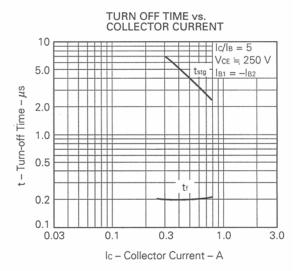


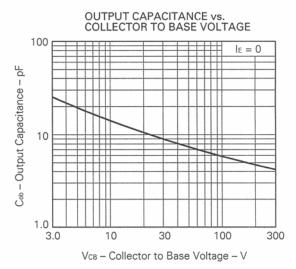


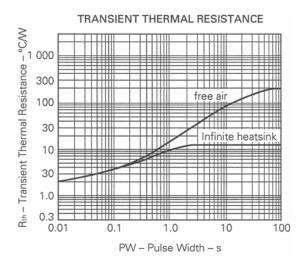












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