# Power Transistor (120V, 2A) 2SC4132 / 2SD1857

#### Features

- 1) High breakdown voltage. (BVCEO = 120V)
- 2) Low collector output capacitance.
- (Typ. 20pF at VCB = 10V)
- 3) High transition frequency. ( $f_T = 80MHz$ )
- 4) Complements the 2SB1236.

#### Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vcbo	120	V	
Collector-emitter voltage		VCEO	120	V	
Emitter-base voltage		VEBO	5	V	
Collector current		lc	2	A	
		ICP	3	A *1	
Collector power dissipation	2SC4132		0.5		
		Pc	2 *2	W	
	2SD1857		1 *3		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

\*1 Single pulse Pw = 10ms
\*2 When mounted on a 40 × 40 × 0.7mm ceramic board.
\*3 When mounted on 1.7mm thick PCB having collector foll dimensions 1cm<sup>2</sup> or more.

#### Packaging specifications and hre

Туре	2SC4132	2SD1857
Package	MPT3	ATV
hfe	PQR	QR
Marking	CB*	-
Code	T100	TV2
Basic ordering unit (pieces)	1000	2500

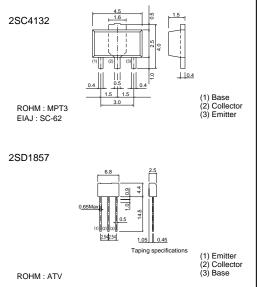
\* Denotes hre

#### •Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	120	-	-	V	Ic = 50μA	
Collector-emitter breakdown voltage	BVCEO	120	-	-	V	Ic = 1mA	
Emitter-base breakdown voltage	ВVево	5	-	-	V	Ιε = 50μΑ	
Collector cutoff current	Ісво	-	-	1	μA	Vcb = 100V	
Emitter cutoff current	Іево	-	-	1	μA	VEB = 4V	
Collector-emitter saturation voltage	VCE(sat)	-	-	2	V	Ic/IB = 1A/0.1A	*
DC current transfer ratio	hfe	82	-	390	-	Vce/Ic = 5V/0.1A	
Transition frequency	f⊤	-	80	-	MHz	Vce = 5V , Ie = -0.1A , f = 30MHz	
Output capacitance	Cob	-	20	-	pF	$V_{CB} = 10V$ , $I_E = 0A$ , $f = 1MHz$	*

\* Measured using pulse current.

•Dimensions (Unit : mm)



Ta=25°C

10

Vce= 100

### Transistors

#### Electrical characteristics curves

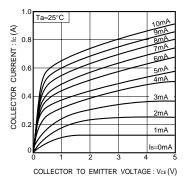
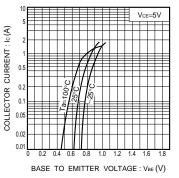
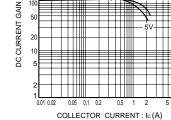


Fig.1 Ground emitter output characteristics





100

50

100

hFE 20

Fig.2 Ground emitter propagation characteristics

Fig.3 DC current gain vs. collector current (I)

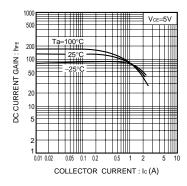
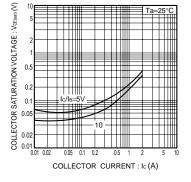
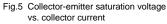


Fig.4 DC current gain vs. collector current (II)





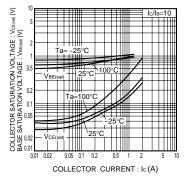


Fig.6 Collector-emitter saturation Base-emitter saturation vs. collector current

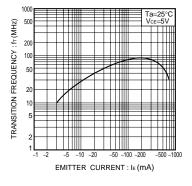
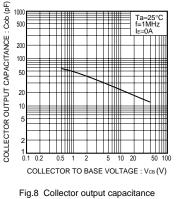
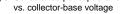
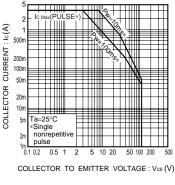


Fig.7 Gain bandwidth product vs. emitter current





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#### Fig.9 Safe operating area (2SC4132)

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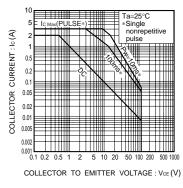


Fig.10 Safe operating area (2SD1857)



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