General Purpose Transistor

NPN Silicon

- Moisture Sensitivity Level: 1
- ESD Rating: Human Body Model: >4000 V Machine Model: >400 V
- This is a Pb–Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	65	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	۱ _C	100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR- 5 Board (Note 1) T _A = 25°C	P _D	265	mW
Derate above 25°C		2.1	mW/°C
Thermal Resistance, Junction to Ambient (Note 1)	$R_{\theta JA}$	470	°C/W
Total Device Dissipation Alumina Substrate (Note 2) T _A = 25°C	P _D	640	mW
Derate above 25°C		5.1	mW/°C
Thermal Resistance, Junction to Ambient (Note 2)	R_{\thetaJA}	195	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	– 55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

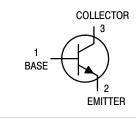
1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.



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MARKING DIAGRAM



ORDERING INFORMATION

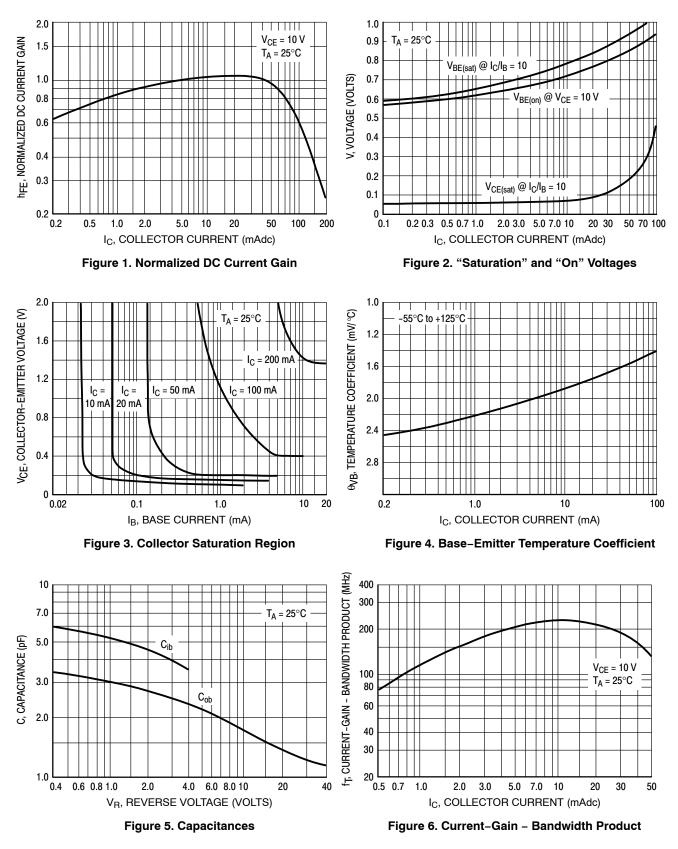
Device	Package	Shipping [†]
BC846BM3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

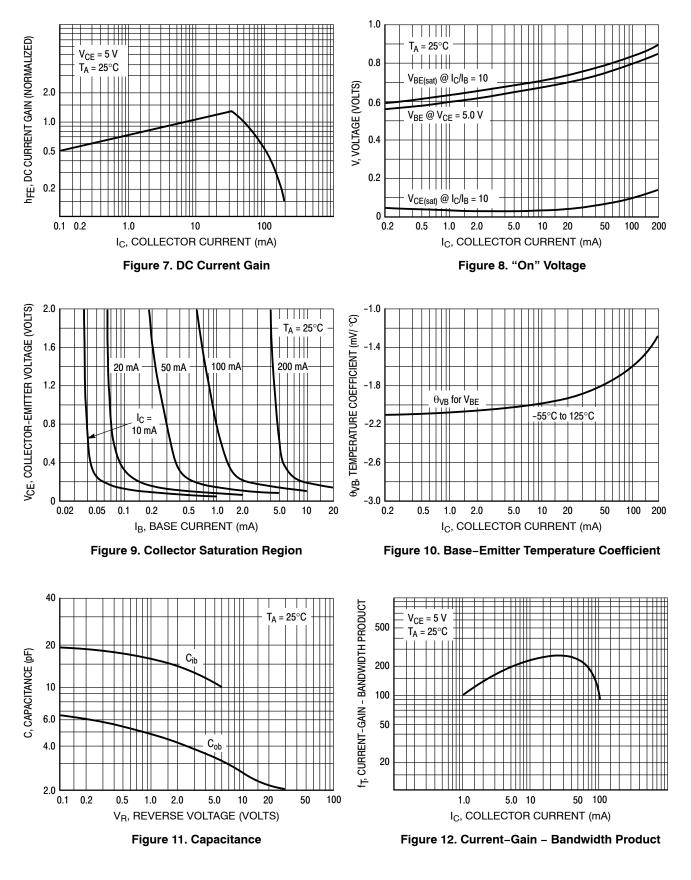
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage $(I_{C} = 10 \text{ mA})$	V _{(BR)CEO}	65	_	_	V
Collector – Emitter Breakdown Voltage ($I_C = 10 \ \mu A, \ V_{EB} = 0$)	V _{(BR)CES}	80	_	_	V
Collector – Base Breakdown Voltage ($I_C = 10 \ \mu A$)	V _{(BR)CBO}	80	_	_	V
Emitter – Base Breakdown Voltage $(I_E = 1.0 \ \mu A)$	V _{(BR)EBO}	6.0	_	_	V
$ \begin{array}{ll} \mbox{Collector Cutoff Current} & (V_{CB}=30 \mbox{ V}) \\ & (V_{CB}=30 \mbox{ V}, \mbox{ T}_{A}=150^{\circ}\mbox{C}) \end{array} $	I _{CBO}	-		15 5.0	nA μA
ON CHARACTERISTICS					
DC Current Gain (I _C = 10 μ A, V _{CE} = 5.0 V) (I _C = 2.0 mA, V _{CE} = 5.0 V)	h _{FE}	200	150 290	_ 450	-
Collector – Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)	V _{CE(sat)}		_ _	0.25 0.6	V
Base – Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)	V _{BE(sat)}		0.7 0.9	_ _	V
$ \begin{array}{l} \text{Base-Emitter Voltage (I_C = 1.0 mA, V_{CE} = 5.0 \text{ V})} \\ (I_C = 2.0 mA, V_{CE} = 5.0 \text{ V}) \\ (I_C = 10 mA, V_{CE} = 5.0 \text{ V}) \end{array} $	V _{BE(on)}	550 580 -	645 660 -	700 700 770	mV
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product ($I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	fT	100	-	_	MHz
Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz)	C _{obo}	-	_	4.5	pF
Noise Figure (I _C = 0.2 mA, V _{CE} = 5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	-	_	10	dB

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



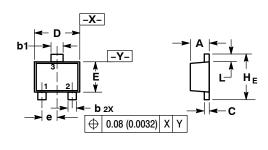
PACKAGE DIMENSIONS

SOT-723 CASE 631AA-01 **ISSUE C**

NOTES

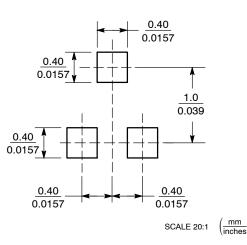
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982
- CONTROLLING DIMENSION: MILLIMETERS. 2
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM З.
- THICKNESS OF BASE MATERIAL. DIMENSIONS D AND E DO NOT INCLUDE MOLD 4 FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.45	0.50	0.55	0.018	0.020	0.022	
b	0.15	0.21	0.27	0.0059	0.0083	0.0106	
b1	0.25	0.31	0.37	0.010	0.012	0.015	
С	0.07	0.12	0.17	0.0028	0.0047	0.0067	
D	1.15	1.20	1.25	0.045	0.047	0.049	
Е	0.75	0.80	0.85	0.03	0.032	0.034	
е	0.40 BSC			0.016 BSC			
ΗE	1.15	1.20	1.25	0.045	0.047	0.049	
L	0.15	0.20	0.25	0.0059	0.0079	0.0098	



SOLDERING FOOTPRINT*

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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