

SANYO Semiconductors DATA SHEET

2SA2200— PNP Epitaxial Planar Silicon Transistor High-Current Switching Applications

Applications

• DC / DC converters, relay drivers, lamp drivers, motor drivers.

Features

- · Adoption of FBET, MBIT processes.
- · Large current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- · High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-60	V
Collector-to-Emitter Voltage	VCES		-60	V
Collector-to-Emitter Voltage	VCEO		-60	V
Emitter-to-Base Voltage	VEBO		-7	V
Collector Current	IC		-3	Α
Collector Current (Pulse)	ICP		-5	Α
Base Current	ΙΒ		-600	mA
Collector Dissipation	D-	Mounted on a ceramic board (250mm ² ×0.8mm)	1.3	W
	PC	Tc=25°C	3.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Onne
Collector Cutoff Current	ІСВО	V _{CB} =-50V, I _E =0A			-1	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =-4V, I _C =0A			-1	μΑ
DC Current Gain	hFE	V _{CE} =-2V, I _C =-100mA	200		400	
Gain-Bandwidth Product	fT	VCE=-10V, IC=-500mA		400		MHz
Output Capacitance	Cob	V _{CB} =-10V, f=1MHz		25		pF

Marking: RA Continued on next page.

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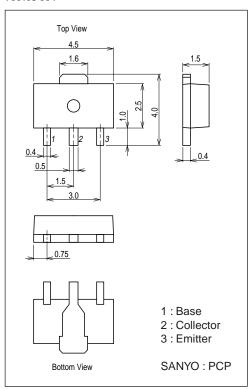
2SA2200

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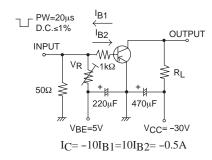
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)1	I _C =-1A, I _B =-50mA		-110	-220	mV
	VCE(sat)2	IC=-1A, IB=-100mA		-90	-180	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =-1A, I _B =-100mA		-0.85	-1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=-10μA, IE=0A	-60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =-100μA, R _{BE} =0Ω	-60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=-1mA, RBE=∞	-60			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	IE=-10μA, IC=0A	-7			V
Turn-ON Time	ton	See specified Test Circuit.		35		ns
Storage Time	tstg	See specified Test Circuit.		480		ns
Fall Time	t _f	See specified Test Circuit.		28		ns

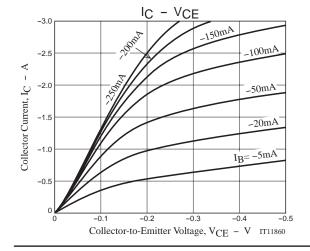
Package Dimensions

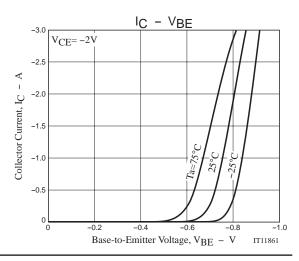
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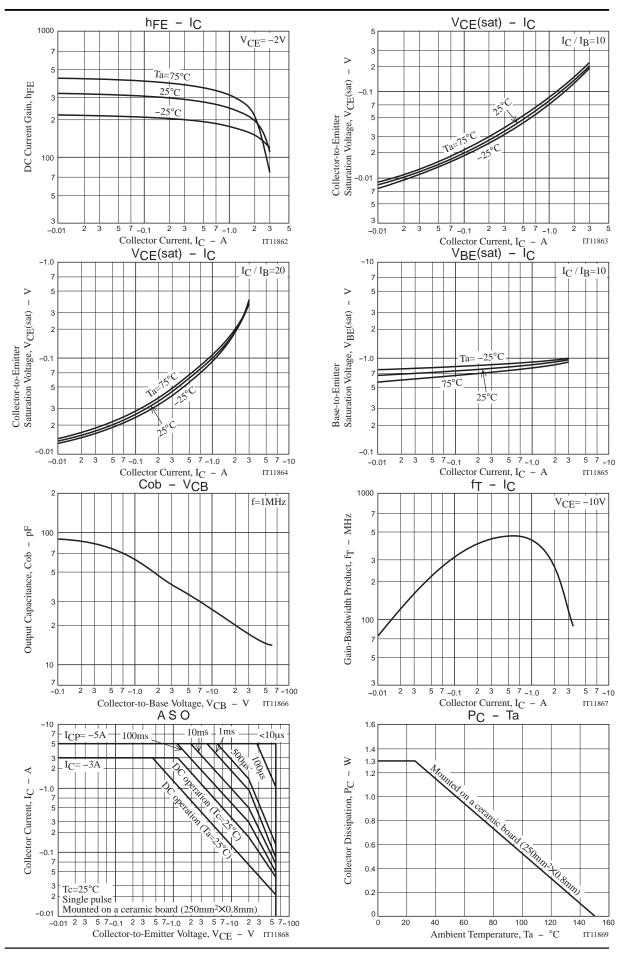


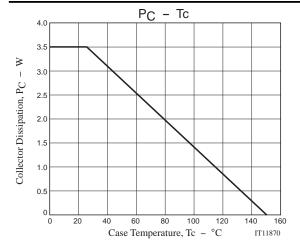
Switching Time Test Circuit











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