TOSHIBA Transistor Silicon PNP Triple Diffused Type

2SA2120

Power Amplifier Applications

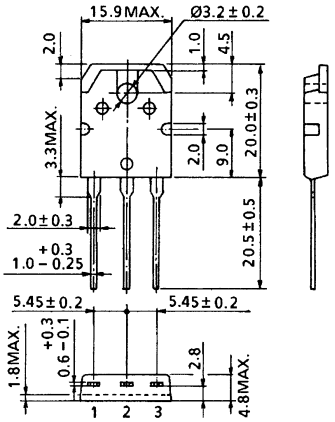
- Complementary to 2SC5948
- Recommended for audio frequency amplifier output stage.

Absolute Maximum Ratings (Tc = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	−200	V
Collector-emitter voltage	V_{CEO}	−200	V
Emitter-base voltage	V_{EBO}	−5	V
Collector current	I_C	−12	A
Base current	I_B	−1.2	A
Collector power dissipation	P_C	200	W
Junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	−55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit: mm



1. BASE
2. COLLECTOR (HEAT SINK)
3. EMITTER

JEDEC	—
JEITA	—
TOSHIBA	2-16C1A

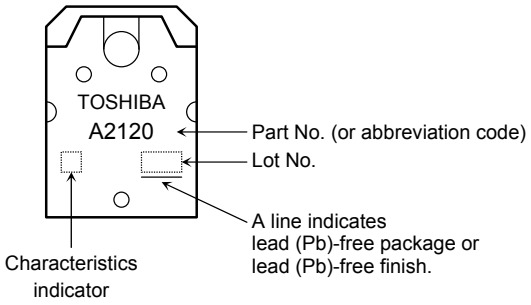
Weight: 4.7 g (typ.)

Electrical Characteristics (Tc = 25°C)

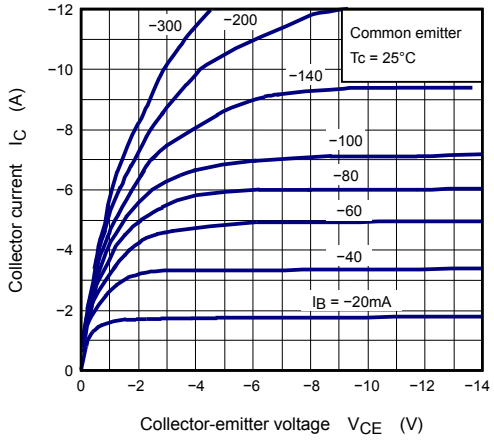
Characteristic	Symbol	Test Conditions	Min	Typ.	Max	Unit
Collector cut-off current	I_{CBO}	V_{CB} = −200 V, I_E = 0	—	—	−5.0	μA
Emitter cut-off current	I_{EBO}	V_{EB} = −5 V, I_C = 0	—	—	−5.0	μA
Collector-emitter breakdown voltage	$V_{(BR)\ CEO}$	I_C = −50 mA, I_B = 0	−200	—	—	V
DC current gain	$h_{FE\ (1)}$ (Note)	V_{CE} = −5 V, I_C = −1 A	55	—	160	
	$h_{FE\ (2)}$	V_{CE} = −5 V, I_C = −7 A	35	80	—	
Collector-emitter saturation voltage	$V_{CE\ (sat)}$	I_C = −8 A, I_B = −0.8 A	—	−1.5	−3.0	V
Base-emitter voltage	V_{BE}	V_{CE} = −5 V, I_C = −7 A	—	−1.0	−1.5	V
Transition frequency	f_T	V_{CE} = −5 V, I_C = −1 A	—	25	—	MHz
Collector output capacitance	C_{ob}	V_{CB} = −10 V, I_E = 0, f = 1 MHz	—	470	—	pF

Note: $h_{FE\ (1)}$ classification R: 55~110, O: 80~160

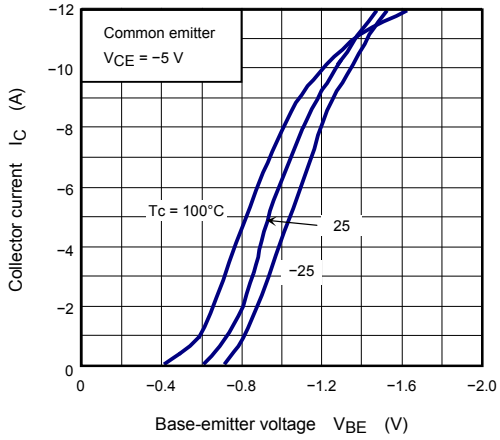
Marking



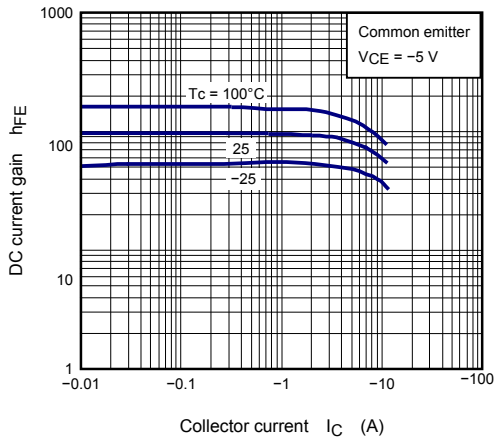
$I_C - V_{CE}$



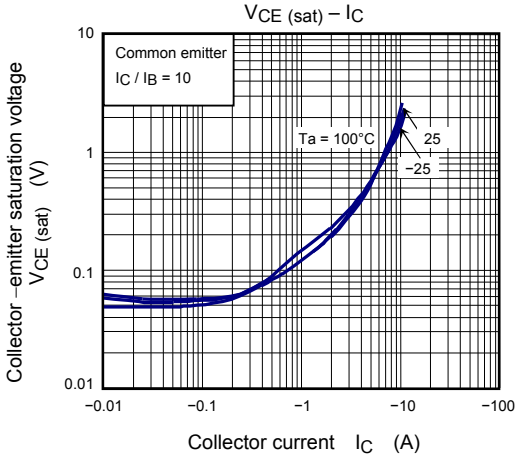
$I_C - V_{BE}$



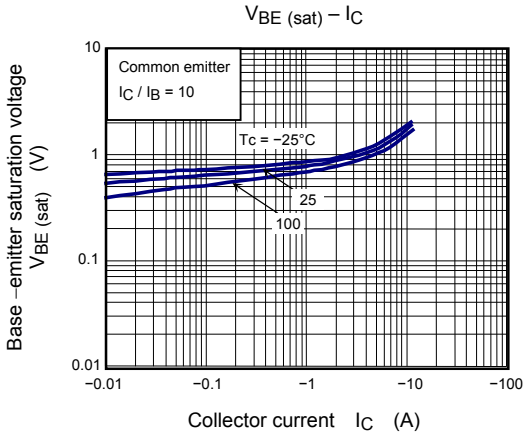
$h_{FE} - I_C$



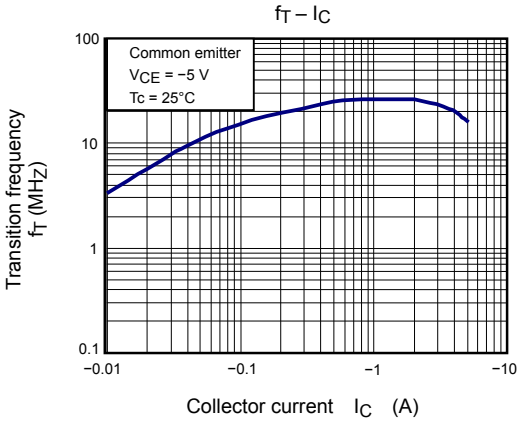
$V_{CE\ (sat)} - I_C$



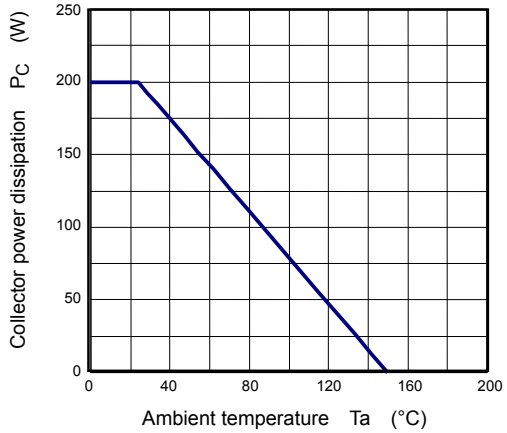
$V_{BE\ (sat)} - I_C$



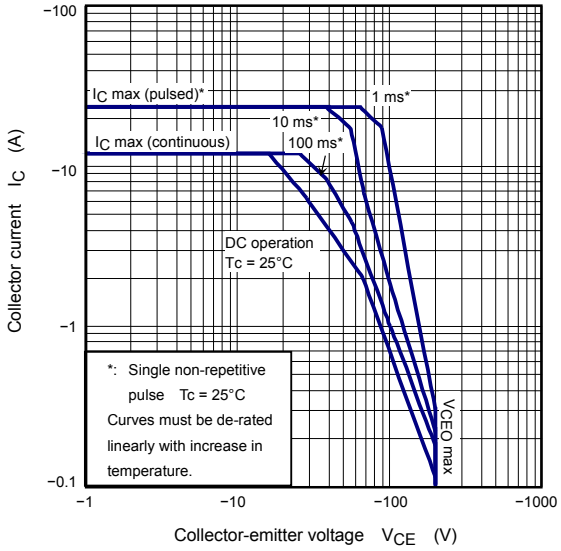
$f_T - I_C$



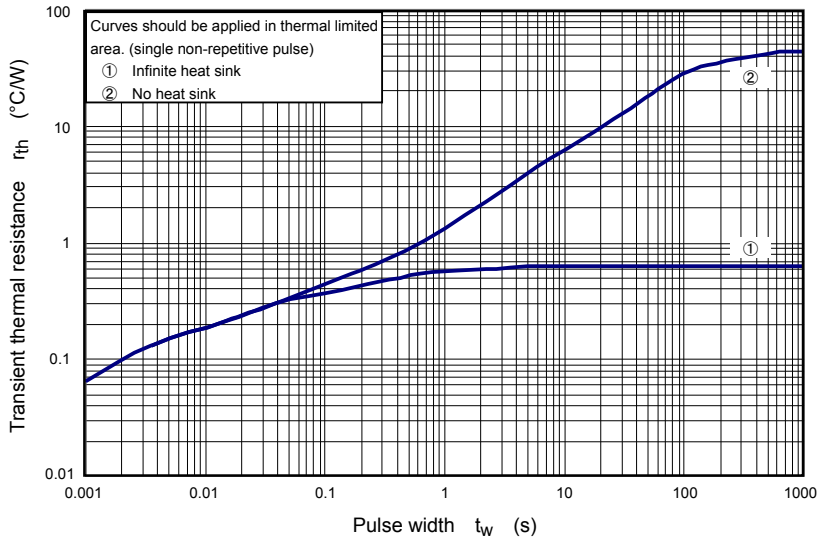
P_C – Ta



Safe operating area



r_{th} – t_w



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