-1A / -60V Bipolar transistor 2SA2092

Symbol

Vсво

VCEO

Vево

lc

CP *1

Pc *2

Тj

Tstg

Limits

-60

-60

-6

-1

-2

500

150

-55 to +150

Unit

V

V

V

А

А

mW

°C

°C

Applications

High-speed switching, low frequency amplification

Feature

- 1) High speed switching. (tf : Typ. : 30ns at lc = -1A)
- 2) Low saturation voltage. (Typ. : -200mV at Ic = -500mA, $I_B = -50mA$)
- Strong discharge resistance for inductive load and capacitance load.
- 4) Low switching noise.

Collector-base voltage

Emitter-base voltage

Collector current

Power dissipation

Junction temperature

Range of storage temperature

Collector-emitter voltage

Structure

PNP epitaxial planar silicon transistor

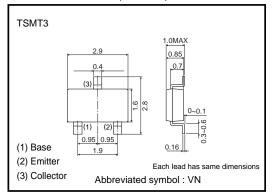
Parameter

●Absolute maximum ratings (Ta=25°C)

DC

PULSE

•External dimensions (Unit : mm)



Packaging specifications

	Package	TSMT3
	Packaging type	Taping
	Code	TL
Part No.	Basic ordering unit (pieces)	3000
2SA2092		0

hfe rank

Q	
120-270	

*1 Pw=10ms

*2 Each terminal mounted on a recommended land

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	-60	-	-	V	Ic= -1mA
Collector-base breakdown voltage	ВУсво	-60	-	-	V	lc= -100μA
Emitter-base breakdown voltage	ВVево	-6	-	-	V	Iε= -100μA
Collector cut-off current	Ісво	-	-	-1.0	μΑ	Vcb= -40V
Emitter cut-off current	Іево	-	-	-1.0	μΑ	VEB= -4V
Collector-emitter saturation voltage	VCE(sat)	-	-200	-500	mV	Ic= −500mA, Iв= −50mA
DC current gain	h _{FE} *3	120	-	270	-	Vce= -2V, Ic= -100mA
Transition frequency	f⊤ *1	-	300	-	MHz	Vce= -10V, le=100mA, f=10MHz
Collector output capacitance	Cob	-	15	-	pF	Vcb= -10V, Ie=0, f=1MHz
Turn-on time	ton	-	30	_	ns	lc= −1A, lb1= −100mA lb2=100mA Vcc ≃ −25V
Storage time	tstg	-	100	_	ns	
Fall time	tf *2	_	30	_	ns	

*1 Pulse measurement

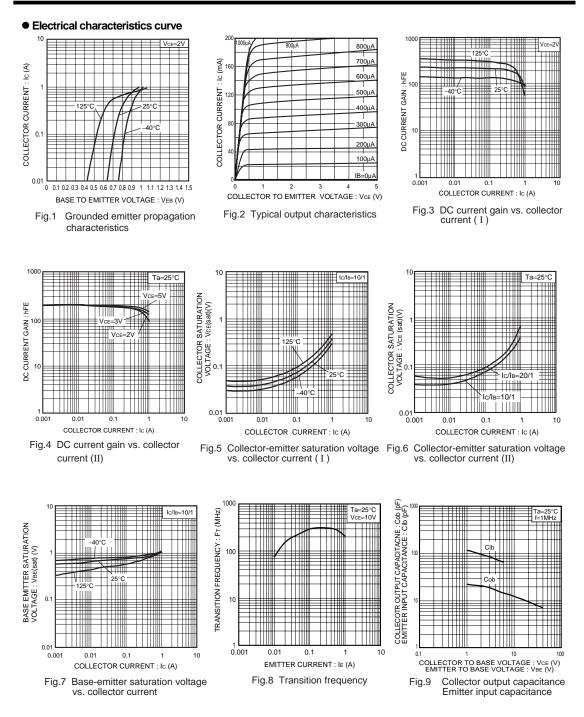
*2 See switching test circuit *3 hre rank

*3 NFE FANK



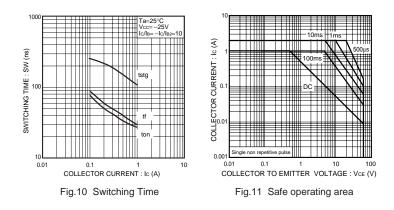
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Transistors

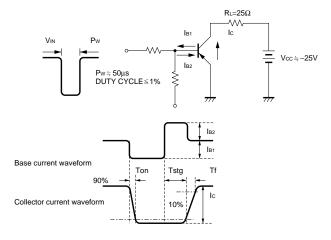


ROHM

Transistors



Switching test circuit



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