

TOSHIBA Multichip Discrete Device

# HN7G11F

○ Power supply Switching

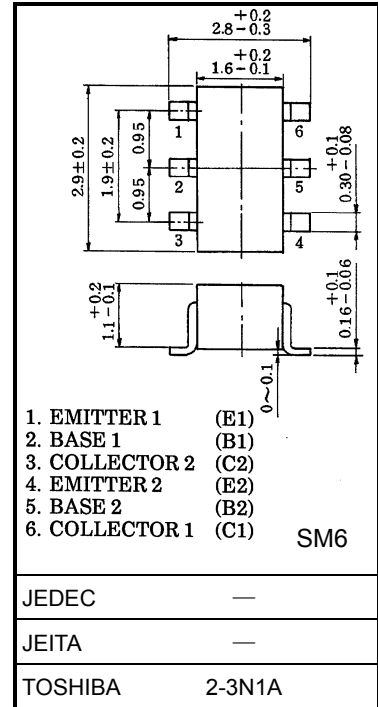
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### Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CB0</sub>	-20	V
Collector-emitter voltage	V <sub>CEO</sub>	-20	V
Emitter-base voltage	V <sub>EBO</sub>	-7	V
Collector current	I <sub>C</sub>	-1	A
Base current	I <sub>B</sub>	-150	mA

### Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CB0</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	I <sub>C</sub>	100	mA



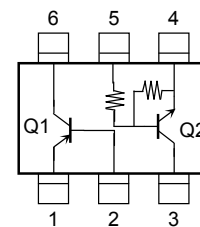
Weight: 15 mg (typ.)

### Q1, Q2 Absolute Common Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> *	300	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

\*: Total rating.

### Equivalent Circuit (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

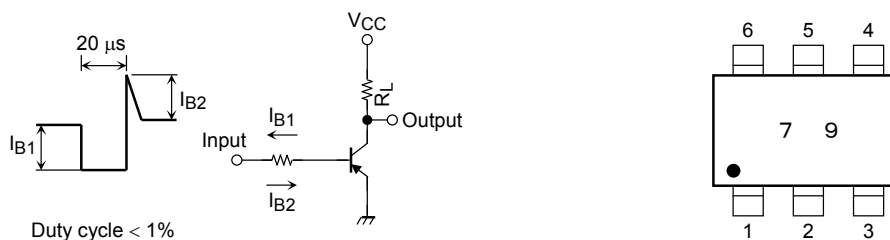
**Electrical Characteristics (Ta = 25°C) (Q1)**

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		$I_{CBO}$	$V_{CB} = -20\text{ V}, I_E = 0$	—	—	-100	nA
Emitter cut-off current		$I_{EBO}$	$V_{EB} = -7\text{ V}, I_C = 0$	—	—	-100	nA
Collector-emitter breakdown voltage		$V_{(BR)CEO}$	$I_C = -10\text{ mA}, I_B = 0$	-20	—	—	V
DC current gain		$h_{FE} (1)$	$V_{CE} = -2\text{ V}, I_C = -0.15\text{ A}$	200	—	500	
		$h_{FE} (2)$	$V_{CE} = -2\text{ V}, I_C = -0.5\text{ A}$	125	—	—	
Collector-emitter saturation voltage		$V_{CE} (sat)$	$I_C = -1\text{ A}, I_B = -50\text{ mA}$	—	-0.21	-0.30	V
			$I_C = -1\text{ A}, I_B = -100\text{ mA}$	—	-0.19	-0.28	
Base-emitter saturation voltage		$V_{BE} (sat)$	$I_C = -0.5\text{ A}, I_B = -17\text{ mA}$	—	—	-1.1	V
Switching time	Rise time	$t_r$	See Figure 1 circuit diagram.	—	40	—	ns
	Storage time	$t_{stg}$	$V_{CC} \approx -10\text{ V}, R_L = 20\ \Omega$	—	135	—	
	Fall time	$t_f$	$I_{B1} = -I_{B2} = -17\text{ mA}$	—	37	—	

**Electrical Characteristics (Ta = 25°C) (Q2)**

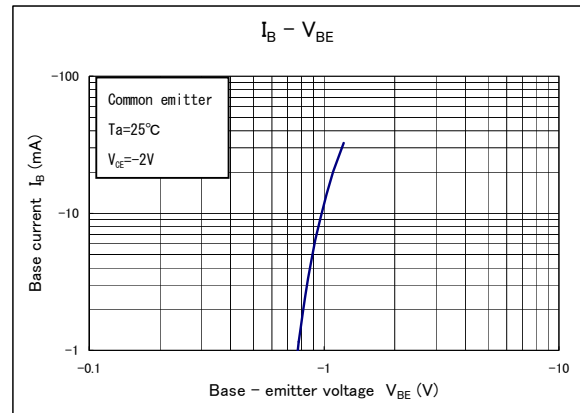
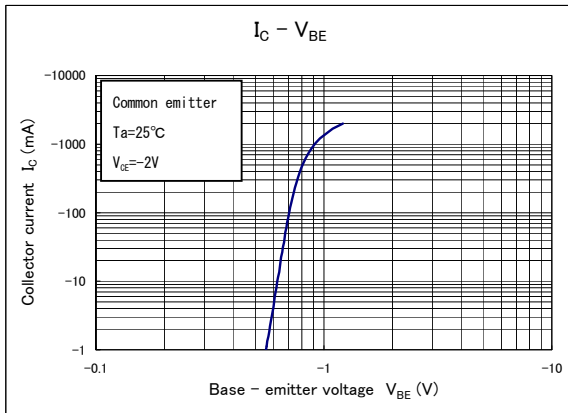
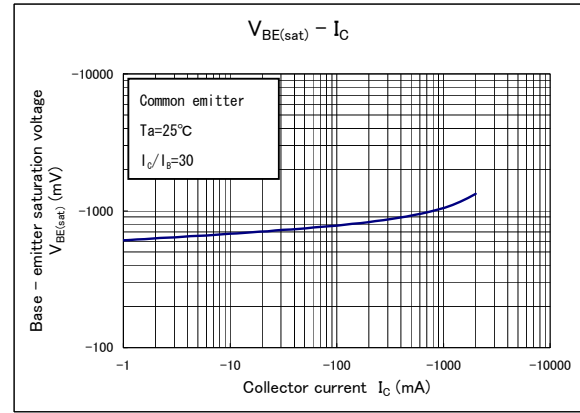
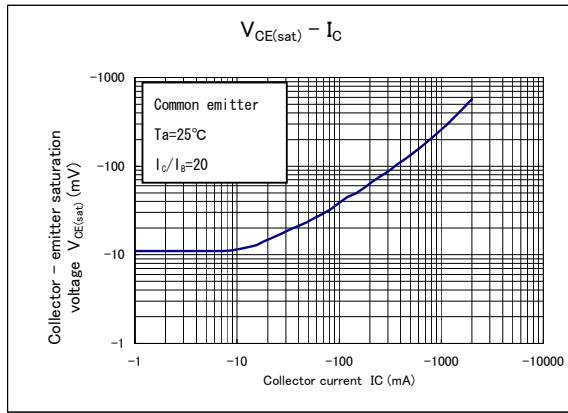
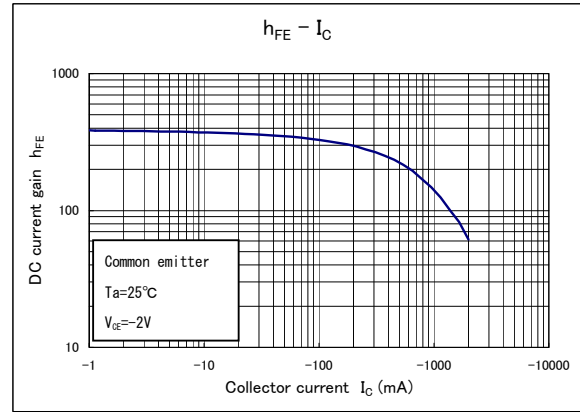
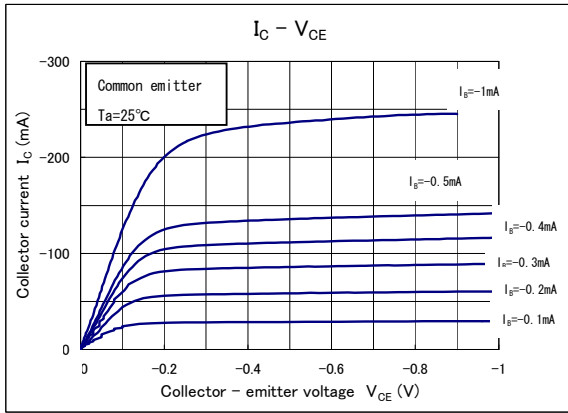
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		$I_{CBO}$	$V_{CB} = 50\text{ V}, I_E = 0$	—	—	100	nA
		$I_{CEO}$	$V_{CE} = 50\text{ V}, I_B = 0$	—	—	500	
Emitter cut-off current		$I_{EBO}$	$V_{EB} = 10\text{ V}, I_C = 0$	0.38	—	0.71	mA
DC current gain		$h_{FE}$	$V_{CE} = 5\text{ V}, I_C = 10\text{ mA}$	50	—	—	
Collector-emitter saturation voltage		$V_{CE} (sat)$	$I_C = 5\text{ mA}, I_B = 0.25\text{ mA}$	—	0.1	0.3	V
Input voltage (ON)		$V_I (ON)$	$V_{CE} = 0.2\text{ V}, I_C = 5\text{ mA}$	1.2	—	2.4	V
Input voltage (OFF)		$V_I (OFF)$	$V_{CE} = 5\text{ V}, I_C = 0.1\text{ mA}$	1.0	—	1.5	V
Transition frequency		$f_T$	$V_{CE} = 10\text{ V}, I_C = 5\text{ mA}$	—	250	—	MHz
Collector output capacitance		$C_{ob}$	$V_{CB} = 10\text{ V}, I_E = 0, f = 1\text{ MHz}$	—	3	6	pF
Input resistor		R1	—	7	10	13	kΩ
Resistor ratio		R1/R2	—	0.9	1.0	1.1	

**Marking**

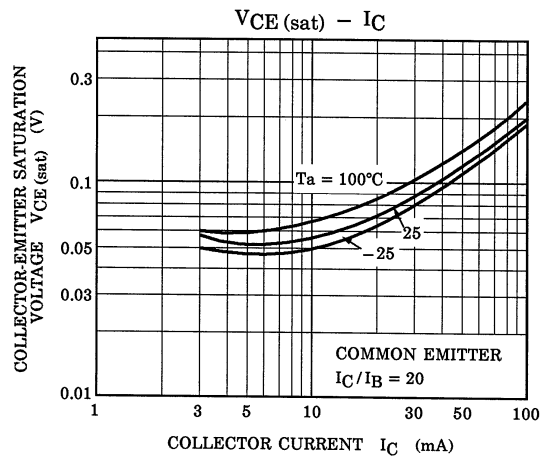
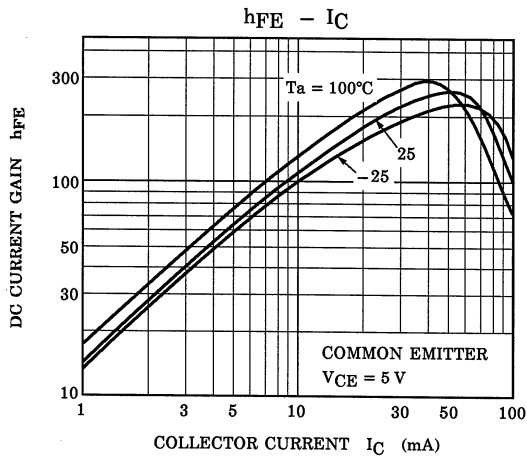
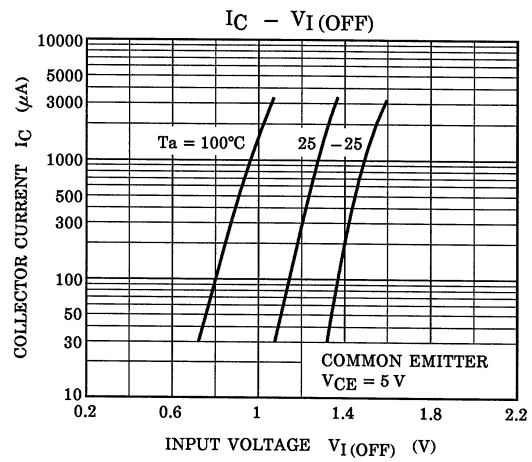
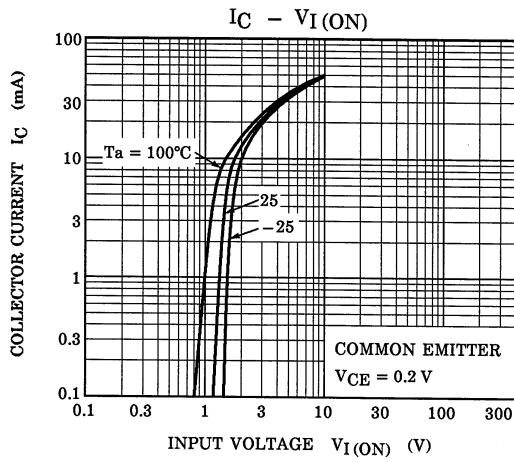


**Figure 1 Switching Time Test Circuit & Timing Chart**

Q1



Q2



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