

Preliminary

TOSHIBA Multi Chip Discrete Device

HN7G01FU

Power Management Switch Application

Driver Circuit Application

Interface Circuit Application

Unit: mm

- Q1 (transistor): 2SA1955 equivalent
- Q2 (MOS-FET): 2SK1830 equivalent

Q1 (transistor) Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-15	V
Collector-emitter voltage	V_{CEO}	-12	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I_C	-400	mA
Base current	I_B	-50	mA

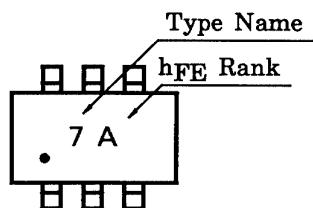
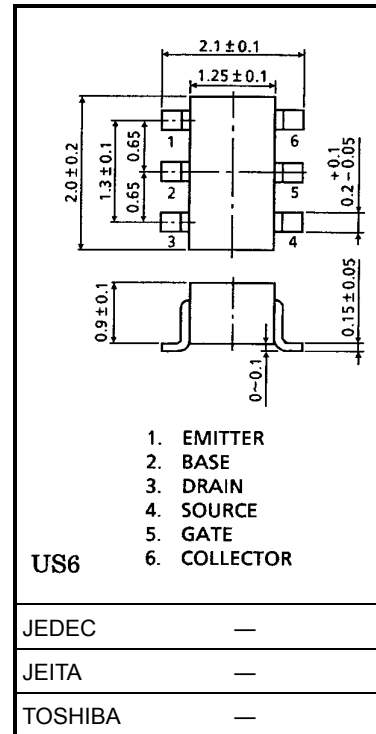
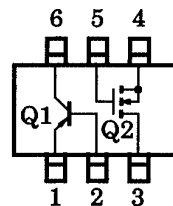
Q2 (MOS-FET) Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GSS}	10	V
Drain current	I_D	50	mA

Q1, Q2 Common Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power dissipation	P_C (Note 1)	200	mW
Junction temperature	T_j	125	°C
Storage temperature range	T_{stg}	-55~150	°C

Note 1: Total rating

Marking**Pin Assignment (top view)**

Weight: 6.8 mg (typ.)

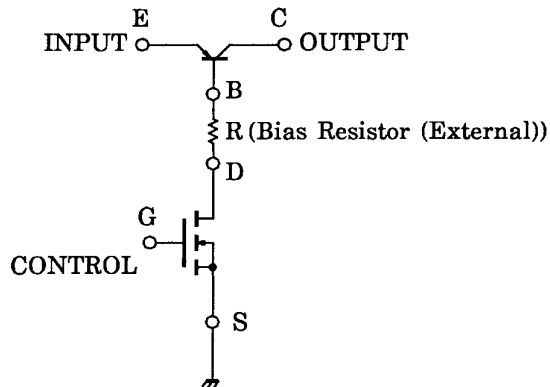
Q1 (transistor) Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	I_{CBO}	$V_{CB} = -15\text{ V}$, $I_E = 0$	—	—	-0.1	μA
Emitter cut-off current	I_{EBO}	$V_{EB} = -5\text{ V}$, $I_C = 0$	—	—	-0.1	mA
DC current gain	h_{FE} (Note 2)	$V_{CE} = -2\text{ V}$, $I_C = -10\text{ mA}$	300	—	1000	
Collector-emitter saturation voltage	$V_{CE(\text{sat}) (1)}$	$I_C = -10\text{ mA}$, $I_B = -0.5\text{ mA}$	—	-15	-30	mV
	$V_{CE(\text{sat}) (2)}$	$I_C = -200\text{ mA}$, $I_B = -10\text{ mA}$	—	-110	-250	
Base-emitter saturation voltage	$V_{BE(\text{sat})}$	$I_C = -200\text{ mA}$, $I_B = -10\text{ mA}$	—	-0.87	-1.2	V

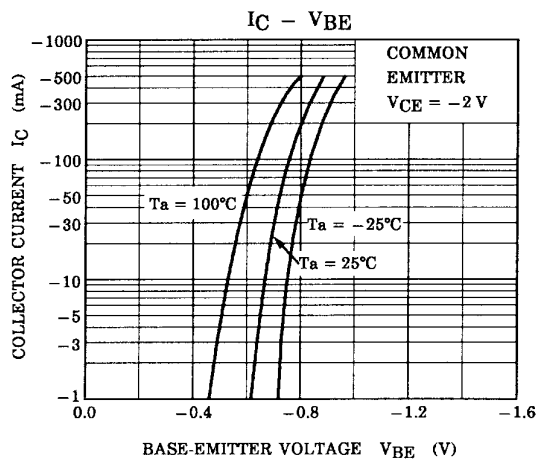
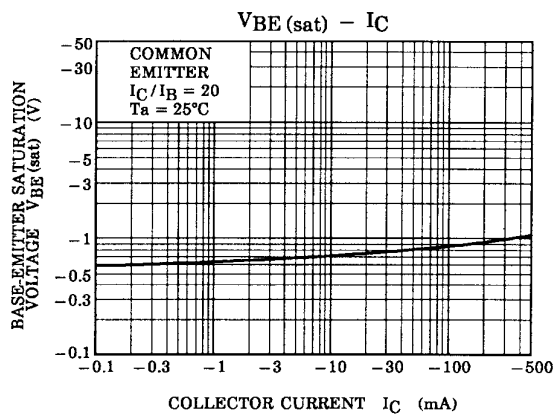
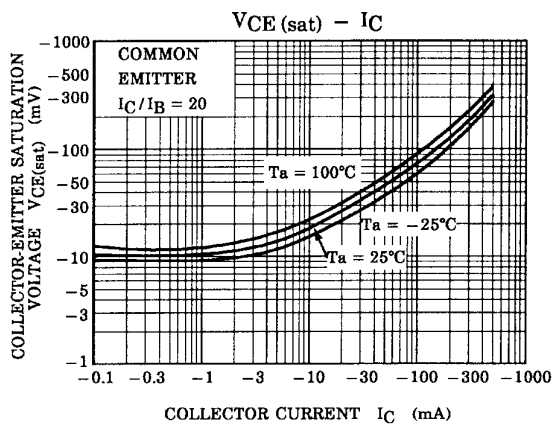
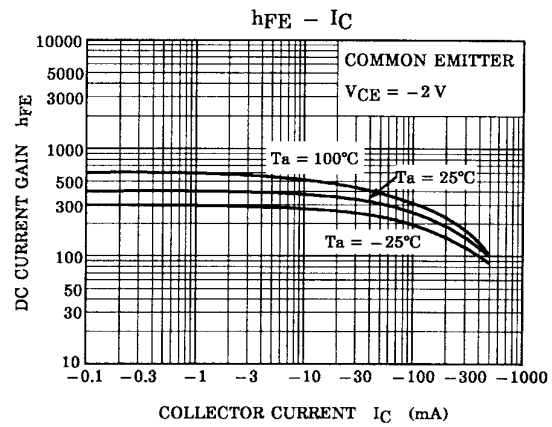
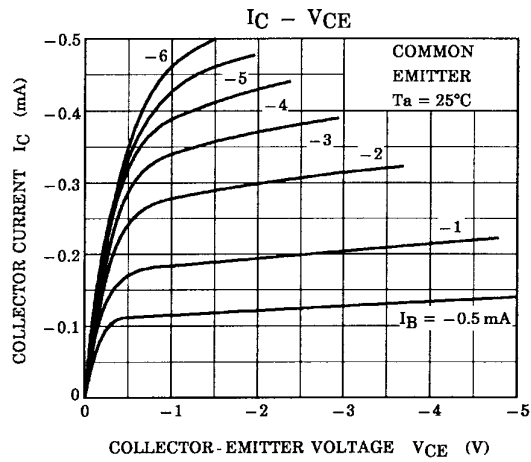
Note 2: h_{FE} classification A: 300~600, B: 500~1000

Q2 (MOS-FET) Electrical Characteristics (Ta = 25°C)

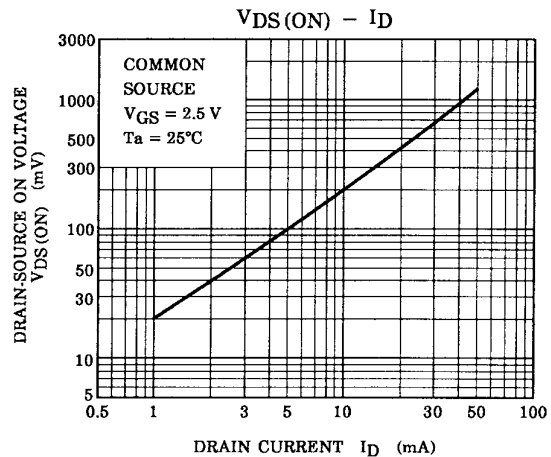
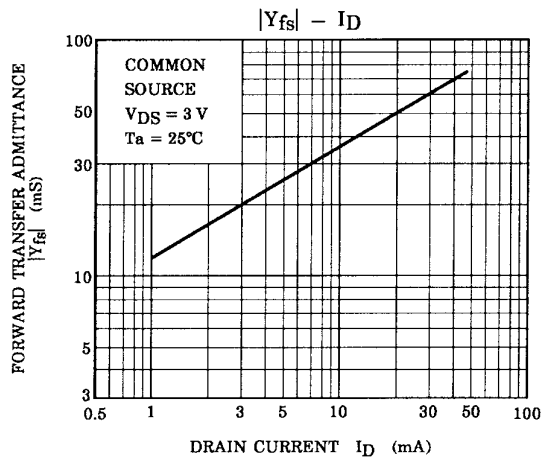
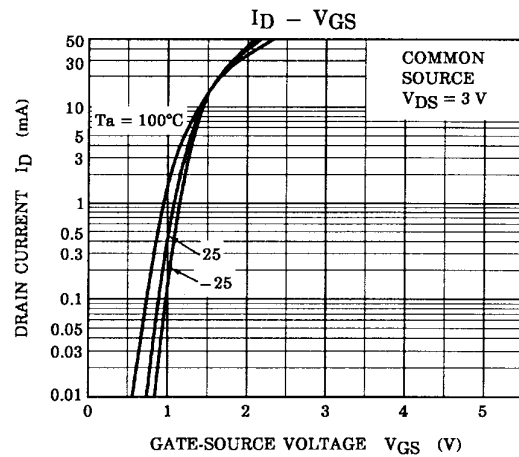
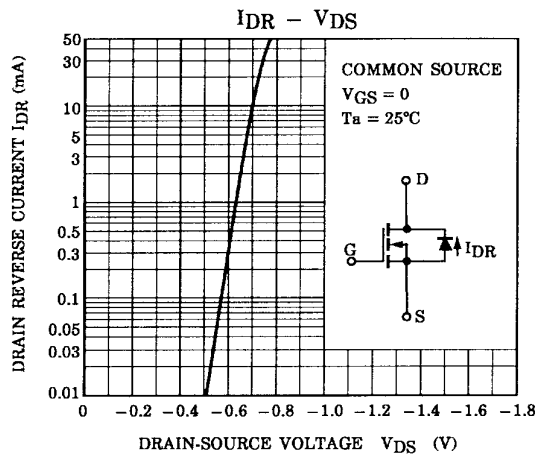
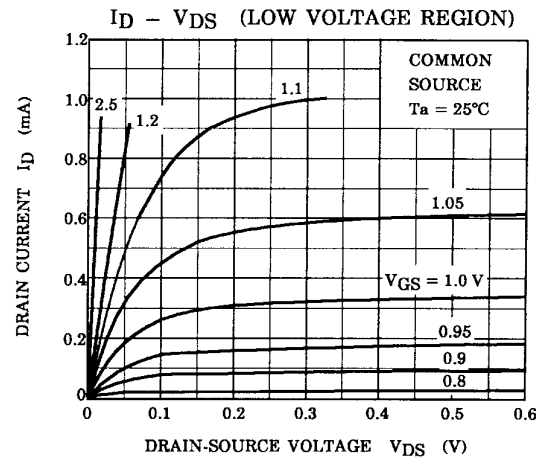
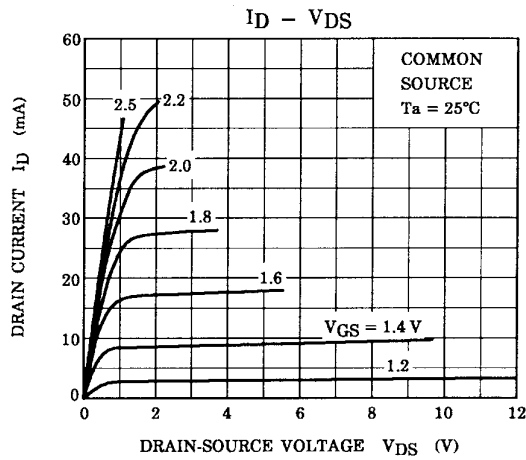
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	$V_{GS} = 10\text{ V}$, $V_{DS} = 0$	—	—	1	μA
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 100\text{ }\mu\text{A}$, $V_{GS} = 0$	20	—	—	V
Drain current	I_{DSS}	$V_{DS} = 20\text{ V}$, $V_{GS} = 0$	—	—	1	μA
Gate threshold voltage	V_{th}	$V_{DS} = 3\text{ V}$, $I_D = 0.1\text{ mA}$	0.5	—	1.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 3\text{ V}$, $I_D = 10\text{ mA}$	20	—	—	mS
Drain-source ON resistance	$R_{DS(ON)}$	$I_D = 10\text{ mA}$, $V_{GS} = 2.5\text{ V}$	—	20	40	Ω

Application Example (power management switch)

Transistor



MOS-FET



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