TOSHIBA Transistor Silicon NPN Epitaxial Type

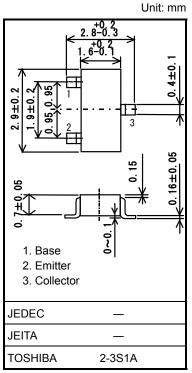
2SC6061

High-Speed Switching Applications DC-DC Converter Applications

- High-DC current gain: $h_{FE} = 120$ to 300 (IC = 0.1 A)
- Low-collector-emitter saturation: V_{CE} (sat) = 0.14 V (max)
- High-speed switching: $t_f = 0.2 \ \mu s \ (typ)$

Characteristic		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	180	V	
Collector-emitter voltage	V _{CEX}	150	V		
Collector-emitter voltage		V _{CEO}	120	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current (Note 1)	DC	Ι _C	1.0	А	
	Pulse	I _{CP}	2.0	А	
Base current	Ι _Β	0.1	А		
Collector power dissipation (Note 2)	t = 10s	D-	1000	mW	
	DC	P _C	625	mW	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	−55 to 150	°C	

Absolute Maximum Ratings (Ta = 25°C)



Weight: 0.01g (Typ.)

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Mounted on FR4 board (glass epoxy, 1.6mm thick, Cu area: 645 mm²)

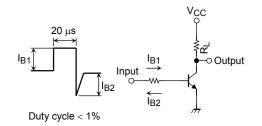
Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

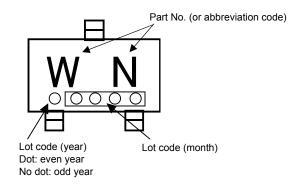
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Conditions	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	V _{CB} = 180 V, I _E = 0	_	_	100	nA
Emitter cut-off current		I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	100	nA
Collector-base breakdown voltage		V (BR) CBO	I _C = 1 mA, I _B = 0	180	_	_	V
Collector-emitter breakdown voltage		V (BR) CEO	I _C = 10 mA, I _B = 0	120	_	_	V
DC current gain		h _{FE (1)}	V _{CE} = 2 V, I _C = 1 mA	100	_	_	
		h _{FE (2)}	V _{CE} = 2 V, I _C = 0.1 A	120	_	300	
		h _{FE (3)}	V _{CE} = 2 V, I _C = 0.3 A	60	_	—	
Collector emitter saturation voltage		V _{CE (sat)}	I _C = 0.3 A, I _B = 0.01 A	_	_	0.14	V
Base-emitter saturation voltage		V _{BE (sat)}	I _C = 0.3 A, I _B = 0.01 A	—	_	1.1	V
Switching time	Rise time	tr	See Figure 3 circuit diagram V_{CC} = 72 V, R _L = 24 Ω I_{B1} = $-I_{B2}$ = 10 mA	_	0.1	_	μS
	Storage time	t _{stg}		_	1.5	_	
	Fall time	t _f		_	0.2	_	

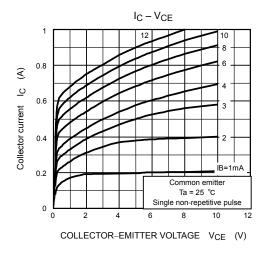
Figure 3 Switching Time Test Circuit & Timing Chart

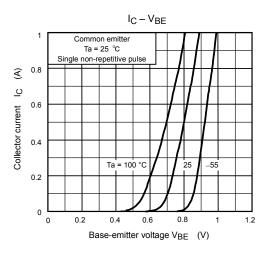


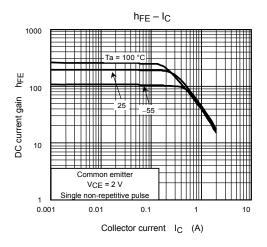
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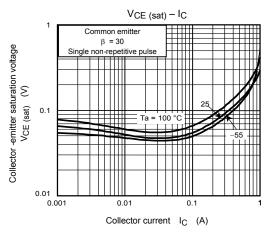


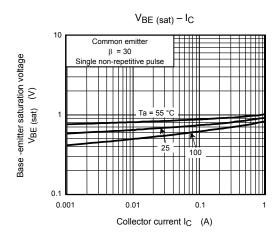
TOSHIBA

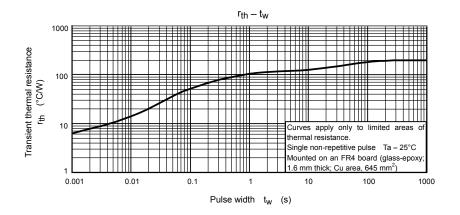


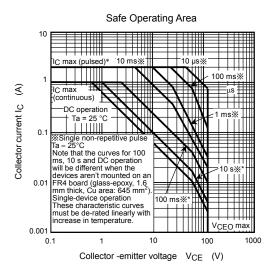












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