



SANYO Semiconductors

DATA SHEET

2SC6022

 — NPN Epitaxial Planar Silicon Transistor

DC / DC Converter Applications

Applications

- Relay drivers, lamp drivers, motor drivers, flash.

Features

- Adoption of FBET and MBIT processes.
- Large current capacitance.
- Low collector-to-emitter saturation voltage.
- High-speed switching.
- Narrow hFE range.
- High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CB0}		40	V
Collector-to-Emitter Voltage	V _{CEO}		30	V
Emitter-to-Base Voltage	V _{EBO}		6	V
Collector Current	I _C		9	A
Collector Current (Pulse)	I _{CP}		12	A
Base Current	I _B		1.2	A
Collector Dissipation	P _C		1	W
		T _c =25°C	15	W
Junction Temperature	T _j		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CB0}	V _{CB} =30V, I _E =0A			0.1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V, I _C =0A			0.1	μA
DC Current Gain	h _{FE}	V _{CE} =2V, I _C =500mA	250		400	
Gain-Bandwidth Product	f _T	V _{CE} =10V, I _C =500mA		320		MHz

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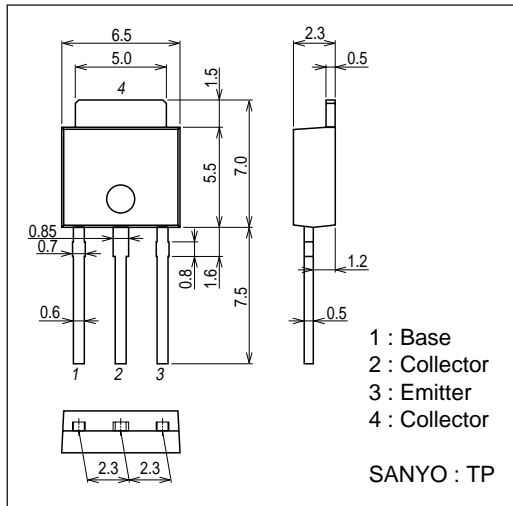
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Output Capacitance	Cob	$V_{CB}=10V, f=1MHz$		40		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=2.5A, I_B=50mA$		100	150	mV
		$I_C=4A, I_B=200mA$		120	180	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=2.5A, I_B=50mA$		0.85	1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0A$	40			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	30			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0A$	6			V
Turn-ON Time	t_{on}	See specified Test Circuit.		30		ns
Storage Time	t_{stg}	See specified Test Circuit.		320		ns
Fall Time	t_f	See specified Test Circuit.		15		ns

Package Dimensions

unit : mm

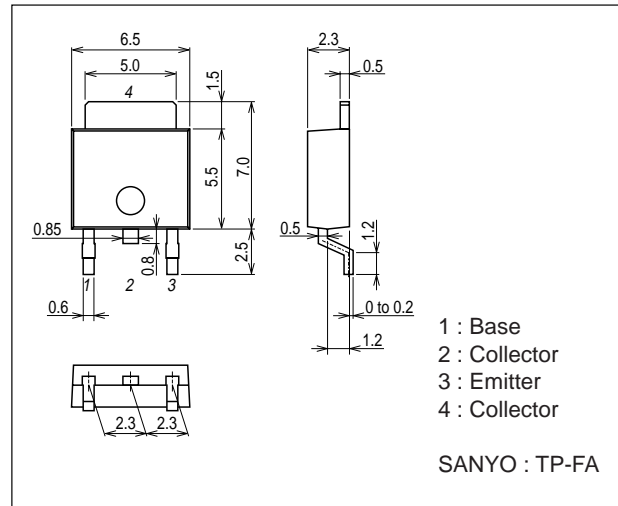
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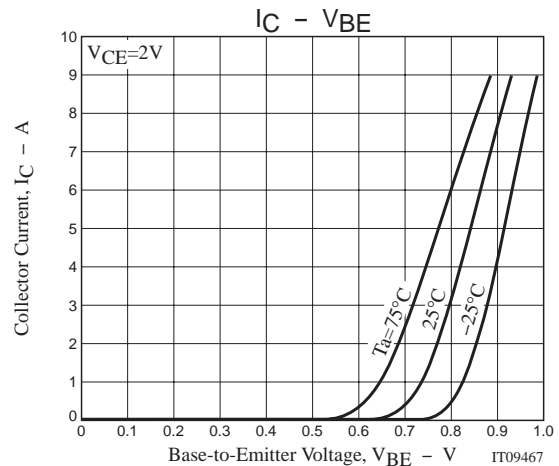
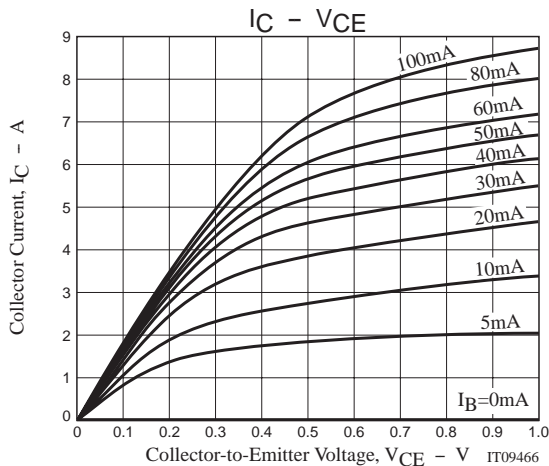
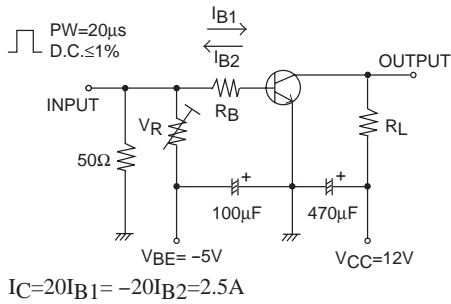
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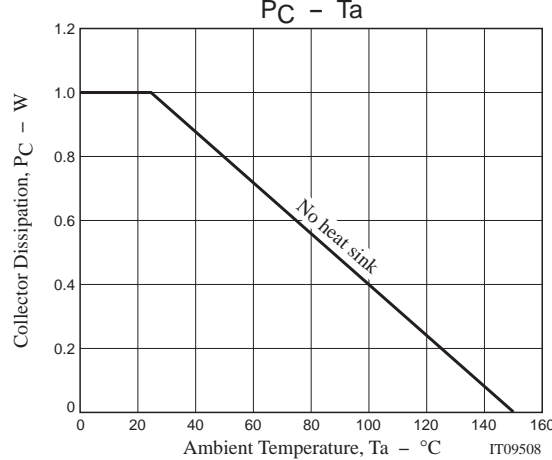
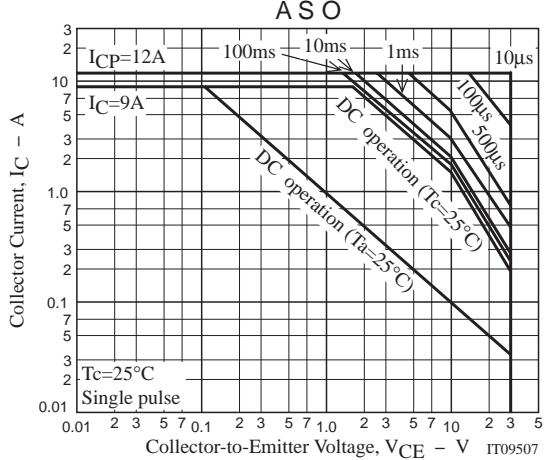
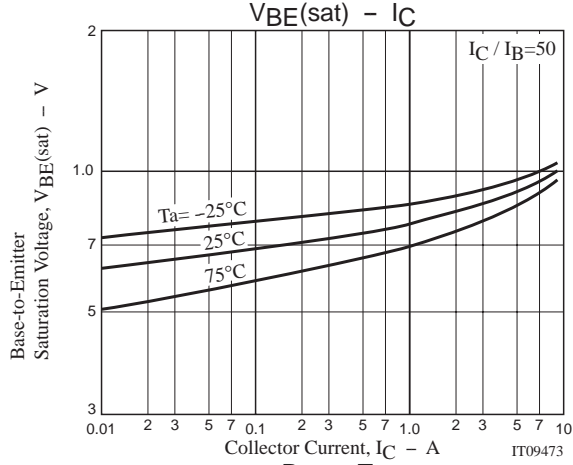
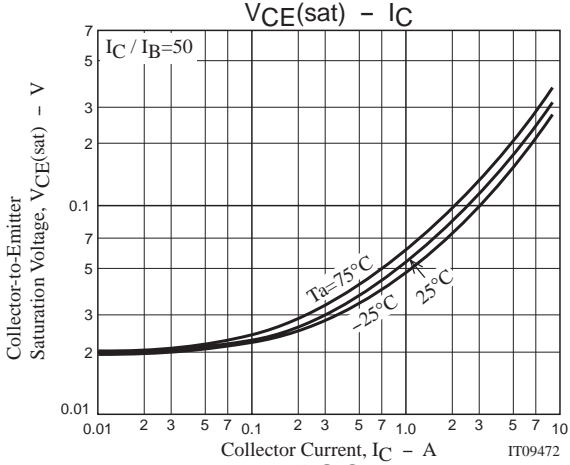
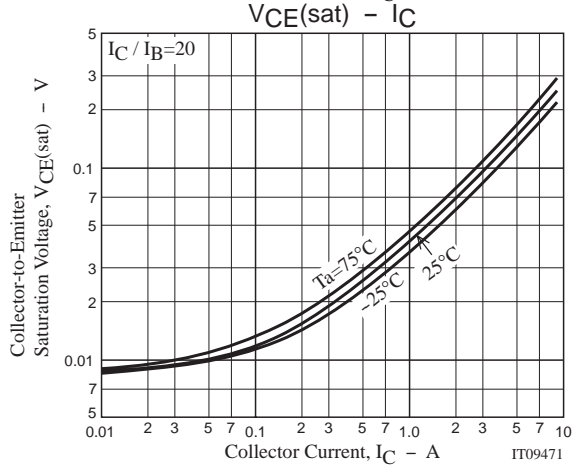
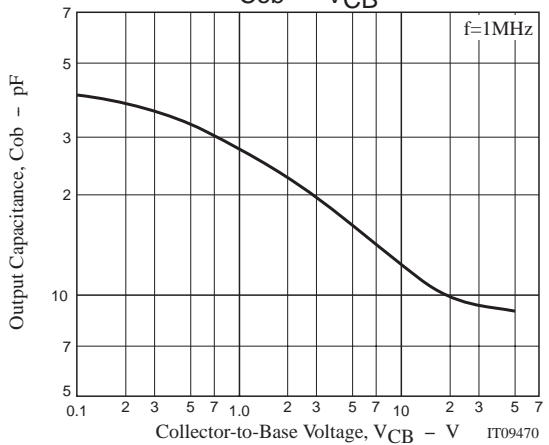
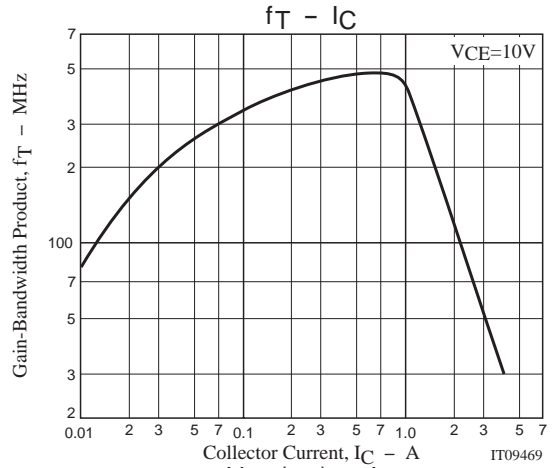
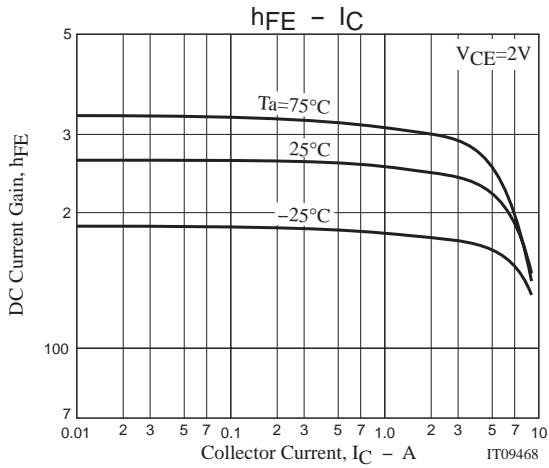
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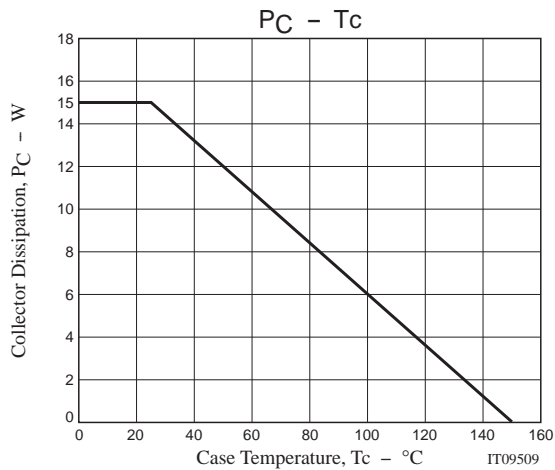
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Switching Time Test Circuit







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