#### DATA SHEET



## NPN SILICON GERMANIUM RF TRANSISTOR

2SC5761

### NPN SiGe RF TRANSISTOR FOR LOW NOISE · HIGH-GAIN AMPLIFICATION FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04)

#### **FEATURES**

- Ideal for low noise · high-gain amplification
- NF = 0.9 dB TYP. @ Vce = 2 V, Ic = 5 mA, f = 2 GHz
  - Maximum stable power gain: MSG = 20.0 dB TYP. @ VcE = 2 V, Ic = 20 mA, f = 2 GHz
  - SiGe technology (fT = 60 GHz, fmax = 60 GHz)
  - Flat-lead 4-pin thin-type super minimold (M04) package

#### ORDERING INFORMATION

Part Number	Quantity	Supplying Form	
2SC5761	50 pcs (Non reel)	8 mm wide embossed taping	
2SC5761-T2	3 kpcs/reel	Pin 1 (Emitter), Pin 2 (Collector) face the perforation side of the tape	

Remark To order evaluation samples, contact your nearby sales office.

The unit sample quantity is 50 pcs.

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = +25$ °C)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	Vсво	8.0	V
Collector to Emitter Voltage	Vceo	2.3	V
Emitter to Base Voltage	V <sub>ЕВО</sub>	1.2	٧
Collector Current	lc	35	mA
Total Power Dissipation	Ptot Note	80	mW
Junction Temperature	Tj	150	°C
Storage Temperature	T <sub>stg</sub>	-65 to +150	°C

**Note** Mounted on 1.08 cm<sup>2</sup> × 1.0 mm (t) glass epoxy substrate

#### THERMAL RESISTANCE

Parameter	Symbol	Value	Unit
Junction to Case Resistance	Rth (j-c)	150	°C/W

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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#### **ELECTRICAL CHARACTERISTICS (TA = +25°C)**

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	Ісво	VcB = 5 V, IE = 0 mA	-	-	200	nA
Emitter Cut-off Current	ІЕВО	V <sub>BE</sub> = 0.5 V, I <sub>C</sub> = 0 mA	-	-	200	nA
DC Current Gain	hfE Note 1	Vce = 2 V, Ic = 5 mA	200	-	400	-
RF Characteristics						
Insertion Power Gain	S <sub>21e</sub>   <sup>2</sup>	Vce = 2 V, Ic = 20 mA, f = 2 GHz	16.0	18.0	-	dB
Noise Figure	NF	$V_{CE} = 2 \text{ V}, \text{ Ic} = 5 \text{ mA}, \text{ f} = 2 \text{ GHz},$ $Z_{S} = Z_{opt}$	_	0.9	1.1	dB
Reverse Transfer Capacitance	Cre Note 2	Vcв = 2 V, IE = 0 mA, f = 1 MHz	-	0.17	0.22	pF
Maximum Stable Power Gain	MSG Note 3	Vce = 2 V, Ic = 20 mA, f = 2 GHz	18.0	20.0	-	dB
Gain 1 dB Compression Output Power	Po (1 dB)	Vce = 2 V, Ic = 20 mA, f = 2 GHz	-	12.0	-	dBm
3rd Order Intermodulation Distortion Output Intercept Point	OIP <sub>3</sub>	VcE = 2 V, lc = 20 mA, f = 2 GHz	-	22.0	-	dBm

**Notes 1.** Pulse measurement: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2%

2. Collector to base capacitance when the emitter grounded

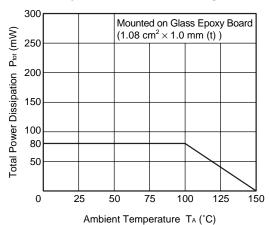
3. MSG = 
$$\frac{S_{21}}{S_{12}}$$

#### **hfe CLASSIFICATION**

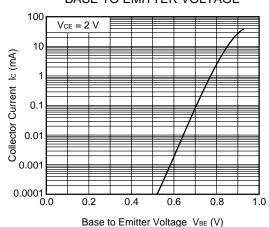
Rank	FB		
Marking	T16		
h <sub>FE</sub> Value	200 to 400		

#### TYPICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)

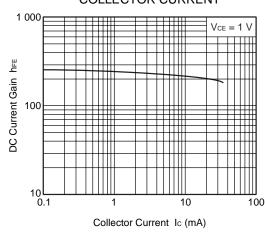
#### TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



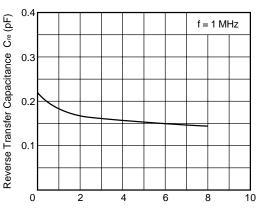
#### COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



DC CURRENT GAIN vs. COLLECTOR CURRENT

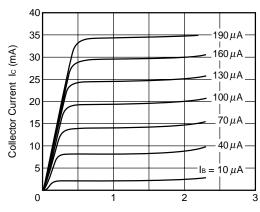


#### REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE



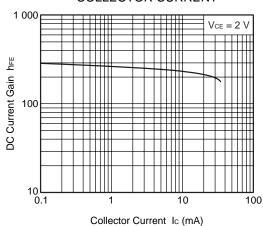
Collector to Base Voltage VcB (V)

#### COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

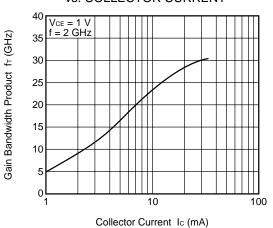


Collector to Emitter Voltage VcE (V)

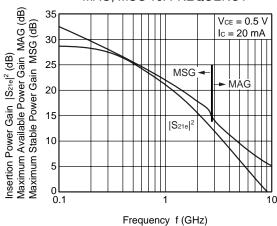
DC CURRENT GAIN vs. **COLLECTOR CURRENT** 



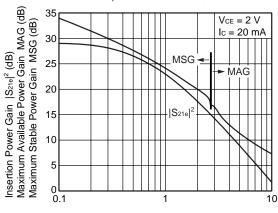
### GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT



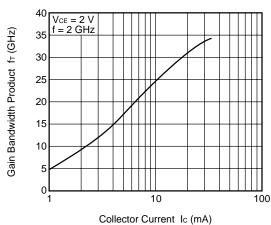
#### INSERTION POWER GAIN, MAG, MSG vs. FREQUENCY



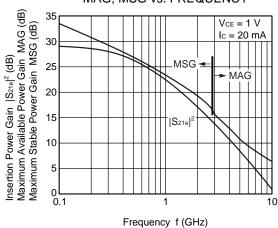
### INSERTION POWER GAIN, MAG, MSG vs. FREQUENCY



### GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT

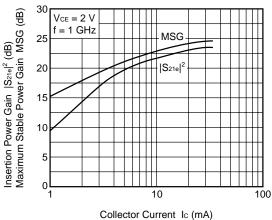


#### INSERTION POWER GAIN, MAG, MSG vs. FREQUENCY



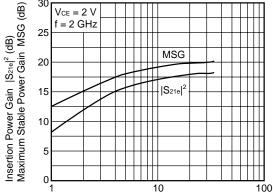
Frequency f (GHz)

#### INSERTION POWER GAIN, MSG vs. COLLECTOR CURRENT



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Vce = 2 V

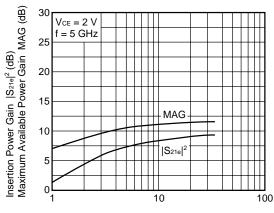


Collector Current Ic (mA)

INSERTION POWER GAIN, MSG

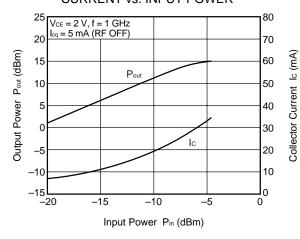
vs. COLLECTOR CURRENT

#### INSERTION POWER GAIN, MAG vs. COLLECTOR CURRENT

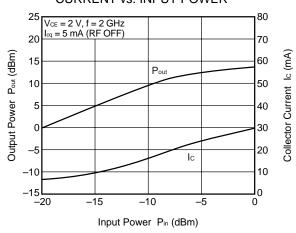


Collector Current Ic (mA)

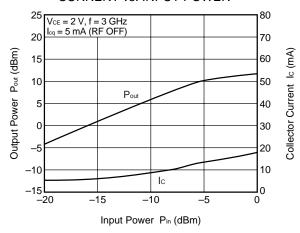
### OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER



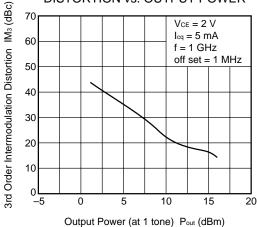
### OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER



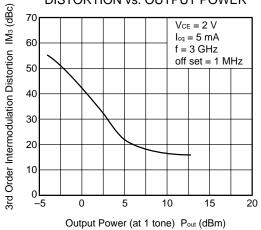
### OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER



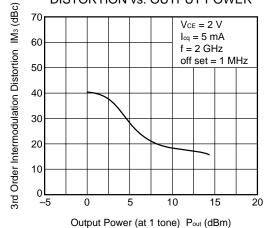
### 3RD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER

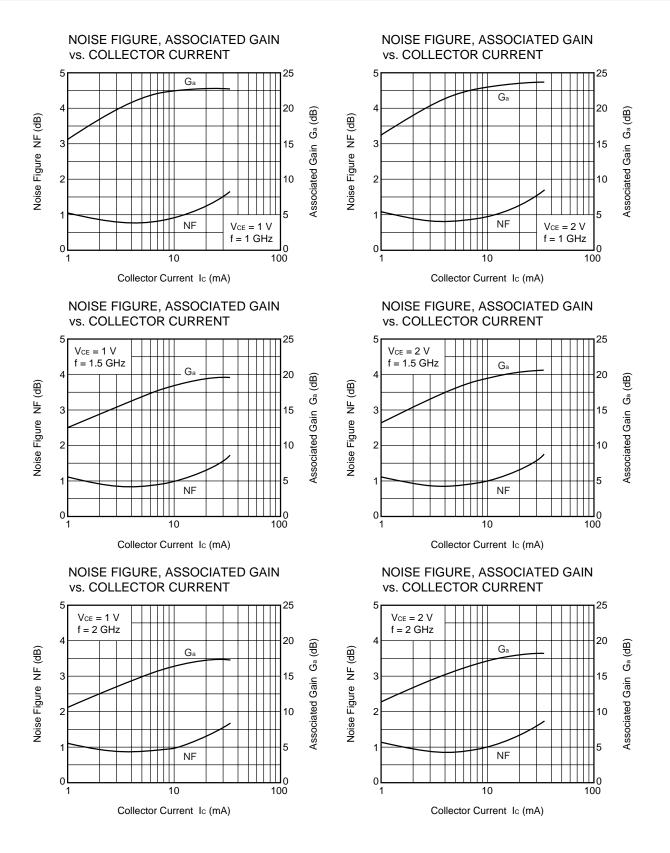


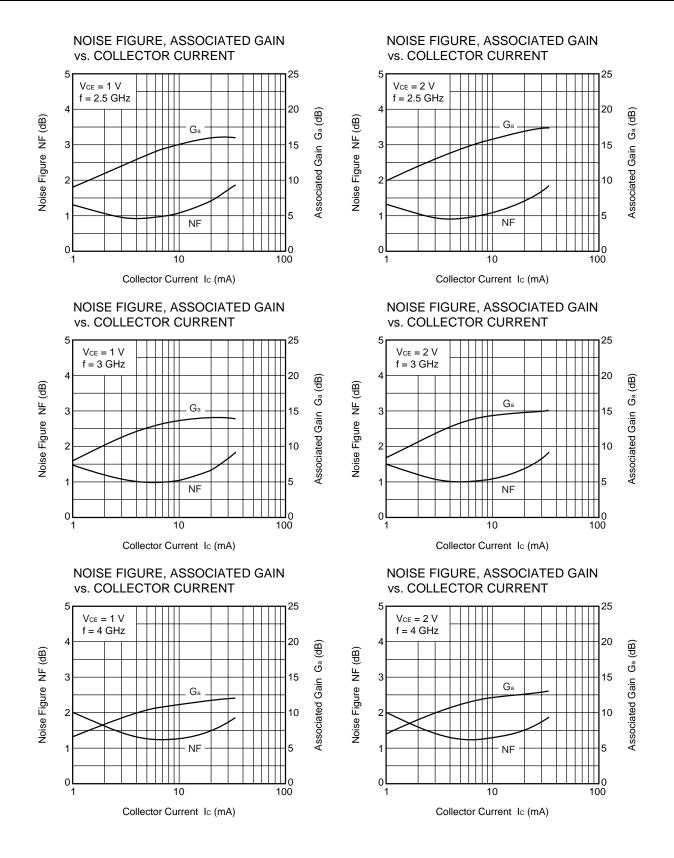
### 3RD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER



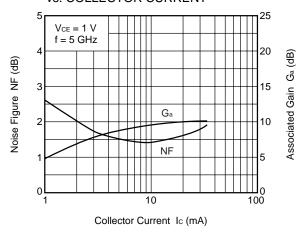
### 3RD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER



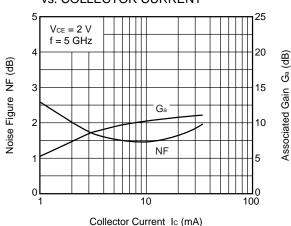




### NOISE FIGURE, ASSOCIATED GAIN vs. COLLECTOR CURRENT



### NOISE FIGURE, ASSOCIATED GAIN vs. COLLECTOR CURRENT



Remark The graphs indicate nominal characteristics.

#### **S-PARAMETERS**

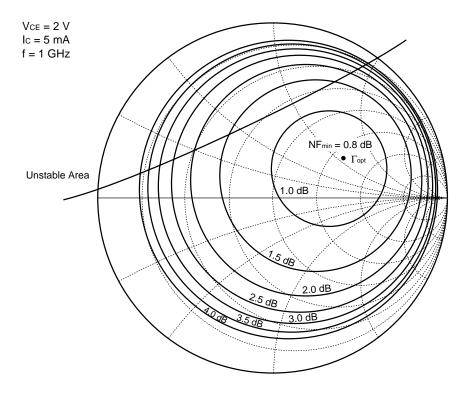
S-parameters/Noise parameters are provided on the NEC Compound Semiconductor Devices Web site in a form (S2P) that enables direct import to a microwave circuit simulator without keyboard input.

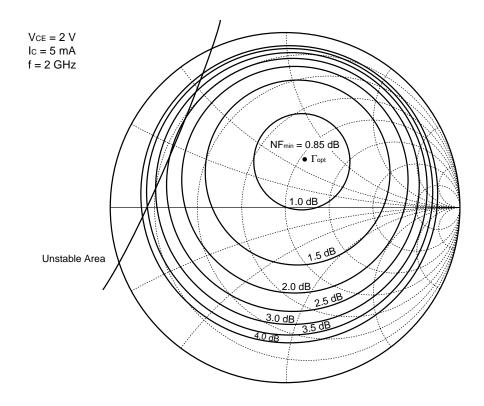
Click here to download S-parameters.

 $[\mathsf{RF} \ \mathsf{and} \ \mathsf{Microwave}] \to [\mathsf{Device} \ \mathsf{Parameters}]$ 

URL http://www.csd-nec.com/

#### **EQUAL NF CIRCLE**

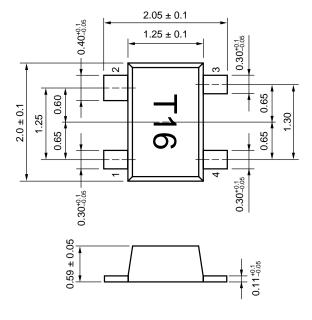




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#### **PACKAGE DIMENSIONS**

#### FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04) (UNIT: mm)



#### **PIN CONNECTIONS**

- 1. Emitter
- 2. Collector
- 3. Emitter
- 4. Base

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