2SC5469

Silicon NPN Triple Diffused Character Display Horizntal Deflection Output

HITACHI

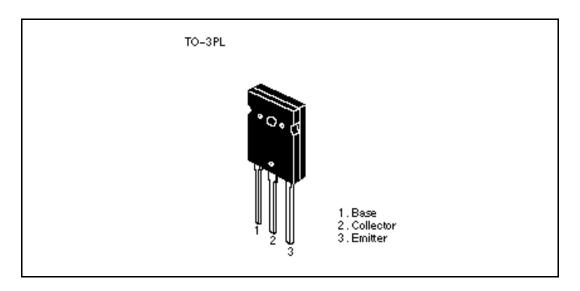
3rd. Edition December 1997 Target Specification

Features

- High breakdown voltage $V_{CBO} = 1500 \text{ V}$
- High speed switching

 $t_{\rm f} = 0.15~\mu sec$ (typ.) at $f_{\rm H} = 64kHz$

Outline





2SC5469

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	1500	V
Collector to emitter voltage	V_{CEO}	700	V
Emitter to base voltage	V_{EBO}	6	V
Collector current	Ic	15	A
Collector peak current	i _{c(peak)}	30	A
Collector power dissipation	P _c ^{Note1}	125	W
Junction temperature	Тј	150	°C
Storage temperature	Tstg	-55 to +150	°C

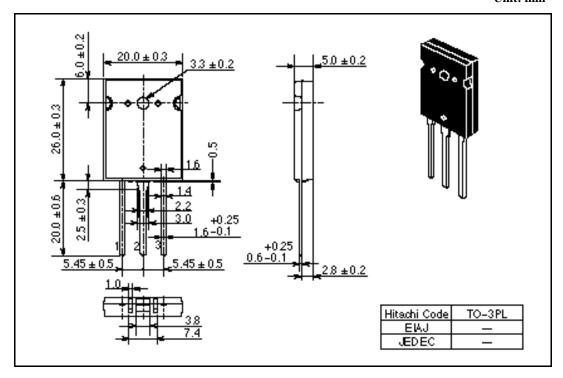
Note: 1. Value at Tc = 25°C

Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	700	_	_	V	$I_{\rm C}$ = 10mA, $R_{\rm BE}$ =
Emitter to base breakdown voltage	$V_{(BR)EBO}$	6	_	_	V	$I_{E} = 10 \text{mA}, I_{C} = 0$
Collector cutoff current	I _{CES}	_	_	500	μΑ	$V_{CE} = 1500V, R_{BE} = 0$
DC current transfer ratio	h _{FE1}	10	_	40		$V_{CE} = 5 \text{ V}, I_{C} = 1 \text{A}$
DC current transfer ratio	h _{FE2}	3.5	_	6.5		$V_{CE} = 5 \text{ V}, I_{C} = 8 \text{A}$
Collector to emitter saturation voltage	V _{CE(sat)}	_	_	5	V	$I_{\rm C} = 10A, I_{\rm B} = 3A$
Base to emitter saturation voltage	$V_{BE(sat)}$	_	_	1.5	V	$I_C = 10A$, $I_B = 3A$
Fall time	t,	_	0.2	0.4	μs	$I_{CP} = 7A, I_{B1} = 2.8A$ $f_{H} = 31.5kHz$
Fall time	t,	_	0.15	_	μs	$I_{CP} = 7A, I_{B1} = 1.8A$ $f_{H} = 64kHz$

Package Dimensions

Unit: mm



When using this document, keep the following in mind:

- 1. This document may, wholly or partially, be subject to change without notice.
- 2. All rights are reserved: No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without Hitachi's permission.
- 3. Hitachi will not be held responsible for any damage to the user that may result from accidents or any other reasons during operation of the user's unit according to this document.
- 4. Circuitry and other examples described herein are meant merely to indicate the characteristics and performance of Hitachi's semiconductor products. Hitachi assumes no responsibility for any intellectual property claims or other problems that may result from applications based on the examples described herein.
- 5. No license is granted by implication or otherwise under any patents or other rights of any third party or Hitachi, Ltd.
- 6. MEDICAL APPLICATIONS: Hitachi's products are not authorized for use in MEDICAL APPLICATIONS without the written consent of the appropriate officer of Hitachi's sales company. Such use includes, but is not limited to, use in life support systems. Buyers of Hitachi's products are requested to notify the relevant Hitachi sales offices when planning to use the products in MEDICAL APPLICATIONS.

HITACHI

Hitachi, Ltd.

Semiconductor & IC Div. Nappon Bida, 26-2, Ohte-machi, Chiyoda-ku, Tokyo 400, Japan Tet Tokyo (03, 3270-2144 Fax: 703, 3270-5109

For further information write to:

Hitechi America, U.d. Semicondudor & IC Dev. 2000 Sierre Point Perlaway Brisbana, CA. 9405- 835 U.S.A. Tet 445-839-8300 Fax: 445-833-4207 Hitachi Burope GmbH
Bedtronic Componente Group
Continental Burope
Darrecher Straße 3
D-85832 Feldkirchen
Minchen
Tet (88,4,94,80,0)
Fex: (89,4,94,20,30,0)

Hitechi Burope Ltd.
Bedronic Componente Div.
Northam Burope Heedquertere
Whitebrook Ferk
Lower Cookhem Roed
Meiderheed
Berkehire SL68YA
United Kingdom
Tet 0628-585000
Fex 0628-178322

Hitschi Asia Pte. Ltd 45 Collyer Quey \$20-00 Hitschi Tower Snappore 0104 Tet 535-2100 Fex: 535-1533

Hischi Asia (Hong Kong) Ltd. Unit 705, North Towar, World Finance Centra, Herbour City, Carton Road Taim She Taul, Kowloon Hong Kong Tet 27359218 Fax: 27309074

Copyright @Hitschi, Ltd., 1997. All rights reserved. Printed in Japan.