DATA SHEET

NEC

NPN SILICON RF TRANSISTOR
2SC5012

NPN EPITAXIAL SILICON RF TRANSISTOR FOR HIGH-FREQUENCY LOW-NOISE AMPLIFICATION 4-PIN SUPER MINIMOLD

FEATURES

- High Gain Bandwidth Product (f_T = 9 GHz TYP.)
- Low Noise, High Gain
- Low Voltage Operation
- 4-pin super minimold Package

★ ORDERING INFORMATION

Part Number	Quantity	Supplying Form
2SC5012	50 pcs (Non reel)	• 8 mm wide embossed taping
2SC5012-T1	3 kpcs/reel	• Pin 3 (Base), Pin 4 (Emitter) face to perforation side of the tape

Remark To order evaluation samples, contact your nearby sales office.
The unit sample quantity is 50 pcs.

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V_{CBO}	20	V
Collector to Emitter Voltage	V_{CEO}	10	V
Emitter to Base Voltage	V_{EBO}	1.5	V
Collector Current	I_C	65	mA
Total Power Dissipation	P_{tot} Note	150	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	–65 to +150	°C

Note Free air

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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The mark ★ shows major revised points.

ELECTRICAL CHARACTERISTICS (T_A = +25°C)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	I_{CBO}	V_{CB} = 10 V, I_E = 0 mA	–	–	1.0	μA
Emitter Cut-off Current	I_{EBO}	V_{EB} = 1 V, I_C = 0 mA	–	–	1.0	μA
DC Current Gain	h_{FE} Note 1	V_{CE} = 8 V, I_C = 20 mA	50	100	250	–
RF Characteristics						
Gain Bandwidth Product	f_T	V_{CE} = 8 V, I_C = 20 mA	–	9.0	–	GHz
Insertion Power Gain	$\|S_{21e}\|^2$	V_{CE} = 8 V, I_C = 20 mA, f = 1.0 GHz	13	15	–	dB
Noise Figure	NF	V_{CE} = 8 V, I_C = 7 mA, f = 1.0 GHz	–	1.2	2.5	dB
Reverse Transfer Capacitance	C_{re} Note 2	V_{CB} = 10 V, I_E = 0 mA, f = 1.0 MHz	–	0.25	0.8	pF

Notes 1. Pulse measurement: PW ≤ 350 μs, Duty Cycle ≤ 2%

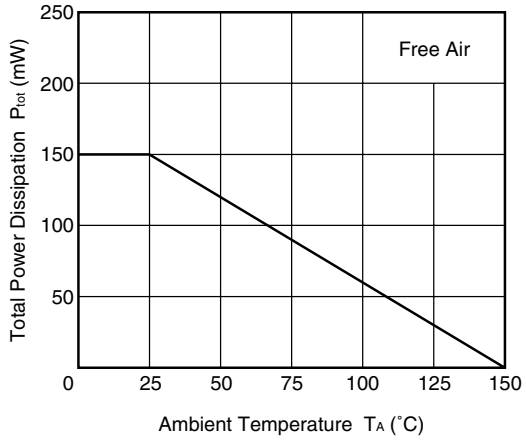
2. Collector to base capacitance when the emitter grounded

h_{FE} CLASSIFICATION

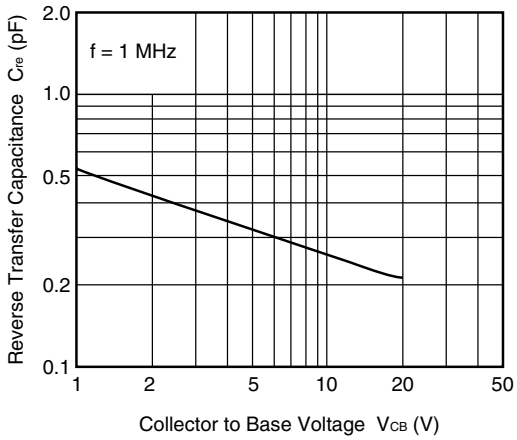
Rank	EB	FB	GB
Marking	R36	R37	R38
h_{FE} Value	50 to 100	80 to 160	125 to 250

TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

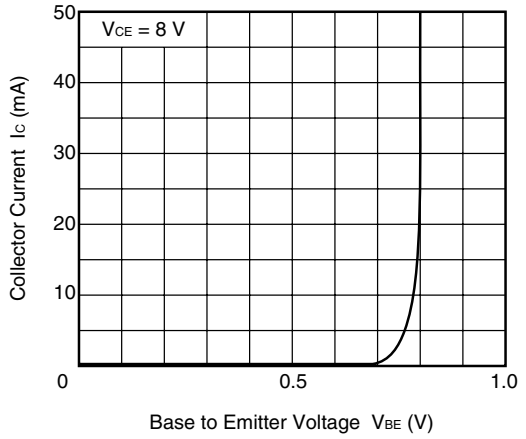
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



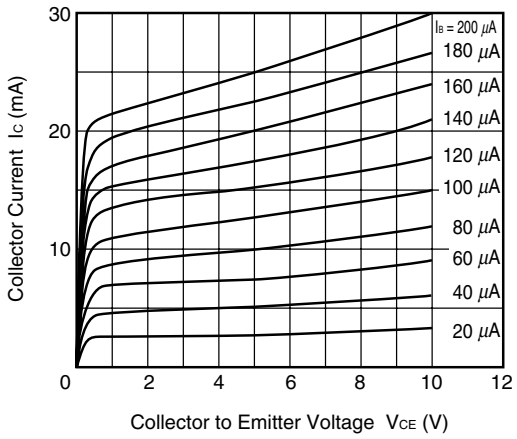
REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE



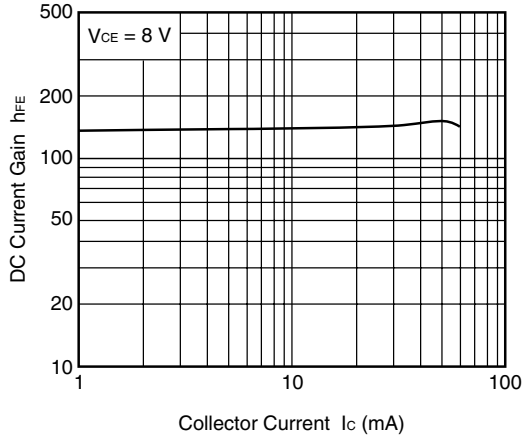
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



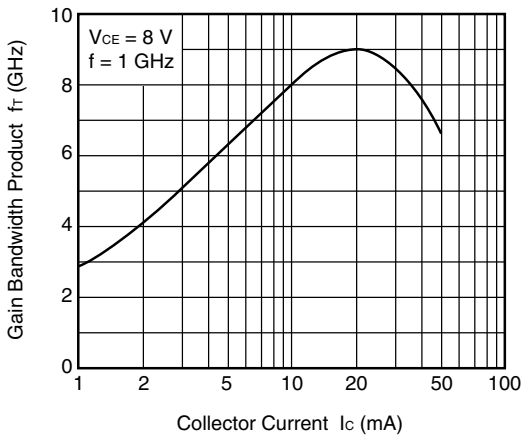
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



DC CURRENT GAIN vs. COLLECTOR CURRENT

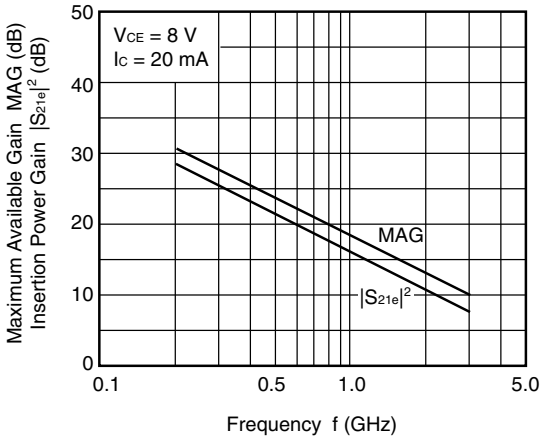


GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT

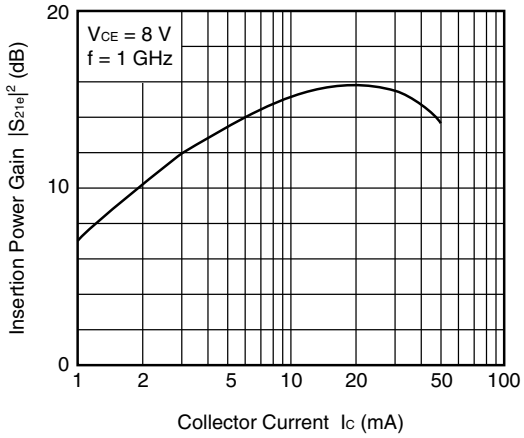


Remark The graphs indicate nominal characteristics.

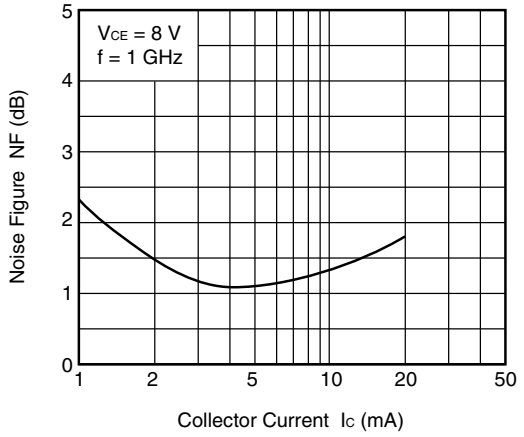
MAXIMUM AVAILABLE GAIN/INSERTION POWER GAIN vs. FREQUENCY



INSERTION POWER GAIN vs. COLLECTOR CURRENT



NOISE FIGURE vs. COLLECTOR CURRENT



Remark The graphs indicate nominal characteristics.

★ S-PARAMETERS

S-parameters/Noise parameters are provided on the NEC Compound Semiconductor Devices Web site in a form (S2P) that enables direct import to a microwave circuit simulator without keyboard input.

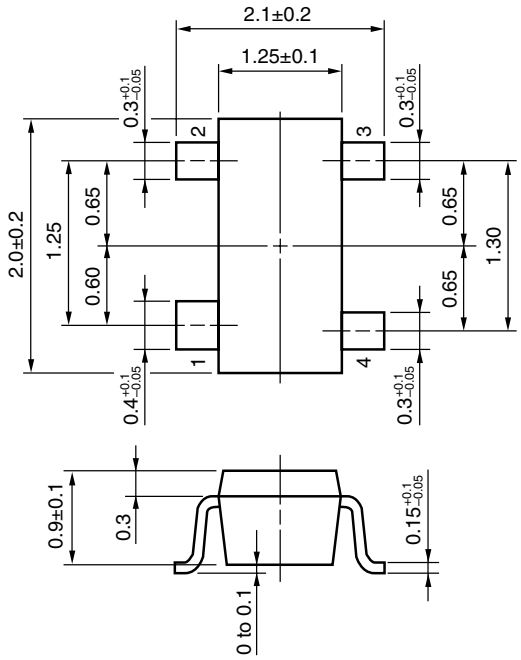
Click here to download S-parameters.

[RF and Microwave] → [Device Parameters]

URL http://www.ncsd.necel.com/

★ **PACKAGE DIMENSIONS**

4-PIN SUPER MINIMOLD (UNIT: mm)



PIN CONNECTIONS

1. Collector
2. Emitter
3. Base
4. Emitter

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▶**For further information, please contact**

NEC Compound Semiconductor Devices, Ltd. **http://www.ncsd.necel.com/**
E-mail: salesinfo@ml.ncsd.necel.com (sales and general)
techinfo@ml.ncsd.necel.com (technical)
Sales Division TEL: +81-44-435-1588 FAX: +81-44-435-1579

NEC Compound Semiconductor Devices Hong Kong Limited
E-mail: ncsd-hk@elhk.nec.com.hk (sales, technical and general)

Hong Kong Head Office	TEL: +852-3107-7303	FAX: +852-3107-7309
Taipei Branch Office	TEL: +886-2-8712-0478	FAX: +886-2-2545-3859
Korea Branch Office	TEL: +82-2-558-2120	FAX: +82-2-558-5209

NEC Electronics (Europe) GmbH **http://www.ee.nec.de/**
TEL: +49-211-6503-0 FAX: +49-211-6503-1327

California Eastern Laboratories, Inc. **http://www.cel.com/**
TEL: +1-408-988-3500 FAX: +1-408-988-0279

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