No.4719

PNP Epitaxial Planar Silicon Transistor

Muting Circuits, Driver Applications

Features

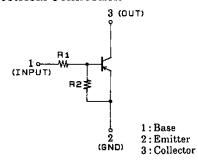
- On-chip bias resistors (R1 = $4.7k\Omega$, R2 = $4.7k\Omega$).
- · Very small-sized package making 2SA1864-applied sets small and slim.
- · Small ON resistance.
- · High gain-bandwidth product f_T.

Absolute Maximum Ratings at Ta = 25°C				unit	
-	V_{CBO}		-15	V	
	VCEO		-15	V	
	V_{EBO}		-10	V	
Input Voltage	V_{IN}^{-1}		-14	V	
Collector Current	$I_{\mathbb{C}}$		100	mA	
	I_{CP}		-200	mA	
	IB		-20	mA	
	$P_{\mathbf{C}}$		150	mW	
	Tj		150	$^{\circ}\mathrm{C}$	
	Tstg		-55 to +150	°C	
Electrical Characteristics at Ta = 25°C			min typ	max	unit
Collector Cutoff Current	I_{CBO}	$V_{CB} = -10V, I_E = 0$	• •	-0.1	μ A
Collector Cutoff Current	ICEO	$V_{CE} = -10V, I_B = 0$		-0.5	μ A
Emitter Cutoff Current		$V_{EB} = -5V, I_C = 0$	-410 -532	-760	μ A
		$V_{CE} = -2V, I_{C} = -20mA$	50		•
		$V_{CE} = -5V, I_{C} = -10mA$	600		MHz
Output Capacitance	Cob*	$V_{CB} = -10V, f = 1MHz$	0.9		рF
C-E Saturation Voltage	V _{CE(sat)}	$I_C = -5 \text{mA}, I_B = -0.5 \text{mA}$	-30	 100	mV
C-B Breakdown Voltage	$V_{(BR)CBO}$	$I_{\rm C} = -10 \mu {\rm A}, I_{\rm E} = 0$	-15		V
C-E Breakdown Voltage	$V_{(BR)CEO}$	$I_C = -1 \text{mA}, R_{BE} = \infty$	-15		V
Input OFF-State Voltage	$V_{IN(off)}$	$V_{CE} = -2V, I_{C} = -100 \mu A$	-0.8 -1.2	-1.5	V
Input ON-State Voltage	V _{IN(on)}	$V_{CE} = -0.3V, I_{C} = -20mA$	-1.0 -2.1	-4.0	V
	R1		3.3 4.7	6.1	$\mathbf{k}\Omega$
Resistance Ratio	R1/R2		0.9 1.0	1.1	
ON Resistance	Ron	$V_{IN} = -5V, f = 1MHz$	5.0		Ω

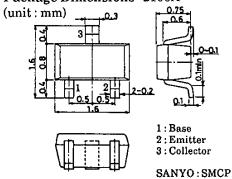
* : Characteristic of the constituent transistor.

Marking: AA

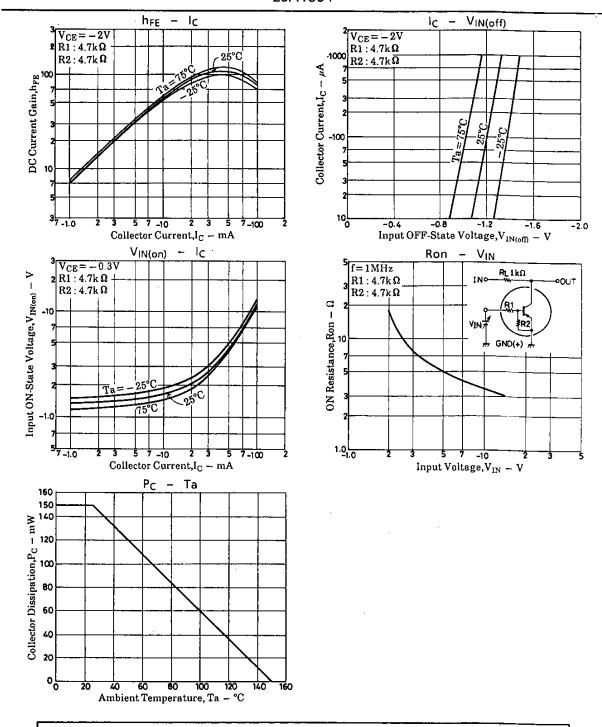
Electrical Connection



Package Dimensions 2106A



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