

2SA1564/2SC4048

PNP/NPN Epitaxial Planar Silicon Transistors

Switching Applications (with Bias Resistance)

Applications

. Switching circuit, inverter circuit, interface circuit, driver circuit

Features

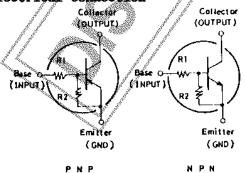
- . On-chip bias resistance: $R_1 = 10 \mathrm{k}\Omega$, $R_2 = 47 \mathrm{k}\Omega$
- . Small-sized package: SPA

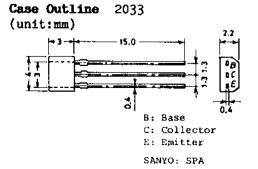
(): 2SA1564

(): ZON 1004	_	À			31878
Absolute Maximum Ratings at Ta:	=25°C	and the second			unit
Collector to Base Voltage	V _{СВО}	11		(+)50	11
Collector to Emitter Voltage	VCEO	A Part of the second		(4)50 /	₩
Emitter to Base Voltage	VEBO	p ^e p ^e		~(-) <i>6/</i> /	/ V
Collector Current	IC	and the state of t	۲.	-) 100 °	mA
Collector Current(Pulse)	I_{CP}) (-)200	mA
Collector Dissipation	PC	-// **\\		∕ /∄00	шW
Junction Temperature	Tj "	//		/ 150	$^{\rm o}{}_{\rm C}$
Storage Temperature	Tstg/		-55 ∕tø	+150	°C
	all all		and the state of t		
	. # . # O	 40x 266000x 6600. 	(B) (B)		

Blectrical Characteristics at Ta=25°C	min	typ	max	unit
Collector Cutoff Current I _{CBO} V _{CB} =(-)40V,I _E =0		(-	.)0.1	$\mu \mathbf{A}$
Collector Cutoff Current I Care Ver 140V, In=0		(-	.)0.5	μA
Emitter Cutoff Current I V V V (-)5V I -= 0	(-)67	(-)88(-	·) 125	μA
DC Current Gain $b_{FF} = V_{CFF}(-)5V_{CF}(-)5mA$	70			
Gain-Bandwidth Product $f_T^{r_E}$ $V_{CE}^{r_E}=(-)/6V, I_{C}=(-)5mA$		250		MHz
		(200)		
Output Capacitance / cob V _{CB} =(-)10V,f=1MHz		3.7		рF
		(5.5)		
C-E Saturation Voltage VCE(sat) Ic=(-)10mA, IB=(-)0.5mA		(-)0.1(-	-)0.3	V
C-B Breakdown Voltage V(BR)CBO/IC=(-)104A,IE=0	(~)50			V
C-E Breakdown Voltage $V_{(BR)CEO}$ $I_{C}=(-)100\mu$ A, $R_{BE}=\infty$	(~)50			V
Input OFF-State Voltage VI(off) VCE=(-)5V,IC=(-)100 #A	(-)0.5	(-)0.7(-	.)0.9	V
	(-)0.7	(-)1.0(-	.)2.0	V
Input ON-State Voltage $V_{I(on)}$ $V_{CE}=(-)0.2V,I_{C}=(-)5mA$ Input Resistance R1	7	10	13	$\mathbf{k}\Omega$
Resistance/Ratio R1/R2	0.193	0.213 0).234	

Electrical Connection





Specifications and information herein are subject to change without notice.

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