

DESCRIPTION

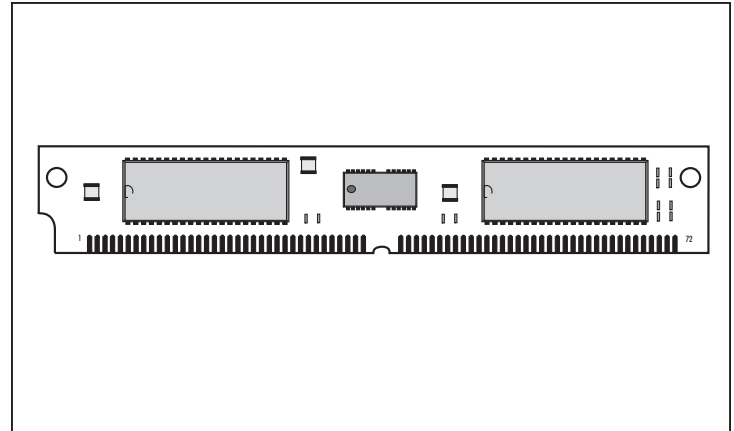
The Accutek AK5362048BW high density memory module is a CMOS dynamic RAM organized in 2048K x 36 bit words. The module consists of four standard 1 Meg x 16 bits DRAMs and two 1 Meg x 4 bit Quad $\overline{\text{CAS}}$ DRAM in plastic SOJ packages. The assembly has 3 DRAMs mounted on each side of a printed circuit board in a 72 pad leadless SIM configuration.

This configuration allows socket-mounting of large quantities of memory in applications where high density and ease of inserting additional memory are important.

The operation of the AK5362048BW is identical to sixteen 1 Meg x 4 plus eight 1 Meg x 1 DRAMs. There are four $\overline{\text{CAS}}$ lines and two $\overline{\text{RAS}}$ lines. On each bank of 1 Meg x 36, independent byte control is accomplished by four $\overline{\text{CAS}}$ lines. Each separate $\overline{\text{CAS}}$ line controls one byte of the 1 Meg x 16 plus a parity bit in the QUAD $\overline{\text{CAS}}$ DRAM.

FEATURES

- 2,097,152 x 36 bit organization
- 72 pad Single In-Line Module
- $\overline{\text{CAS}}$ -before- $\overline{\text{RAS}}$, $\overline{\text{RAS}}$ -only or hidden refresh
- Operating free air temperature 0°C to 70°C
- Single 5 Volt Power Supply
- 1024 Refresh Cycles, 16 mSEC
- Available in Fast Page Mode and EDO



- Power
 - 2.125 Watt Max Active (50nS)
 - 1.975 Watt Max Active (60 nS)
 - 1.825 Watt Max Active (70 nS)
 - 30 mW Max Standby
- Downward compatible with AK5361024W, AK536512W and AK536256W
- Upward compatible with AK5364096W and AK5368192W

ADDITIONAL OPTIONS AVAILABLE

- 1 Meg x 32 version, AK5321024BW
- 1 Meg x 36 version, AK5361024BW
- 2 Meg x 32 version, AK5322048BW

PIN NOMENCLATURE

A ₀ - A ₉	Address Inputs
DQ ₀ - DQ ₃₅	Data In/Data Out
$\overline{\text{W}}$	Write Enable
$\overline{\text{RAS}}_0$ - $\overline{\text{RAS}}_3$	Row Address Strobe
$\overline{\text{CAS}}_0$ - $\overline{\text{CAS}}_3$	Column Address Strobe
PD ₁ - PD ₄	Presence Detect
V _{ss}	Ground
V _{cc}	5v Supply
NC	No Connect

MODULE OPTIONS

Leadless SIM: AK5362048BW

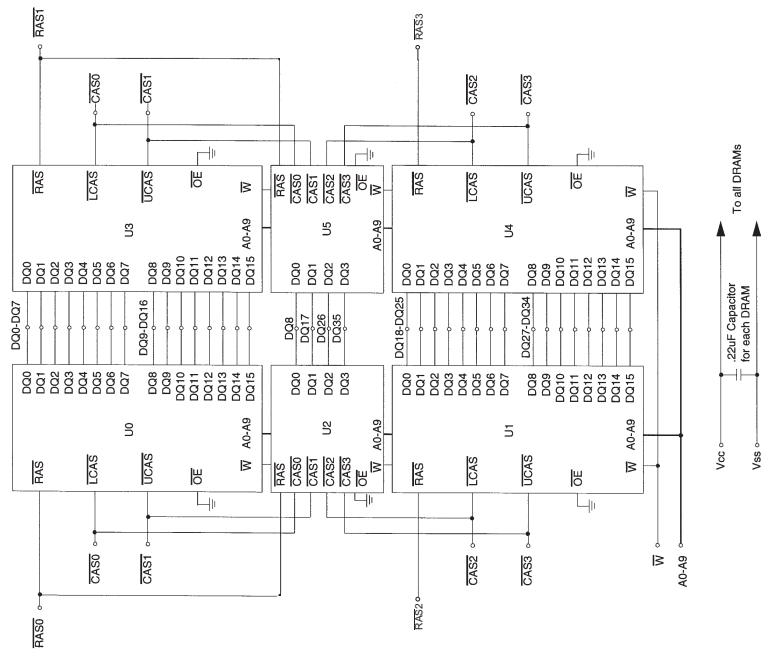
PIN ASSIGNMENT

PIN #	SYMBOL	PIN #	SYMBOL	PIN #	SYMBOL	PIN #	SYMBOL
1	V _{ss}	19	NC	37	DQ17	55	DQ12
2	DQ0	20	DQ4	38	DQ35	56	DQ30
3	DQ18	21	DQ22	39	V _{ss}	57	DQ13
4	DQ1	22	DQ5	40	$\overline{\text{CAS}}_0$	58	DQ31
5	DQ19	23	DQ23	41	$\overline{\text{CAS}}_2$	59	V _{cc}
6	DQ2	24	DQ6	42	$\overline{\text{CAS}}_3$	60	DQ32
7	DQ20	25	DQ24	43	$\overline{\text{CAS}}_1$	61	DQ14
8	DQ3	26	DQ7	44	$\overline{\text{RAS}}_0$	62	DQ33
9	DQ21	27	DQ25	45	$\overline{\text{RAS}}_1$	63	DQ15
10	V _{cc}	28	A7	46	NC	64	DQ34
11	NC	29	NC	47	$\overline{\text{W}}$	65	DQ16
12	A0	30	V _{cc}	48	NC	66	NC
13	A1	31	A8	49	DQ9	67	PD1
14	A2	32	A9	50	DQ27	68	PD1
15	A3	33	$\overline{\text{RAS}}_3$	51	DQ10	69	PD3
16	A4	34	$\overline{\text{RAS}}_2$	52	DQ28	70	PD4
17	A5	35	DQ26	53	DQ11	71	NC
18	A6	36	DQ8	54	DQ29	72	V _{ss}

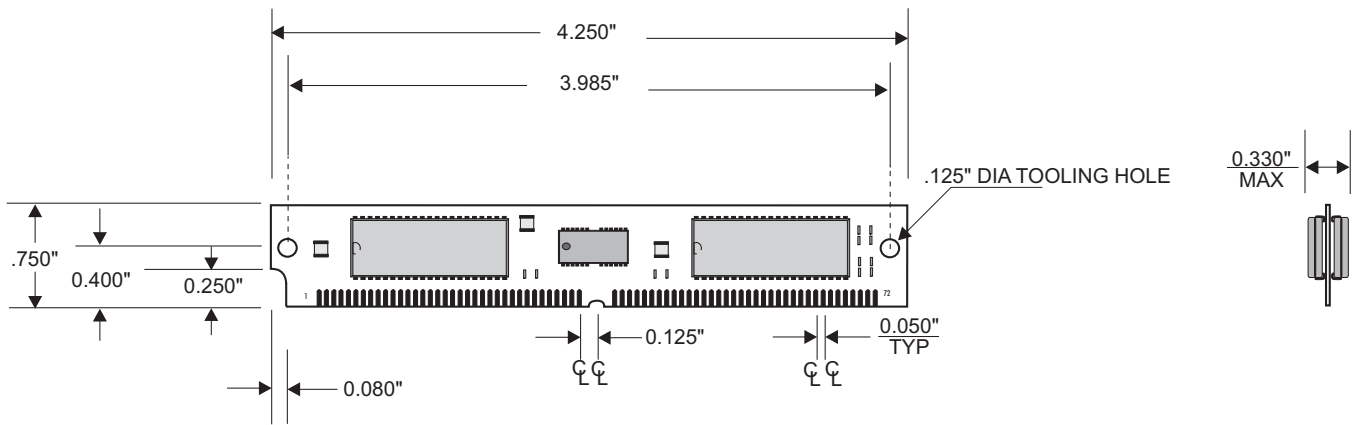
Presence Detect

	-50	-60	-70
PD1	NC	NC	NC
PD2	NC	NC	NC
PD3	V _{ss}	NC	V _{ss}
PD4	V _{ss}	NC	NC

FUNCTIONAL DIAGRAM



MECHANICAL DIMENSIONS



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