

PQ30RV1/PQ30RV11/PQ30RV2/PQ30RV21

Variable Output Low Power-Loss Voltage Regulators

■ Features

- Compact resin full-mold package
- Low power-loss (Dropout voltage : MAX.0.5V)
- Variable output voltage (setting range : 1.5 to 30V)
- Built-in output ON/OFF control function

■ Applications

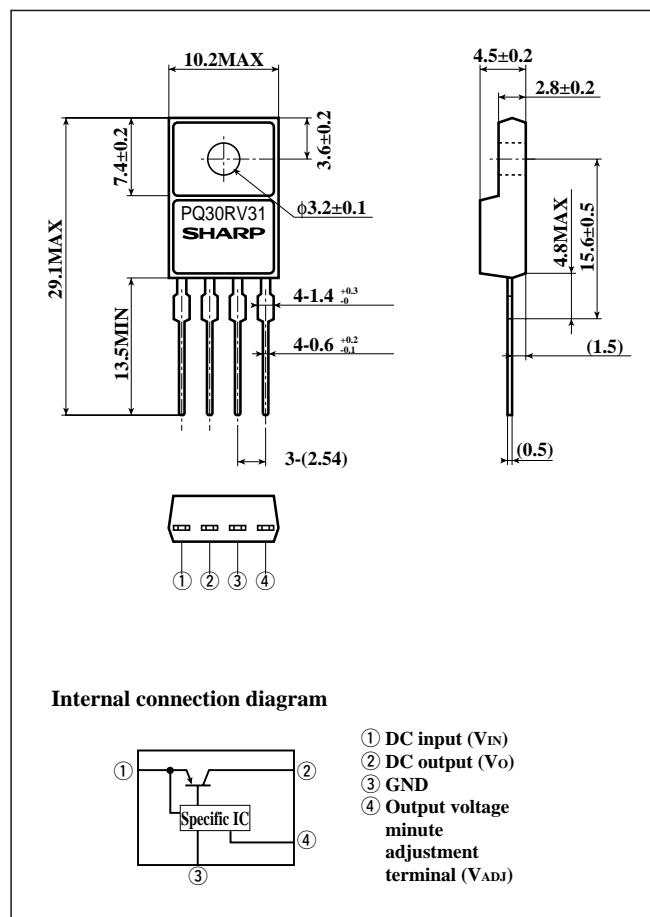
- Power supply for print concentration control of electronic typewriters with display
- Series power supply for motor drives
- Series power supply for VCRs and TVs

■ Model Line-ups

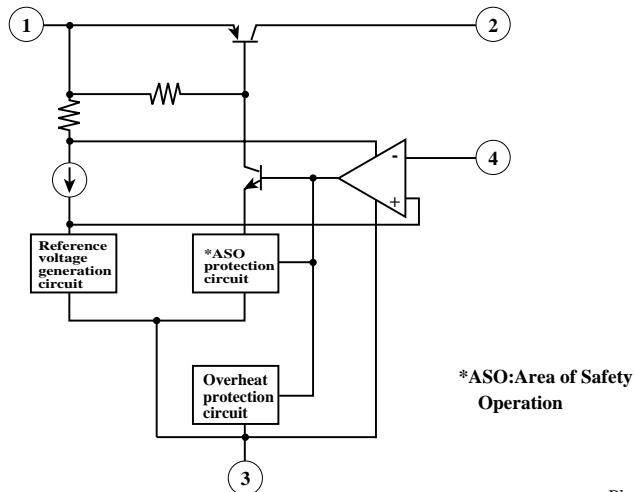
	1A output	2A output
Reference voltage precision : $\pm 4\%$	PQ30RV1	PQ30RV2
Reference voltage precision : $\pm 2\%$	PQ30RV11	PQ30RV21

■ Outline Dimensions

(Unit : mm)



■ Equivalent Circuit Diagram



Please refer to the chapter "Handling Precautions".

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■ Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
*1 Input voltage	V _{IN}	35	V
*1 Output voltage adjustment voltage	V _{ADJ}	7	V
Output current	I _O	1 2	A
Power dissipation (No heat sink)	P _{D1}	1.5	W
Power dissipation (With infinite heat sink)	P _{D2}	15 18	W
*2 Junction temperature	T _j	150	°C
Operating temperature	T _{opr}	-20 to +80	°C
Storage temperature	T _{stg}	-40 to +150	°C
Soldering temperature	T _{sol}	260 (For 10s)	°C

*1 All are open except GND and applicable terminals.

*2 Overheat protection may operate at T_j>=125°C.

■ Electrical Characteristics

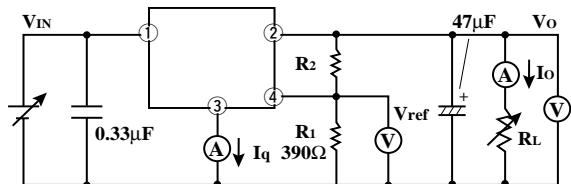
Unless otherwise specified, condition shall be

V_{IN}=15V, V_O=10V, I_O=0.5A, R_L=390Ω (PQ30RV1/PQ30RV11)V_{IN}=15V, V_O=10V, I_O=1.0A, R_L=390Ω (PQ30RV2/PQ30RV21)

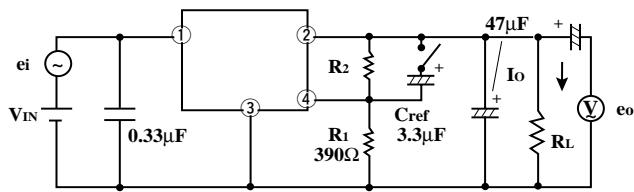
(Ta=25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input voltage	V _{IN}	-	4.5	-	35	V
Output voltage	V _O	R ₂ =94Ω to 8.5kΩ	1.5	-	30	V
		R ₂ =84Ω to 8.7kΩ				
Load regulation	R _{egL}	I _O =5mA to 1A	-	0.3	1.0	%
		I _O =5mA to 2A		0.5	1.0	
Line regulation	R _{egI}	V _{IN} =11 to 28V	-	0.5	2.5	%
Ripple rejection	RR	C _{ref} =0	45	55	-	dB
		C _{ref} =3.3μF		55	65	
Reference voltage	V _{ref}	-	1.20	1.25	1.30	V
		-	1.225	1.25	1.275	
Temperature coefficient of reference voltage	T _{eV_{ref}}	T _j =0 to 125°C	-	±1.0	-	%
Dropout voltage	V _{i·o}	* ₃ , I _O =0.5A	-	-	0.5	V
		* ₃ , I _O =2A				
Quiescent current	I _q	I _O =0	-	-	7	mA

*3 Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

Fig.1 Test Circuit

$$V_O = V_{ref} \times \left(1 + \frac{R_2}{R_1} \right) \approx 1.25 \times \left(1 + \frac{R_2}{R_1} \right)$$

[R₁=390Ω, V_{ref}=1.25V]**Fig.2 Test Circuit of Ripple Rejection**

$$I_O = 0.5A$$

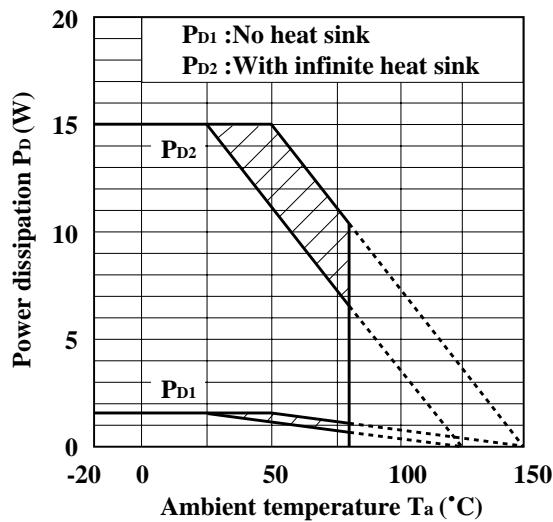
$$f = 120Hz \text{ (sine wave)}$$

$$e_i = 0.5V_{rms}$$

$$RR = 20 \log (e_i/e_o)$$

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Fig.3 Power Dissipation vs. Ambient Temperature (PQ30RV1/PQ30RV11)



Note) Oblique line portion:Overheat protection may operate in this area.

Fig.5 Overcurrent Protection Characteristics (PQ30RV1/PQ30RV11)

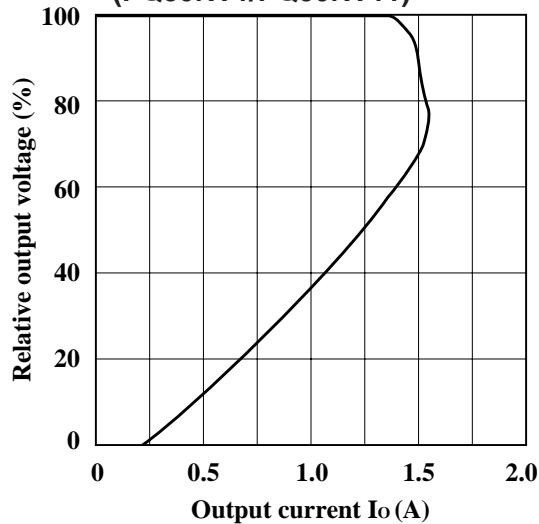


Fig.7 Output Voltage Adjustment Characteristics

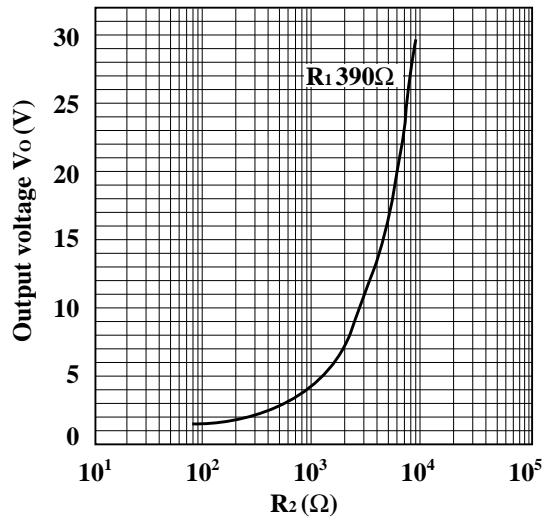
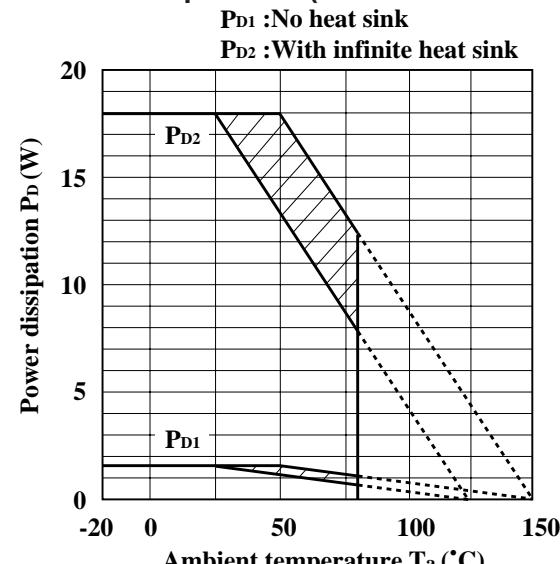


Fig.4 Power Dissipation vs. Ambient Temperature (PQ30RV2/PQ30RV21)



Note) Oblique line portion:Overheat protection may operate in this area.

Fig.6 Overcurrent Protection Characteristics (PQ30RV2/PQ30RV21)

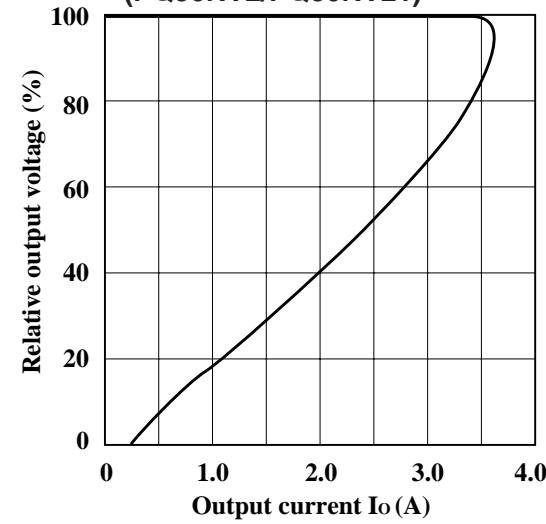


Fig.8 Reference Voltage Deviation vs. Junction Temperature

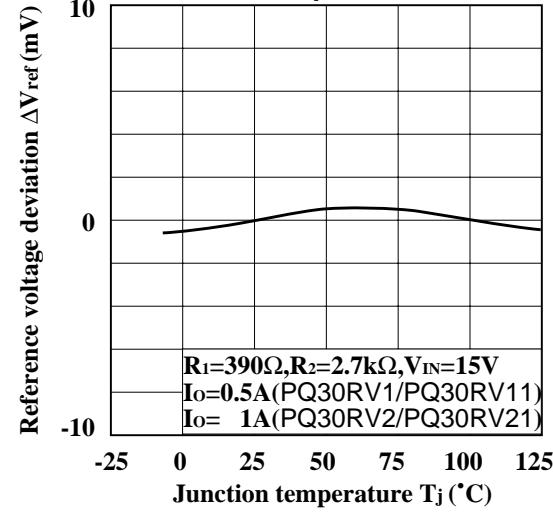


Fig.9 Output Voltage vs. Input Voltage (PQ30RV1/PQ30RV11)

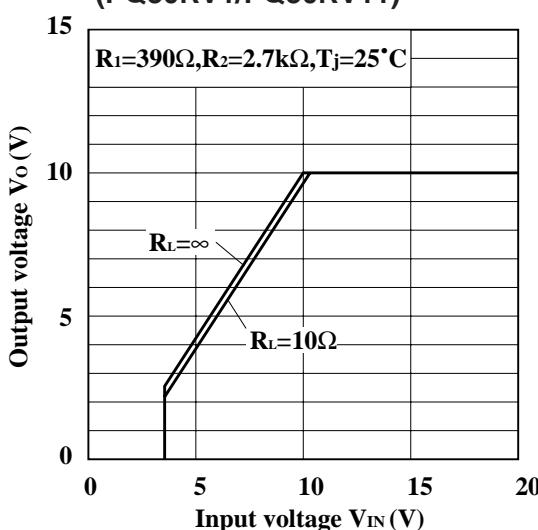


Fig.11 Dropout Voltage vs. Junction Temperature (PQ30RV1/PQ30RV11)

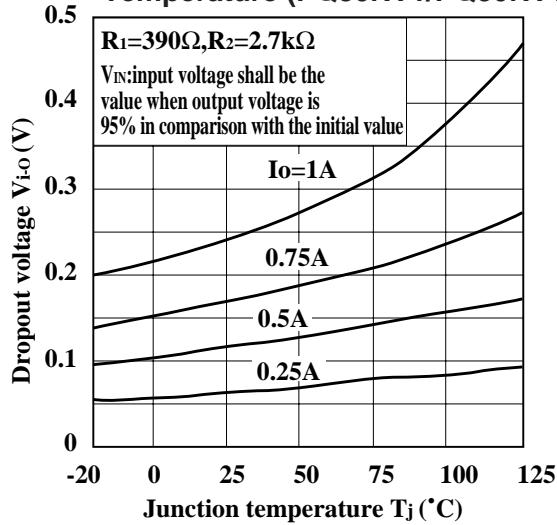


Fig.13 Quiescent Current vs. Junction Temperature

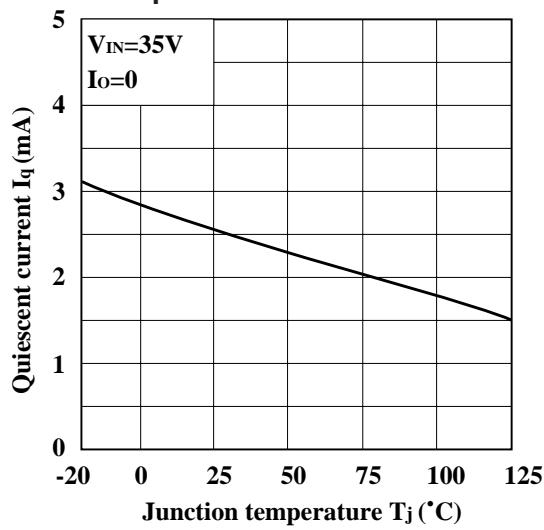


Fig.10 Output Voltage vs. Input Voltage (PQ30RV2/PQ30RV21)

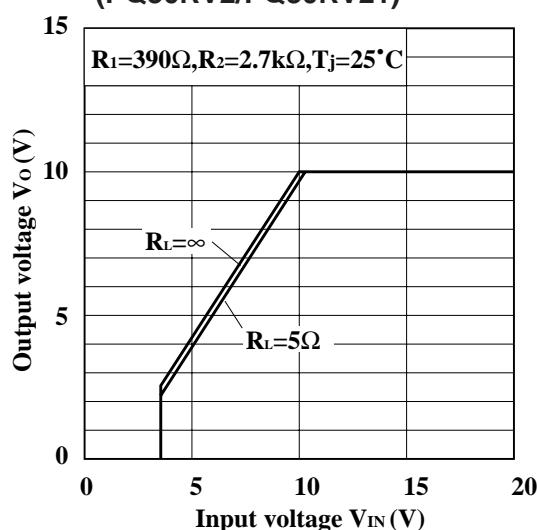


Fig.12 Dropout Voltage vs. Junction Temperature (PQ30RV2/PQ30RV21)

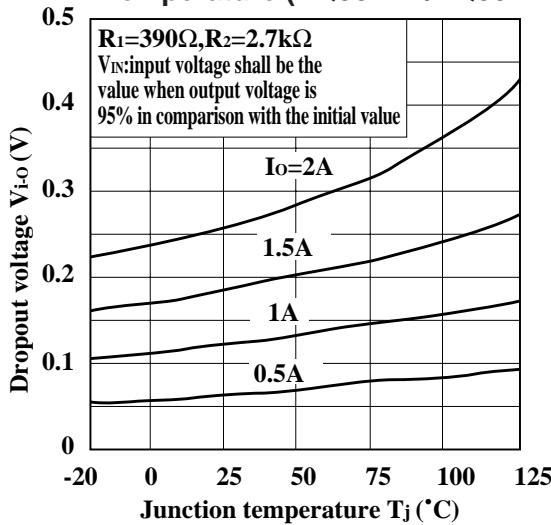
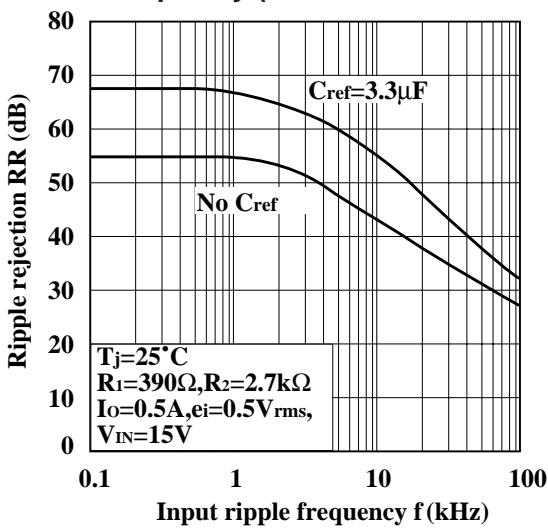


Fig.14 Ripple Rejection vs. Input Ripple Frequency (PQ30RV1/PQ30RV11)



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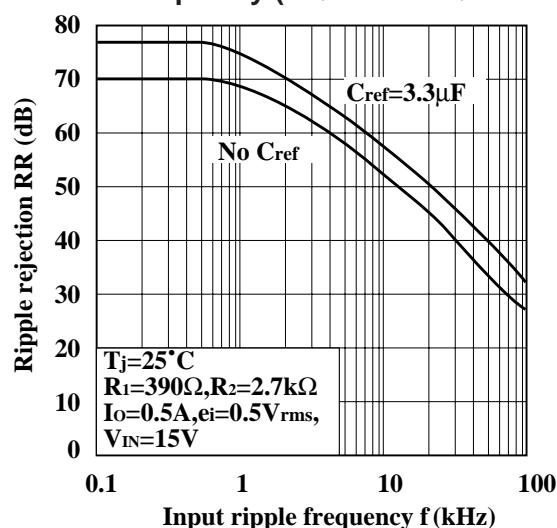
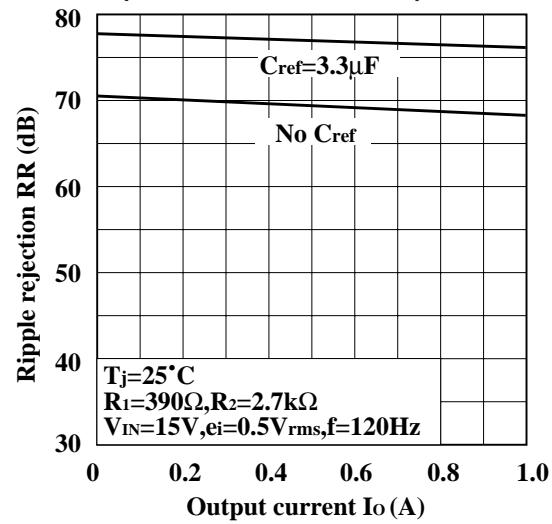
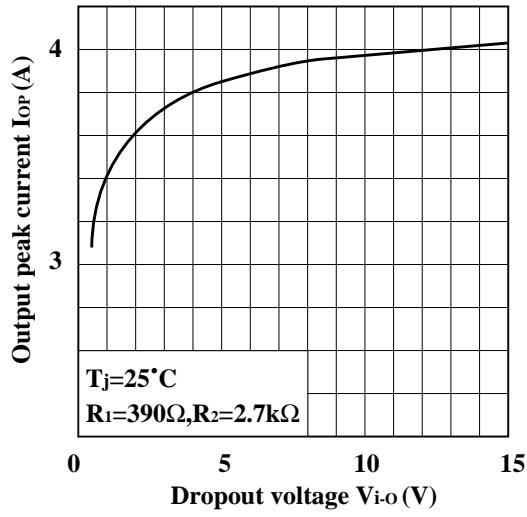
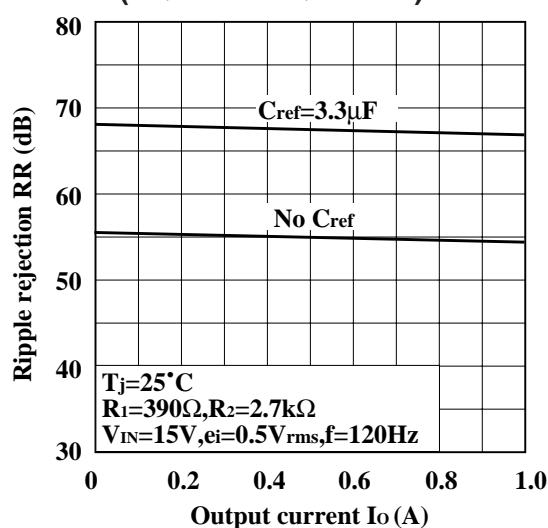
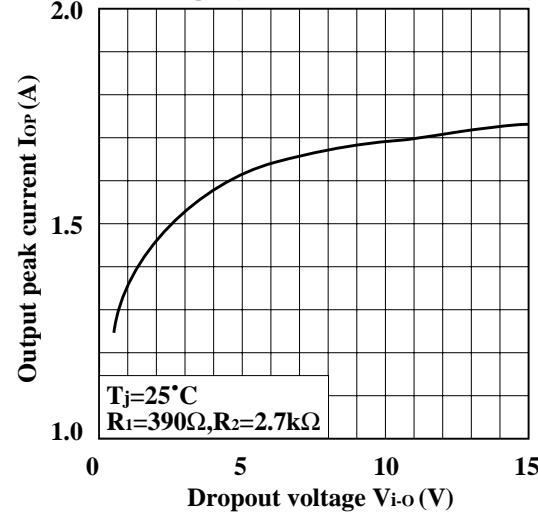
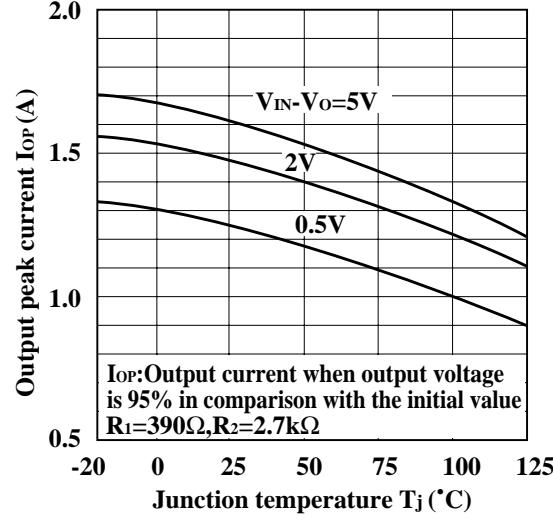
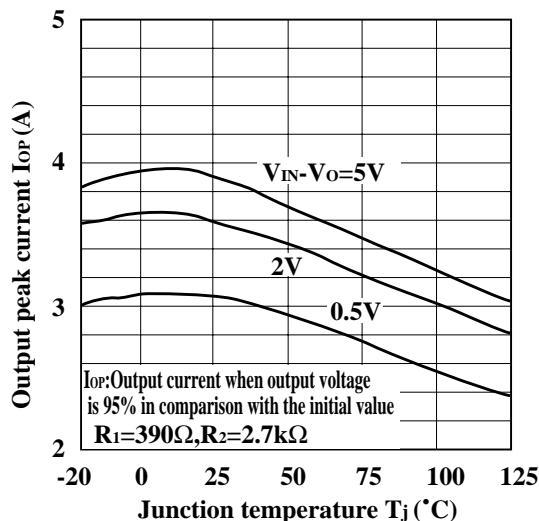
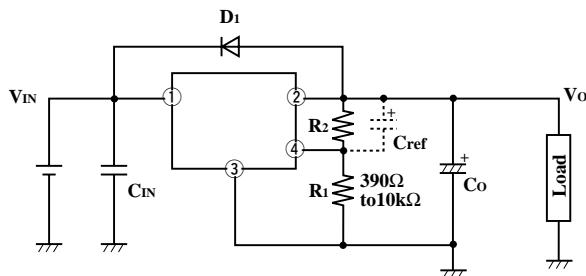
Fig.15 Ripple Rejection vs. Input Ripple Frequency (PQ30RV2/PQ30RV21)**Fig.17 Ripple Rejection vs. Output Current (PQ30RV2/PQ30RV21)****Fig.19 Output Peak Current vs. Dropout Voltage (PQ30RV2/PQ30RV21)****Fig.16 Ripple Rejection vs. Output Current (PQ30RV1/PQ30RV11)****Fig.18 Output Peak Current vs. Dropout Voltage (PQ30RV1/PQ30RV11)****Fig.20 Output Peak Current vs. Junction Temperature (PQ30RV1/PQ30RV11)**

Fig.21 Output Peak Current vs. Junction Temperature (PQ30RV2/PQ30RV21)



■ Standard Connection



D₁ : This device is necessary to protect the element from damage when reverse voltage may be applied to the regulator in case of input short-circuiting.

C_{ref} : This device is necessary when it is required to enhance the ripple rejection or to delay the output start-up time(*1).

(*1)Otherwise, it is not necessary.

(Care must be taken since C_{ref} may raise the gain, facilitating oscillation.)

(*1)The output start-up time is proportional to C_{ref} \times R₂.

C_{IN}, C_O: Be sure to mount the devices C_{IN} and C_O as close to the device terminal as possible so as to prevent oscillation.

The standard specification of C_{IN} and C_O is 0.33μF and 47μF, respectively. However, adjust them as necessary after checking.

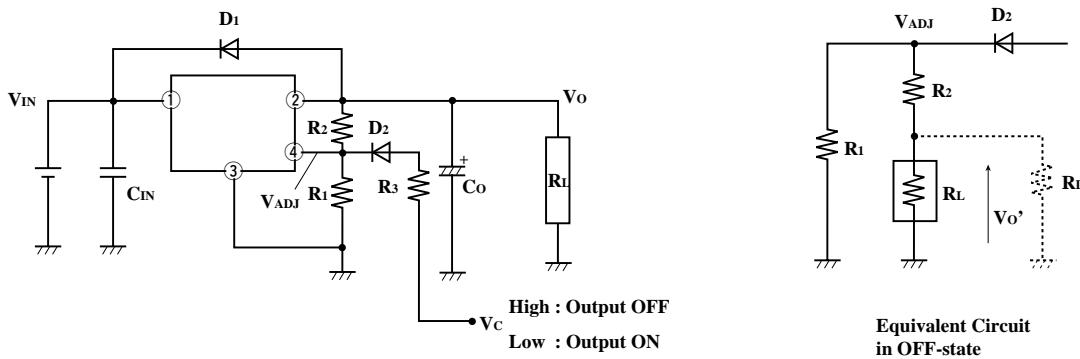
R₁, R₂ : These devices are necessary to set the output voltage. The output voltage V_O is given by the following formula:

$$V_O = V_{ref} \times (1 + R_2 / R_1)$$

(V_{ref} is 1.25V TYP)

The standard value of R₁ is 390Ω. But value up 10kΩ does not cause any trouble.

■ ON/OFF Operation



- ON/OFF operation is available by mounting externally D₂ and R₃.
- When V_{ADJ} is forcibly raised above V_{ref} (1.25V TYP) by applying the external signal, the output is turned off (pass transistor of regulator is turned off). When the output is OFF, V_{ADJ} must be higher than V_{ref} MAX., and at the same time must be lower than maximum rating 7V.

In OFF-state, the load current flows to R_L from V_{ADJ} through R₂. Therefore the value of R₂ must be as high as possible.

• $V_o' = V_{ADJ} \times R_L / (R_L + R_2)$

occurs at the load. OFF-state equivalent circuit R₁ up to 10Ω is allowed. Select as high value of R_L and R₂ as possible in this range. In some case, as output voltage is getting lower (V_O<1V), impedance of load resistance rises. In such condition, it is sometime impossible to obtain the minimum value of V_{O'}. So add the dummy resistance indicated by R_D in the figure to the circuit parallel to the load.

■ An Example of ON/OFF Circuit Using the 1-chip Microcomputer Output Port (PQ30RV1)

<Specification>

Output port of microcomputer

$$V_{OH}(\text{max}) = 0.5 \text{ V}$$

$$V_{OH}(\text{min}) = 2.4 \text{ V } (I_{OH}=0.2\text{mA})$$

MAX. rating of I_{OH}=0.5mA

Output should be set as follows.

$$15.6 \text{ V } R_L=52\Omega \text{ (I}_o=0.3\text{A)}$$

From $V_o=1.25 \text{ V } (1+R_2/R_1)$ we get $V_o=15.6 \text{ V}$.

$$R_2/R_1=11.48$$

Assuming that V_{F(max)}=0.8V for D₂ in case of V_{OH(min)}=2.4V, we get V_{ADJ}=V_{OH(min)}-V_{F(max)}=2.4V-0.8V=1.6V. From V_{ref(max)}=1.3V we get R₃=0Ω

If R₁=10kΩ, we get R₂=11.48 × R₁=114.8kΩ and I_{OH} as follows, ignoring R_L (52Ω) :

$$I_{OH}=1.6V \times (R_1+R_2)/R_1 \times R_2$$

$$=1.6V \times (10k\Omega+114.8k\Omega)/10k\Omega \times 114.8k\Omega=0.17\text{mA}$$

Hence, I_{OH}<0.2mA. Therefore V_{OH(min)} is ensured.

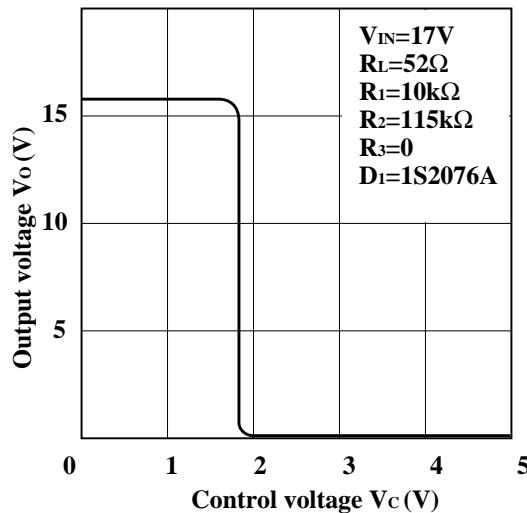
Next, assuming that V_{F(min)}=0.5V for D₂ in case of V_{OH(max)}, we get:

$$I_{OH}=(5V-0.5V)/(R_1+R_2) \times R_1 \times R_2=0.49\text{mA}$$

which is less than the rating.

Figure 1 shows the V_O-V_C characteristics when R₁=10kΩ, R₂=115kΩ, R₃=0Ω, V_{IN}=17V, R_L=52Ω, and D₁=1S2076A (Hitachi).

Output Voltage vs. Control Voltage (PQ30RV1)

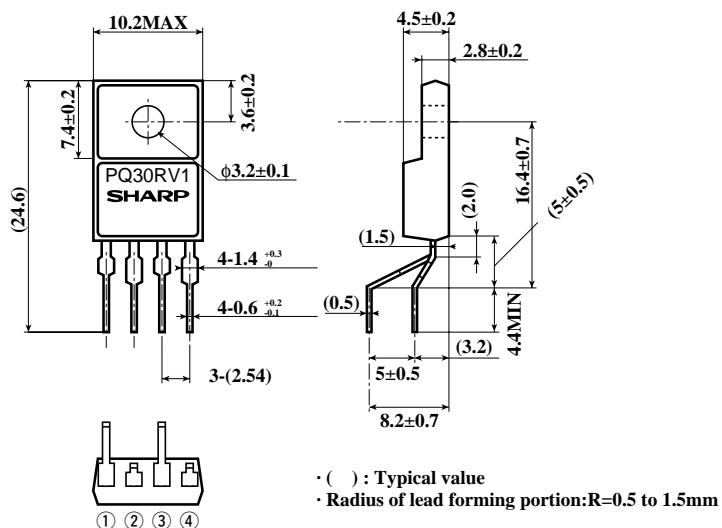


■ Model Line-ups for Lead Forming Type

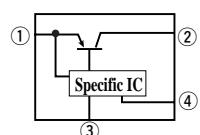
Output voltage	5V output	2A output
Output voltage precision: $\pm 2.5\%$	PQ30RV1B	PQ30RV2B

■ Outline Dimensions (PQ30RV1B/PQ30RV2B)

(Unit : mm)



Internal connection diagram



- ① DC input (V_{IN})
- ② DC output (V_o)
- ③ GND
- ④ Output voltage minute adjustment terminal (V_{ADJ})

Note) The value of absolute maximum ratings and electrical characteristics is same as ones of PQ30RV1/2 series.