

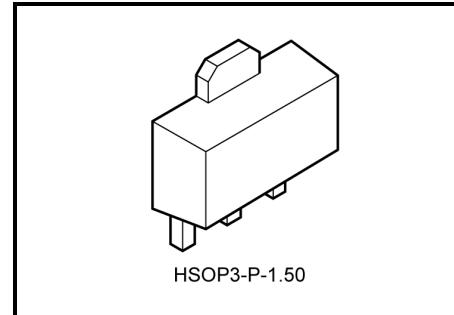
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

**TA48L018F, TA48L02F, TA48L025F,  
TA48L03F, TA48L033F, TA48L05F**

1.8 V, 2 V, 2.5 V, 3 V, 3.3 V, 5 V

Three-Terminal Low Dropout Voltage Regulator with Output Current of 0.15 A

The TA48L\*\*F series consists of fixed-positive-output, low-dropout regulators with an output current of 1 A (max) that utilize V-PNP transistors for the output stage. In response to the need for low-voltage and low-power dissipation devices for use in consumer electronics and industrial appliances, the series offers devices with low output voltages: 1.8 V, 2 V, 2.5 V, 3 V, 3.3 V, 5 V.

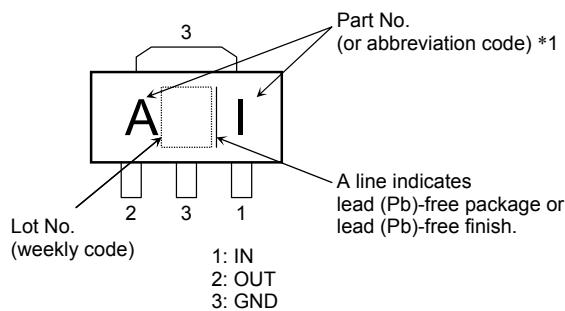


HSOP3-P-1.50

Weight: 0.05 g (typ.)

**Features**

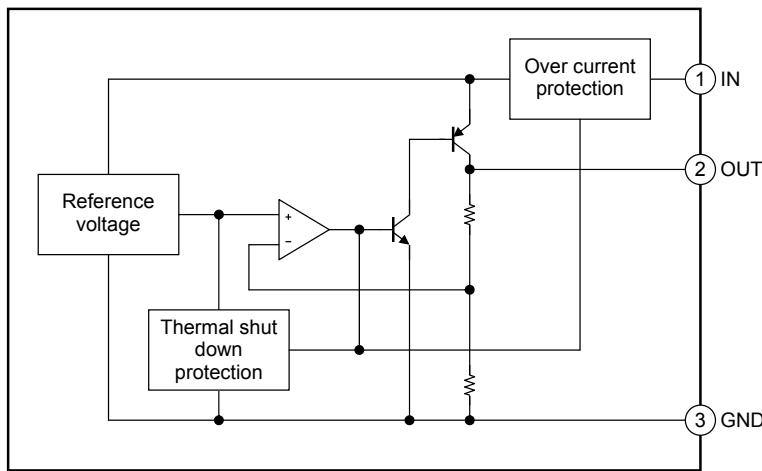
- Maximum output current: 0.15 A
- Output voltage accuracy:  $V_{OUT} \pm 3\% (@T_j = 25^\circ C)$
- Low standby current: 400  $\mu A$  (typ.) ( $@I_{OUT} = 0 A$ )
- Low-dropout voltage:  $V_D = 0.5 V$  (max) ( $@I_{OUT} = 100 mA$ )
- Protection function: overheat/overcurrent
- Package type: PW-MINI (SOT-89) package

**Pin Assignment/Marking**

	Part No. (or abbreviation code)	Part No.
*1	AI	TA48L018F
	BI	TA48L02F
	CI	TA48L025F
	DI	TA48L03F
	EI	TA48L033F
	FI	TA48L05F

**How to Order**

Product No.	Package	Packing Type and Unit for Orders
TA48L**F	PW-MINI (SOT-89) Surface-mount package	On cut tape (TE12L): 100/tape section
TA48L**F (TE12L)		Embossed tape: 1000 pcs/tape

**Block Diagram****Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Input voltage	$V_{IN}$	16	V
Output current	$I_{OUT}$	0.15	A
Operating temperature	$T_{opr}$	-40~85	$^\circ\text{C}$
Junction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55~150	$^\circ\text{C}$
Power dissipation	$P_D$	0.5	W
Thermal resistance (Junction to ambient)	$R_{th(j-a)}$	250	$^\circ\text{C}/\text{W}$

Note 1: External current and voltage (including negative voltage) should not be applied to pins not specified.

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

**Protection Function (reference)**

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Thermal shutdown	$T_{SD} (T_j)$	—	—	160	—	$^\circ\text{C}$
Peak circuit current	$I_{PEAK}$	$V_{IN} = V_{OUT} + 2 \text{ V}, T_j = 25^\circ\text{C}$	—	0.27	—	A
Short circuit current	$I_{SC}$	$V_{IN} = V_{OUT} + 2 \text{ V}, T_j = 25^\circ\text{C}$	—	0.27	—	A

Note 3: The maximum ratings should not be exceeded when the IC is actually used.

**TA48L018F****Electrical Characteristics**(C<sub>IN</sub> = 0.33 µF, C<sub>OUT</sub> = 3.3 µF, T<sub>j</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Conditions	Min	Typ.	Max	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 3.8 V, I <sub>OUT</sub> = 40 mA	1.746	1.8	1.854	V
		2.8 V ≤ V <sub>IN</sub> ≤ 12 V, 5 mA ≤ I <sub>OUT</sub> ≤ 100 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	1.71	1.8	1.89	
Line regulation	Reg · line	2.8 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA	—	2	20	mV
Load regulation	Reg · load	V <sub>IN</sub> = 3.8 V, 5 mA ≤ I <sub>OUT</sub> ≤ 150 mA	—	18	40	mV
Quiescent current	I <sub>B</sub>	2.8 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 0 A	—	0.4	0.8	mA
		2.8 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 100 mA	—	1	5	
Starting quiescent current	I <sub>Bstart</sub>	V <sub>IN</sub> = 2.1 V, I <sub>OUT</sub> = 0 A	—	0.5	1.5	mA
		V <sub>IN</sub> = 2.1 V, I <sub>OUT</sub> = 100 mA	—	5	20	
Output noise voltage	V <sub>NO</sub>	V <sub>IN</sub> = 3.8 V, I <sub>OUT</sub> = 40 mA, 10 Hz ≤ f ≤ 100 kHz	—	45	—	µVrms
Ripple rejection	R.R.	2.8 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA, f = 120 Hz	54	72	—	dB
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 40 mA	—	0.28	0.4	V
		I <sub>OUT</sub> = 100 mA	—	0.32	0.5	
Average temperature coefficient of output voltage	T <sub>CVO</sub>	V <sub>IN</sub> = 3.8 V, I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	—	0.3	—	mV/°C

**TA48L02F****Electrical Characteristics**(C<sub>IN</sub> = 0.33 µF, C<sub>OUT</sub> = 3.3 µF, T<sub>j</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Conditions	Min	Typ.	Max	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 4.0 V, I <sub>OUT</sub> = 40 mA	1.94	2.0	2.06	V
		3.0 V ≤ V <sub>IN</sub> ≤ 12 V, 5 mA ≤ I <sub>OUT</sub> ≤ 100 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	1.90	2.0	2.10	
Line regulation	Reg · line	3.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA	—	2	20	mV
Load regulation	Reg · load	V <sub>IN</sub> = 4.0 V, 5 mA ≤ I <sub>OUT</sub> ≤ 150 mA	—	18	40	mV
Quiescent current	I <sub>B</sub>	3.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 0 A	—	0.4	0.8	mA
		3.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 100 mA	—	1	5	
Starting quiescent current	I <sub>Bstart</sub>	V <sub>IN</sub> = 2.1 V, I <sub>OUT</sub> = 0 A	—	0.5	1.5	mA
		V <sub>IN</sub> = 2.1 V, I <sub>OUT</sub> = 100 mA	—	5	20	
Output noise voltage	V <sub>NO</sub>	V <sub>IN</sub> = 4.0 V, I <sub>OUT</sub> = 40 mA, 10 Hz ≤ f ≤ 100 kHz	—	55	—	µVrms
Ripple rejection	R.R.	3.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA, f = 120 Hz	52	70	—	dB
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 40 mA	—	0.2	0.35	V
		I <sub>OUT</sub> = 100 mA	—	0.3	0.5	
Average temperature coefficient of output voltage	T <sub>CVO</sub>	V <sub>IN</sub> = 4.0 V, I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	—	0.35	—	mV/°C

**TA48L025F****Electrical Characteristics**(C<sub>IN</sub> = 0.33 µF, C<sub>OUT</sub> = 3.3 µF, T<sub>j</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Conditions	Min	Typ.	Max	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 4.5 V, I <sub>OUT</sub> = 40 mA	2.425	2.5	2.575	V
		3.5 V ≤ V <sub>IN</sub> ≤ 12 V, 5 mA ≤ I <sub>OUT</sub> ≤ 100 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	2.375	2.5	2.625	
Line regulation	Reg · line	3.5 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA	—	2	20	mV
Load regulation	Reg · load	V <sub>IN</sub> = 4.5 V, 5 mA ≤ I <sub>OUT</sub> ≤ 150 mA	—	18	40	mV
Quiescent current	I <sub>B</sub>	3.5 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 0 A	—	0.4	0.8	mA
		3.5 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 100 mA	—	1	5	
Starting quiescent current	I <sub>Bstart</sub>	V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = 0 A	—	0.5	1.5	mA
		V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = 100 mA	—	7	20	
Output noise voltage	V <sub>NO</sub>	V <sub>IN</sub> = 4.5 V, I <sub>OUT</sub> = 40 mA, 10 Hz ≤ f ≤ 100 kHz	—	65	—	µVrms
Ripple rejection	R.R.	3.5 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA, f = 120 Hz	52	70	—	dB
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 40 mA	—	0.16	0.35	V
		I <sub>OUT</sub> = 100 mA	—	0.27	0.5	
Average temperature coefficient of output voltage	T <sub>CVO</sub>	V <sub>IN</sub> = 4.5 V, I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	—	0.45	—	mV/°C

**TA48L03F****Electrical Characteristics**(C<sub>IN</sub> = 0.33 µF, C<sub>OUT</sub> = 3.3 µF, T<sub>j</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Conditions	Min	Typ.	Max	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 5.0 V, I <sub>OUT</sub> = 40 mA	2.91	3.0	3.09	V
		4.0 V ≤ V <sub>IN</sub> ≤ 12 V, 5 mA ≤ I <sub>OUT</sub> ≤ 100 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	2.85	3.0	3.15	
Line regulation	Reg · line	4.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA	—	2	20	mV
Load regulation	Reg · load	V <sub>IN</sub> = 5.0 V, 5 mA ≤ I <sub>OUT</sub> ≤ 150 mA	—	18	40	mV
Quiescent current	I <sub>B</sub>	4.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 0 A	—	0.4	0.8	mA
		4.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 100 mA	—	1	5	
Starting quiescent current	I <sub>Bstart</sub>	V <sub>IN</sub> = 2.8 V, I <sub>OUT</sub> = 0 A	—	0.5	1.5	mA
		V <sub>IN</sub> = 2.8 V, I <sub>OUT</sub> = 100 mA	—	7	20	
Output noise voltage	V <sub>NO</sub>	V <sub>IN</sub> = 5.0 V, I <sub>OUT</sub> = 40 mA, 10 Hz ≤ f ≤ 100 kHz	—	80	—	µVrms
Ripple rejection	R.R.	4.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA, f = 120 Hz	50	68	—	dB
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 40 mA	—	0.16	0.35	V
		I <sub>OUT</sub> = 100 mA	—	0.27	0.5	
Average temperature coefficient of output voltage	T <sub>CVO</sub>	V <sub>IN</sub> = 5 V, I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	—	0.5	—	mV/°C

**TA48L033F****Electrical Characteristics**(C<sub>IN</sub> = 0.33 µF, C<sub>OUT</sub> = 3.3 µF, T<sub>j</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Conditions	Min	Typ.	Max	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 5.3 V, I <sub>OUT</sub> = 40 mA	3.2	3.3	3.4	V
		4.3 V ≤ V <sub>IN</sub> ≤ 12 V, 5 mA ≤ I <sub>OUT</sub> ≤ 100 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	3.135	3.3	3.465	
Line regulation	Reg · line	4.3 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA	—	2	20	mV
Load regulation	Reg · load	V <sub>IN</sub> = 5.3 V, 5 mA ≤ I <sub>OUT</sub> ≤ 150 mA	—	18	40	mV
Quiescent current	I <sub>B</sub>	4.3 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 0 A	—	0.4	0.8	mA
		4.3 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 100 mA	—	1	5	
Starting quiescent current	I <sub>Bstart</sub>	V <sub>IN</sub> = 3.0 V, I <sub>OUT</sub> = 0 A	—	0.5	1.5	mA
		V <sub>IN</sub> = 3.0 V, I <sub>OUT</sub> = 100 mA	—	7	20	
Output noise voltage	V <sub>NO</sub>	V <sub>IN</sub> = 5.3 V, I <sub>OUT</sub> = 40 mA, 10 Hz ≤ f ≤ 100 kHz	—	85	—	µVrms
Ripple rejection	R.R.	4.3 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA, f = 120 Hz	50	68	—	dB
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 40 mA	—	0.16	0.35	V
		I <sub>OUT</sub> = 100 mA	—	0.27	0.5	
Average temperature coefficient of output voltage	T <sub>CVO</sub>	V <sub>IN</sub> = 5.3 V, I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	—	0.55	—	mV/°C

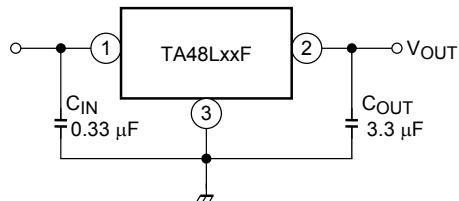
**TA48L05F****Electrical Characteristics**(C<sub>IN</sub> = 0.33 µF, C<sub>OUT</sub> = 3.3 µF, T<sub>j</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Conditions	Min	Typ.	Max	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 7.0 V, I <sub>OUT</sub> = 40 mA	4.85	5.0	5.15	V
		6.0 V ≤ V <sub>IN</sub> ≤ 12 V, 5 mA ≤ I <sub>OUT</sub> ≤ 100 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	4.75	5.0	5.25	
Line regulation	Reg · line	6.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA	—	2	20	mV
Load regulation	Reg · load	V <sub>IN</sub> = 7.0 V, 5 mA ≤ I <sub>OUT</sub> ≤ 150 mA	—	18	45	mV
Quiescent current	I <sub>B</sub>	6.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 0 A	—	0.4	0.8	mA
		6.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 100 mA	—	1	5	
Starting quiescent current	I <sub>Bstart</sub>	V <sub>IN</sub> = 4.5 V, I <sub>OUT</sub> = 0 A	—	0.5	1.5	mA
		V <sub>IN</sub> = 4.5 V, I <sub>OUT</sub> = 100 mA	—	7	20	
Output noise voltage	V <sub>NO</sub>	V <sub>IN</sub> = 7.0 V, I <sub>OUT</sub> = 40 mA, 10 Hz ≤ f ≤ 100 kHz	—	135	—	µVrms
Ripple rejection	R.R.	6.0 V ≤ V <sub>IN</sub> ≤ 12 V, I <sub>OUT</sub> = 40 mA, f = 120 Hz	50	64	—	dB
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 40 mA	—	0.16	0.35	V
		I <sub>OUT</sub> = 100 mA	—	0.27	0.5	
Average temperature coefficient of output voltage	T <sub>CVO</sub>	V <sub>IN</sub> = 7.0 V, I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	—	0.85	—	mV/°C

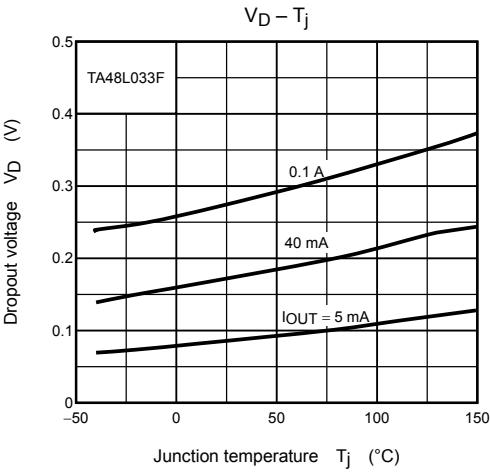
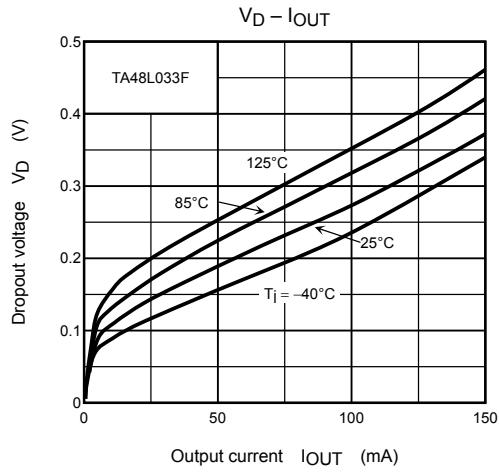
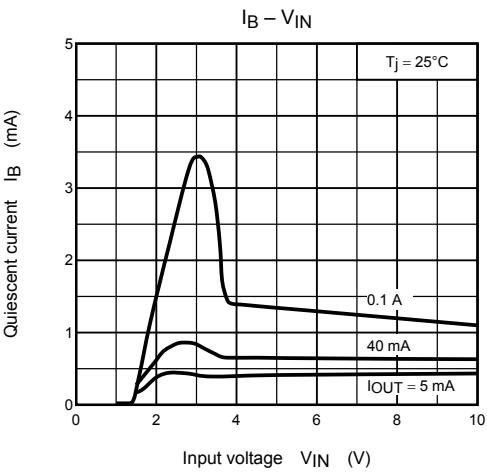
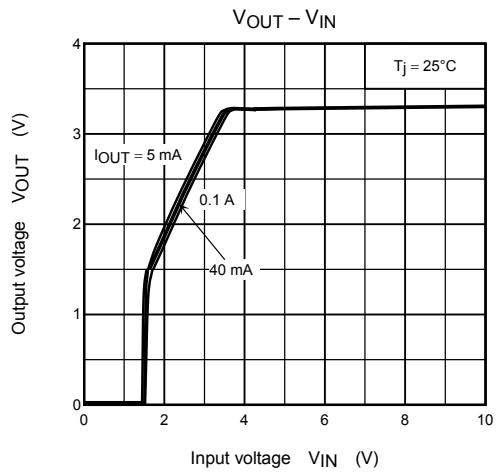
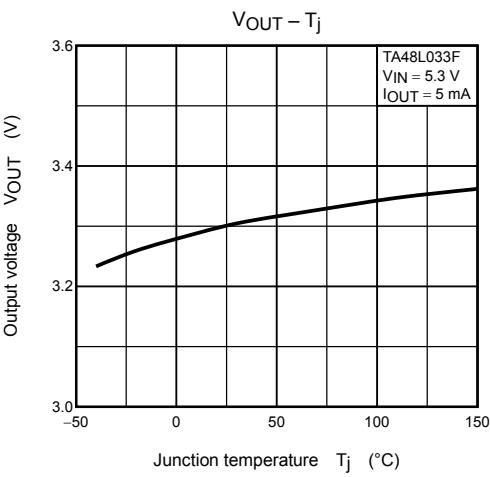
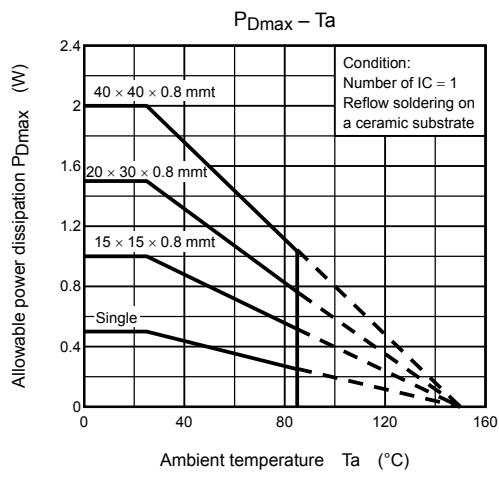
## Electrical Characteristics for All Products

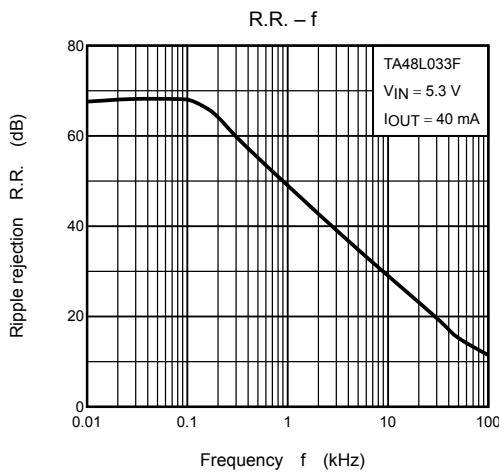
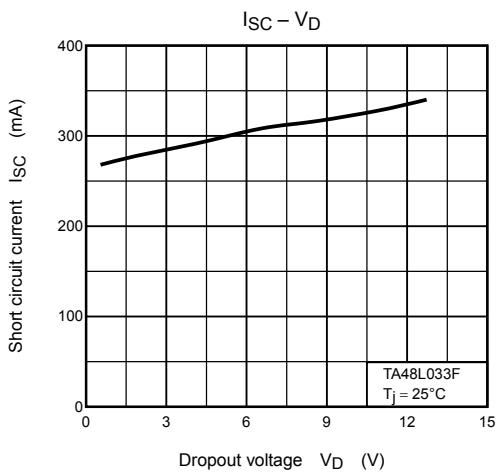
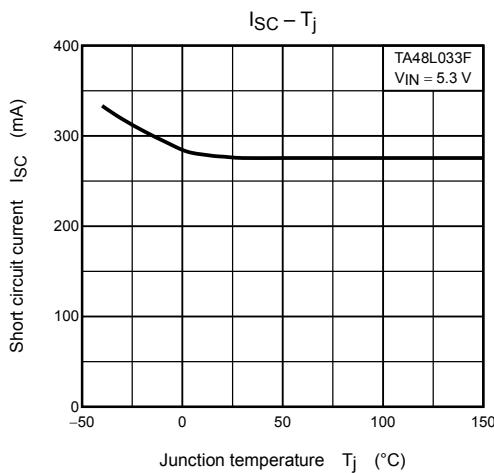
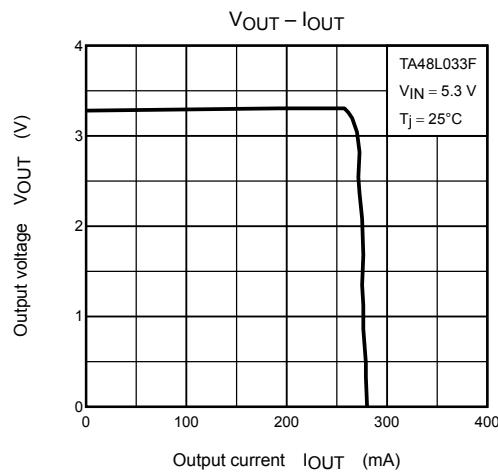
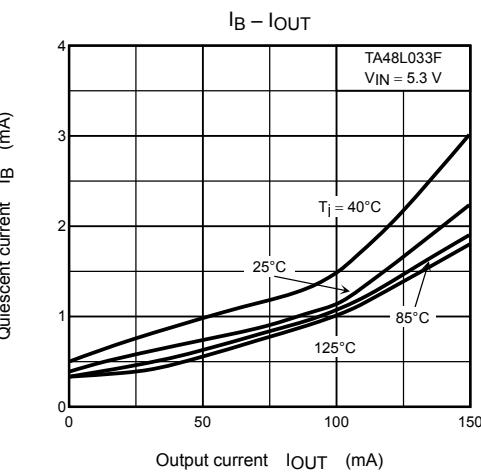
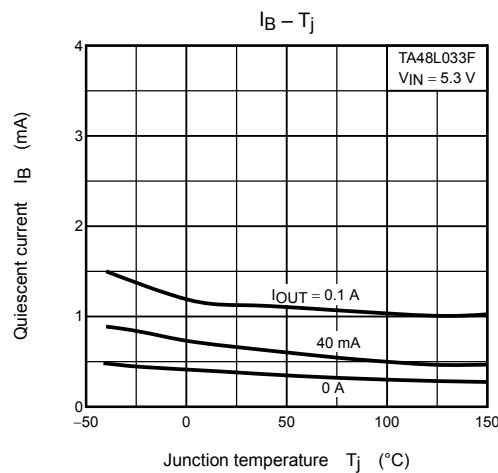
Generally, the characteristics of power supply ICs change according to temperature fluctuations. The specification  $T_j = 25^\circ\text{C}$  is based on a state where temperature increase has no effect (assuming no fluctuation in the characteristics) as ascertained by pulse tests.

## Standard Application Circuit



Be sure to connect a capacitor near the input terminal and output terminal between both terminals and GND. The capacitances should be determined experimentally. In particular, adequate investigation should be made so that there is no problem even in high or low temperatures.

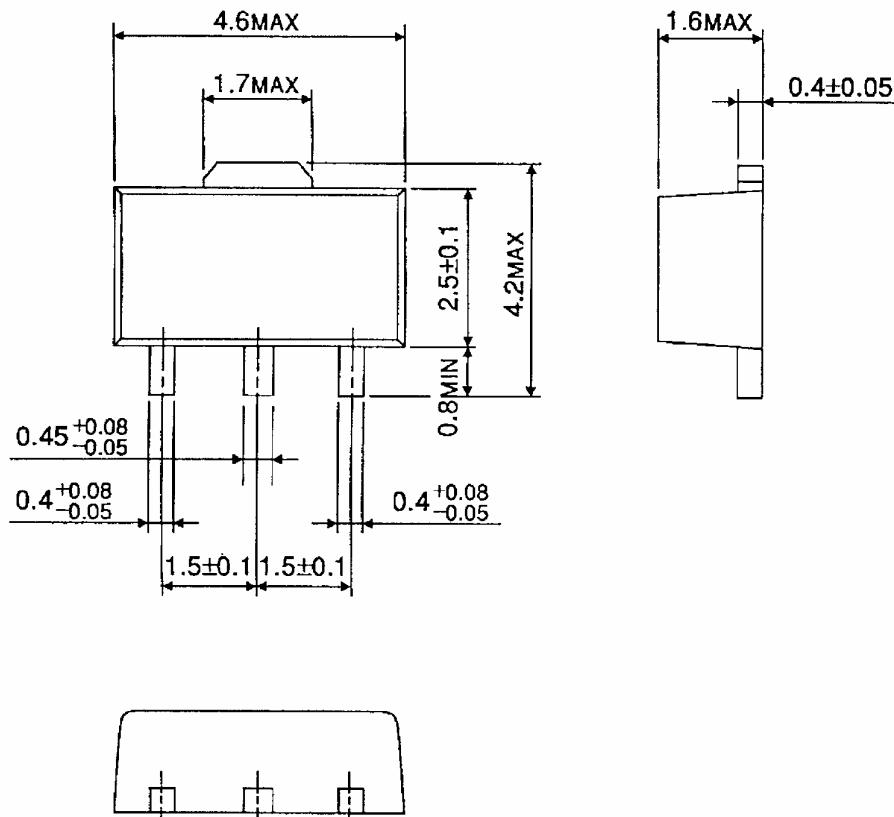




**Package Dimensions**

HSOP3-P-1.50

Unit : mm



Weight: 0.05 g (typ.)

## RESTRICTIONS ON PRODUCT USE

20070701-EN

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.  
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