

**GENERAL DESCRIPTION**

This Series of fixed-voltage monolithic integrated circuit voltage regulators is designed for a wide range of applications. These applications include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power-pass elements to make high-current voltage regulators. One of these regulators can deliver up to 100 mA of output current. The internal limiting and thermal shutdown features of these regulators make them essentially immune to overload. When used as a replacement for a zener diode-resistor combination, an effective improvement in output impedance can be obtained together with lower-bias current.

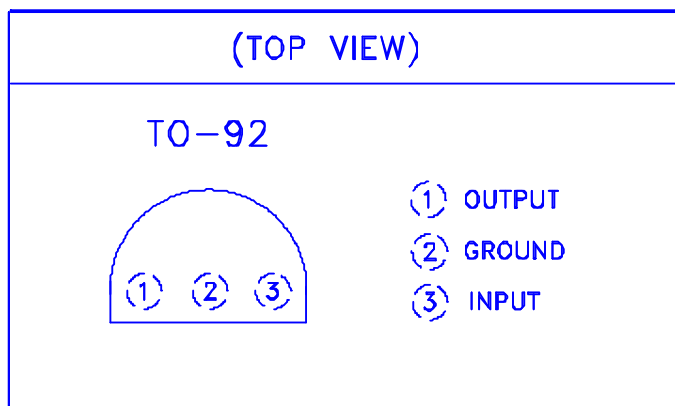
**FEATURES**

- 3-terminal regulators
- Output current up to 100 mA
- No external component
- Internal thermal overload protection
- Internal short-circuit current limiting

**APPLICATIONS**

- Linear regulator
- Instrumentation
- Switching power supplies
- PCs, Industrial equipment

**PIN CONFIGURATIONS**



**DEVICE SELECTION GUIDE**

<b>Device</b>	L78L18N
<b>Package</b>	TO-92
<b>Marking</b>	78L18

**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	VALUE
Input Voltage - $V_I$	35V
Continuous total power dissipation	(See Note 1)
Storage Temperature Range - $T_{STG}$	-55 to +150 °C
Junction Temperature - $T_J$	125 °C
Lead Temperature (Soldering, 10 Seconds) - $T_L$	260 °C

Note1: To avoid exceeding the design maximum virtual junction temperature, three ratings should not be exceeded. Due to variations in individual device electrical characteristics and thermal resistance, the built-in thermal overload protection may be activated at power levels slightly above or below the rated dissipation.

**DISSIPATION RATING TABLE 1 – FREE-AIR TEMPERATURE**

PACKAGE	$T_A \leq 25^\circ\text{C}$ POWER RATING	DERATING FACTOR	DERATING ABOVE $T_A$	$T_A = 70^\circ\text{C}$ POWER RATING
TO-92	650 mW	6.2 mW/°C	25 °C	350 mW

† The TO-92 package dissipation rating is based on thermal resistance  $\theta_{JA}$  measured in still air with the device mounted in an Augat socket. The bottom of the package is 10mm (0.375 in) above the stock.

**DISSIPATION RATING TABLE 2 – CASE TEMPERATURE**

PACKAGE	$T_A \leq 25^\circ\text{C}$ POWER RATING	DERATING FACTOR	DERATING ABOVE $T_C$	$T_C = 125^\circ\text{C}$ POWER RATING
TO-92	1600 mW	28.6 mW/°C	94 °C	713 mW

**RECOMMENDED OPERATING CONDITIONS**

PARAMETER	VALUE
Input Voltage - $V_I$ L78L18	21V to 33V
Output Current - $I_{OUT}$	100 mA (Max)
Operating Virtual Junction Temperature - $T_J$	0 to 125 °C

**ELECTRICAL SPECIFICATIONS (L78L18)**

( $V_I = 25V$ ,  $I_o = 40\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ ,  $0\text{ }^\circ\text{C} < T_J < +125\text{ }^\circ\text{C}$  unless otherwise noted.)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Output Voltage	$T_J = +25\text{ }^\circ\text{C}$	17.3	18	18.7	V
Line Regulation	$T_J = +25\text{ }^\circ\text{C}$ $21V \leq V_I \leq 33V$ $22V \leq V_I \leq 33V$		145 135	300 250	mV
Load Regulation	$T_J = +25\text{ }^\circ\text{C}$ $1.0\text{ mA} \leq I_o \leq 100\text{ mA}$ $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$		30 15	170 85	mV
Output Voltage	$21V \leq V_I \leq 33V$ , $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$ $21V \leq V_I \leq V_{MAX}$ , $1.0\text{ mA} \leq I_o \leq 70\text{ mA}$	17.1 17.1		18.9 18.9	V
Input Bias Current	$T_J = +25\text{ }^\circ\text{C}$		2.0	6.0	mA
Input Bias Current Change	$21V \leq V_I \leq 33V$ $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$			1.5 0.1	mA
Output Noise Voltage	$T_A = +25\text{ }^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ KHz}$		150		$\mu\text{V}$
Ripple Rejection	$T_J = +25\text{ }^\circ\text{C}$ , $f = 120\text{ Hz}$ $23V \leq V_I \leq 33V$	34	48		dB
Dropout Voltage	$T_J = +25\text{ }^\circ\text{C}$		1.7		V

**TO-92 MECHANICAL DATA**

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.445		5.207	H	2.413	2.540	2.667
B	4.318		5.334	I	0.356		0.533
C	12.7		15.5	J			
D	0.356		0.533	K			
E	1.143	1.27	1.397	L			
F	3.175		4.191	M			
G	0.762		1.270	N			

