1.0 A Negative Voltage Regulators

The MC7900 series of fixed output negative voltage regulators are intended as complements to the popular MC7800 series devices. These negative regulators are available in the same seven–voltage options as the MC7800 devices. In addition, one extra voltage option commonly employed in MECL systems is also available in the negative MC7900 series.

Available in fixed output voltage options from -5.0 V to -24 V, these regulators employ current limiting, thermal shutdown, and safe–area compensation – making them remarkably rugged under most operating conditions. With adequate heatsinking they can deliver output currents in excess of 1.0 A.

- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Available in 2% Voltage Tolerance (See Ordering Information)
- Pb–Free Package May be Available. The G–Suffix Denotes a Pb–Free Lead Finish

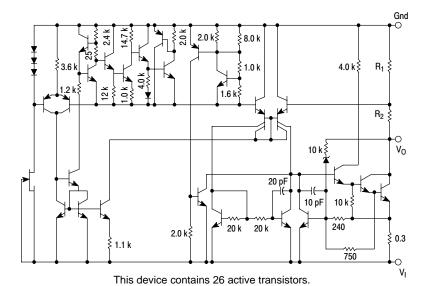


Figure 1. Representative Schematic Diagram

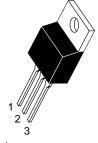


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TO-220 T SUFFIX CASE 221AB

Heatsink surface connected to Pin 2.



Pin 1. Ground 2. Input

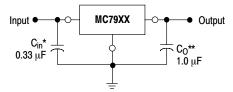
3. Output

D²PAK D2T SUFFIX CASE 936



Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.

STANDARD APPLICATION



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above more negative even during the high point of the input ripple voltage.

- XX, These two digits of the type number indicate nominal voltage.
 - * C_{in} is required if regulator is located an appreciable distance from power supply filter.
- ** C_O improve stability and transient response.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 11 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 14 of this data sheet.

MAXIMUM RATINGS ($T_A = +25$ °C, unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (-5.0 V ≥ V _O ≥ -18 V) (24 V)	VI	-35 -40	Vdc
Power Dissipation Case 221A			
$T_A = +25^{\circ}\text{C}$ Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case Case 936 (D ² PAK)	${\sf P_D} \ {\sf heta_{\sf JA}} \ {\sf heta_{\sf JC}}$	Internally Limited 65 5.0	W °C/W °C/W
$T_A = +25^{\circ}C$ Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case	P _D θ _{JA} θ _{JC}	Internally Limited 70 5.0	°C/W °C/W
Storage Junction Temperature Range	T _{stg}	-65 to +150	°C
Junction Temperature	TJ	+150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Machine Model Method 200 V

MC7905B, MC7905C

ELECTRICAL CHARACTERISTICS ($V_I = -10 \text{ V}$, $I_O = 500 \text{ mA}$, $Tlow^* < T_J < +125^{\circ}C$, unless otherwise noted.)

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	-4.8	-5.0	-5.2	Vdc
Line Regulation (Note 1) (T _J = +25°C, I _O = 100 mA)	Reg _{line}				mV
$-7.0 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -25 \text{ Vdc}$		_	7.0	50	
$-8.0 \text{ Vdc} \ge V_1 \ge -12 \text{ Vdc}$ (T _J = +25°C, I _O = 500 mA)		_	2.0	25	
(1) = +23 C, 10 = 300 HA) $-7.0 \text{ Vdc} \ge \text{V}_1 \ge -25 \text{ Vdc}$		_	35	100	
$-8.0 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -12 \text{ Vdc}$		_	8.0	50	
Load Regulation, T _J = +25°C (Note 1)	Reg _{load}				mV
$5.0 \text{ mA} \le I_0 \le 1.5 \text{ A}$		_	11	100	
250 mA ≤ I _O ≤ 750 mA		_	4.0	50	
Output Voltage	V _O				Vdc
$-7.0 \text{ Vdc} \ge V_1 \ge -20 \text{ Vdc}, 5.0 \text{ mA} \le I_0 \le 1.0 \text{ A}, P \le 15 \text{ W}$		-4.75	_	-5.25	
Input Bias Current ($T_J = +25^{\circ}C$)	I _{IB}	_	4.3	8.0	mA
Input Bias Current Change	Δl_{IB}				mA
$-7.0 \text{ Vdc} \ge V_1 \ge -25 \text{ Vdc}$		-	-	1.3	
$5.0 \text{ mA} \le I_{O} \le 1.5 \text{ A}$		_	_	0.5	
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz $\leq f \leq$ 100 kHz)	V _n	-	40	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	70	_	dB
Dropout Voltage	V_I – V_O				Vdc
$I_{O} = 1.0 \text{ A}, T_{J} = +25^{\circ}\text{C}$		_	1.3	_	
Average Temperature Coefficient of Output Voltage	$\Delta V_{O}/\Delta T$				mV/°C
$I_O = 5.0 \text{ mA}, \text{Tlow}^* \le T_J \le +125^{\circ}\text{C}$		_	-1.0	_	

^{1.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

^{*}This device series contains ESD protection and exceeds the following tests: Human Body Model 2000 V per MIL_STD_883, Method 3015

^{*}Tlow = -40°C for MC7905B and Tlow = 0°C for MC7905C.

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	-4.9	-5.0	-5.1	Vdc
Line Regulation (Note 2) $-8.0 \text{ Vdc} \ge V_I \ge -12 \text{ Vdc}$; $I_O = 1.0 \text{ A}$, $T_J = +25^{\circ}\text{C}$ $-8.0 \text{ Vdc} \ge V_I \ge -12 \text{ Vdc}$; $I_O = 1.0 \text{ A}$ $-7.5 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}$; $I_O = 500 \text{ mA}$ $-7.0 \text{ Vdc} \ge V_I \ge -20 \text{ Vdc}$; $I_O = 1.0 \text{ A}$, $T_J = +25^{\circ}\text{C}$	Reg _{line}	- - - -	2.0 7.0 7.0 6.0	25 50 50 50	mV
Load Regulation (Note 2) $5.0 \text{ mA} \le I_O \le 1.5 \text{ A}, T_J = +25^{\circ}\text{C}$ $250 \text{ mA} \le I_O \le 750 \text{ mA}$ $5.0 \text{ mA} \le I_O \le 1.0 \text{ A}$	Reg _{load}	- - -	11 4.0 9.0	100 50 100	mV
Output Voltage $-7.5 \text{ Vdc} \ge V_l \ge -20 \text{ Vdc}, 5.0 \text{ mA} \le I_O \le 1.0 \text{ A}, \text{ P} \le 15 \text{ W}$	V _O	-4.80	_	-5.20	Vdc
Input Bias Current	I _{IB}	_	4.4	8.0	mA
Input Bias Current Change -7.5 Vdc \ge V _I \ge -25 Vdc 5.0 mA \le I _O \le 1.0 A 5.0 mA \le I _O \le 1.5 A, T _J = +25°C	Δl _{IB}	- - -	- - -	1.3 0.5 0.5	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz \leq f \leq 100 kHz)	V _n	_	40	-	μV
Ripple Rejection (I _O = mA, f = 120 Hz)	RR	-	70	-	dB
Dropout Voltage (I _O = 1.0 A. T _J = +25°C)	$V_I - V_O$	-	1.3	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ A, $0^{\circ}C \le T_J \le +125^{\circ}C$	ΔV _O /ΔΤ	-	-1.0	-	mV/°C

$\label{eq:control_loss} \textbf{MC7905.2C}$ $\textbf{ELECTRICAL CHARACTERISTICS} \ (V_I = -10 \ V, \ I_O = 500 \ mA, \ 0^{\circ}C < T_J < +125^{\circ}C, \ unless \ otherwise \ noted.)$

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	-5.0	-5.2	-5.4	Vdc
Line Regulation (Note 2) (T _J = +25°C, I _O = 100 mA)	Reg _{line}				mV
$-7.2 \text{ Vdc} \ge \text{V}_1 \ge -25 \text{ Vdc}$		_	8.0	52	
$-8.0 \text{ Vdc} \ge V_1 \ge -12 \text{ Vdc}$ $(T_J = +25^{\circ}\text{C}, I_O = 500 \text{ mA})$		_	2.2	27	
$-7.2 \text{ Vdc} \ge V_1 \ge -25 \text{ Vdc}$		-	37	105	
-8.0 Vdc ≥ V _I ≥ -12 Vdc		_	8.5	52	
Load Regulation, T _J = +25°C (Note 2)	Reg _{load}				mV
$5.0 \text{ mA} \le I_0 \le 1.5 \text{ A}$		_	12 4.5	105 52	
250 mA ≤ I _O ≤ 750 mA			4.5	52	
Output Voltage $-7.2 \text{ Vdc} \ge V_1 \ge -20 \text{ Vdc}$, $5.0 \text{ mA} \le I_0 \le 1.0 \text{ A}$, $P \le 15 \text{ W}$	Vo	-4.95	_	-5.45	Vdc
Input Bias Current (T _J = +25°C)	I _{IB}	-	4.3	8.0	mA
Input Bias Current Change	ΔI_{IB}				mA
$-7.2 \text{ Vdc} \ge V_1 \ge -25 \text{ Vdc}$		_	_	1.3	
5.0 mA ≤ I _O ≤ 1.5 A		_	_	0.5	
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz \leq f \leq 100 kHz)	V _n	_	42	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	-	68	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	V _I –V _O	-	1.3	_	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, $0^{\circ}C \le T_J \le +125^{\circ}C$	$\Delta V_{O}/\Delta T$	_	-1.0	_	mV/°C

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

 $\label{eq:control} \mbox{MC7906C} $$ELECTRICAL CHARACTERISTICS (V_I = -11 \ V, \ I_O = 500 \ mA, \ 0^{\circ}C < T_J < +125^{\circ}C, \ unless \ otherwise \ noted.)$$

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	-5.75	-6.0	-6.25	Vdc
Line Regulation (Note 3) (T _J = +25°C, I _O = 100 mA)	Reg _{line}				mV
$-8.0 \text{ Vdc} \ge \text{V}_1 \ge -25 \text{ Vdc}$		_	9.0	60	
$-9.0 \text{ Vdc} \ge V_1 \ge -13 \text{ Vdc}$ $(T_1 = +25^{\circ}\text{C}, I_0 = 500 \text{ mA})$		_	3.0	30	
$-8.0 \text{ Vdc} \ge V_1 \ge -25 \text{ Vdc}$		_	43	120	
-9.0 Vdc ≥ V _I ≥ -13 Vdc		_	10	60	
Load Regulation, T _J = +25°C (Note 3)	Reg _{load}				mV
$5.0 \text{ mA} \le I_O \le 1.5 \text{ A}$ $250 \text{ mA} \le I_O \le 750 \text{ mA}$		_	13 5.0	120 60	
, and the second		_	5.0	60	
Output Voltage $-8.0 \text{ Vdc} \ge V_1 \ge -21 \text{ Vdc}, 5.0 \text{ mA} \le I_0 \le 1.0 \text{ A}, P \le 15 \text{ W}$	Vo	-5.7	-	-6.3	Vdc
Input Bias Current (T _J = +25°C)	I _{IB}	_	4.3	8.0	mA
Input Bias Current Change	ΔI_{IB}				mA
$-8.0 \text{ Vdc} \ge \text{V}_1 \ge -25 \text{ Vdc}$		_	_	1.3	
5.0 mA ≤ I _O ≤ 1.5 A		_	_	0.5	
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f ≤ 100 kHz)	V_n	_	45	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	65	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	$V_I - V_O$	_	1.3	_	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0 \text{ A}, 0^{\circ}\text{C} \leq T_J \leq +125^{\circ}\text{C}$	$\Delta V_{O}/\Delta T$	_	-1.0	_	mV/°C

MC7908C

ELECTRICAL CHARACTERISTICS ($V_I = -14 \text{ V}, I_O = 500 \text{ mA}, 0^{\circ}\text{C} < T_J < +125^{\circ}\text{C}, \text{ unless otherwise noted.}$)

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	-7.7	-8.0	-8.3	Vdc
Line Regulation (Note 3) (T _J = +25°C, I _O = 100 mA)	Reg _{line}				mV
$-10.5 \text{ Vdc} \ge V_1 \ge -25 \text{ Vdc}$ $-11 \text{ Vdc} \ge V_1 \ge -17 \text{ Vdc}$ $(T_1 = +25^{\circ}\text{C}, I_0 = 500 \text{ mA})$		- -	12 5.0	80 40	
$-10.5 \text{ Vdc} \ge V_1 \ge -25 \text{ Vdc}$ $-11 \text{ Vdc} \ge V_1 \ge -17 \text{ Vdc}$		- -	50 22	160 80	
Load Regulation, T_J = +25°C (Note 3) 5.0 mA \leq I _O \leq 1.5 A 250 mA \leq I _O \leq 750 mA	Reg _{load}	- -	26 9.0	160 80	mV
Output Voltage $-10.5 \text{ Vdc} \ge V_I \ge -23 \text{ Vdc, } 5.0 \text{ mA} \le I_O \le 1.0 \text{ A, } P \le 15 \text{ W}$	V _O	-7.6	_	-8.4	Vdc
Input Bias Current (T _J = +25°C)	I _{IB}	_	4.3	8.0	mA
Input Bias Current Change -10.5 Vdc \geq V _I \geq -25 Vdc 5.0 mA \leq I _O \leq 1.5 A	Δl_{IB}	- -	- -	1.0 0.5	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz \leq f \leq 100 kHz)	V _n	_	52	-	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	62	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	V_I – V_O	_	1.3	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, $0^{\circ}C \le T_J \le +125^{\circ}C$	$\Delta V_{O}/\Delta T$	-	-1.0	_	mV/°C

^{3.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

MC7912B, MC7912C ELECTRICAL CHARACTERISTICS (V $_I$ = -19 V, I $_O$ = 500 mA, Tlow* < T $_J$ < +125°C, unless otherwise noted.)

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	V _O	-11.5	-12	-12.5	Vdc
Line Regulation (Note 4) (T _{.I} = +25°C, I _O = 100 mA)	Reg _{line}				mV
$-14.5 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -30 \text{ Vdc}$		_	13	120	
$-16 \text{ Vdc} \ge \text{V}_1 \ge -22 \text{ Vdc}$		_	6.0	60	
$(T_J = +25^{\circ}C, I_O = 500 \text{ mA})$ -14.5 Vdc $\geq V_I \geq -30 \text{ Vdc}$ -16 Vdc $\geq V_I \geq -22 \text{ Vdc}$		- -	55 24	240 120	
Load Regulation, T _J = +25°C (Note 4)	Reg _{load}				mV
$5.0 \text{ mA} \le I_0 \le 1.5 \text{ A}$		_	46	240	
$250 \text{ mA} \leq I_{O} \leq 750 \text{ mA}$		_	17	120	
Output Voltage -14.5 Vdc \geq V _I \geq -27 Vdc, 5.0 mA \leq I _O \leq 1.0 A, P \leq 15 W	Vo	-11.4	_	-12.6	Vdc
Input Bias Current (T _J = +25°C)	I _{IB}	_	4.4	8.0	mA
Input Bias Current Change $-14.5 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -30 \text{ Vdc}$ $5.0 \text{ mA} \le \text{I}_{\text{O}} \le 1.5 \text{ A}$	Δl _{IB}	- -	- -	1.0 0.5	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz \leq f \leq 100 kHz)	V _n	_	75	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	61	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	$V_I - V_O$	-	1.3	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, Tlow* $\leq T_J \leq +125^{\circ}C$	ΔV _O /ΔΤ	-	-1.0	-	mV/°C

MC7912AC

ELECTRICAL CHARACTERISTICS ($V_I = -19 \text{ V}, I_O = 500 \text{ mA}, Tlow^* < T_J < +125 ^{\circ}\text{C}, unless otherwise noted.)$

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	V _O	-11.75	-12	-12.25	Vdc
Line Regulation (Note 4) $-16 \text{ Vdc} \ge V_I \ge -22 \text{ Vdc}$; $I_O = 1.0 \text{ A}$, $T_J = +25^{\circ}\text{C}$ $-16 \text{ Vdc} \ge V_I \ge -22 \text{ Vdc}$; $I_O = 1.0 \text{ A}$ $-14.8 \text{ Vdc} \ge V_I \ge -30 \text{ Vdc}$; $I_O = 500 \text{ mA}$ $-14.5 \text{ Vdc} \ge V_I \ge -27 \text{ Vdc}$; $I_O = 1.0 \text{ A}$, $T_J = +25^{\circ}\text{C}$	Reg _{line}	- - -	6.0 24 24 13	60 120 120 120	mV
Load Regulation (Note 4) 5.0 mA \leq I _O \leq 1.5 A, T _J = +25°C 250 mA \leq I _O \leq 750 mA 5.0 mA \leq I _O \leq 1.0 A	Reg _{load}	- - -	46 17 35	150 75 150	mV
Output Voltage $-14.8 \text{ Vdc} \ge V_I \ge -27 \text{ Vdc}$, $5.0 \text{ mA} \le I_O \le 1.0 \text{ A}$, $P \le 15 \text{ W}$	Vo	-11.5	-	-12.5	Vdc
Input Bias Current	I _{IB}	-	4.4	8.0	mA
Input Bias Current Change $-15 \text{ Vdc} \ge \text{V}_1 \ge -30 \text{ Vdc}$ $5.0 \text{ mA} \le \text{I}_O \le 1.0 \text{ A}$ $5.0 \text{ mA} \le \text{I}_O \le 1.5 \text{ A}, \text{T}_J = +25^{\circ}\text{C}$	ΔI _{IB}	- - -		0.8 0.5 0.5	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz $\leq f \leq$ 100 kHz)	V _n	_	75	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	61	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	V _I –V _O	_	1.3	_	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ A, $Tlow^* \le T_J \le +125^{\circ}C$	$\Delta V_{O}/\Delta T$	_	-1.0	_	mV/°C

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

^{*}Tlow = -40°C for MC7912B and Tlow = 0°C for MC7912C.

MC7915B, MC7915C ELECTRICAL CHARACTERISTICS (V_I = -23 V, I_O = 500 mA, Tlow* < T_J < +125°C, unless otherwise noted.)

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	-14.4	-15	-15.6	Vdc
Line Regulation (Note 5) $(T_{.l} = +25^{\circ}C, I_{O} = 100 \text{ mA})$	Reg _{line}				mV
$-17.5 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -30 \text{ Vdc}$		_	14	150	
$-20 \text{ Vdc} \ge V_1 \ge -26 \text{ Vdc}$ $(T_{.1} = +25^{\circ}\text{C}, I_{O} = 500 \text{ mA})$		_	6.0	75	
(1) = +23 C, 10 = 300 MA -17.5 Vdc $\geq V_1 \geq -30 \text{ Vdc}$		_	57	300	
$-20 \text{ Vdc} \ge \text{V}_1 \ge -26 \text{ Vdc}$		_	27	150	
Load Regulation, T _J = +25°C (Note 5)	Reg _{load}				mV
$5.0 \text{ mA} \le I_0 \le 1.5 \text{ A}$		_	68	300	
$250 \text{ mA} \le I_0 \le 750 \text{ mA}$		_	25	150	
Output Voltage	Vo	44.05		45.75	Vdc
$-17.5 \text{ Vdc} \ge V_1 \ge -30 \text{ Vdc}, 5.0 \text{ mA} \le I_0 \le 1.0 \text{ A}, P \le 15 \text{ W}$		-14.25	_	-15.75	
Input Bias Current (T _J = +25°C)	I _{IB}	_	4.4	8.0	mA
Input Bias Current Change	ΔI_{IB}				mA
$-17.5 \text{ Vdc} \ge V_1 \ge -30 \text{ Vdc}$		_	-	1.0	
5.0 mA ≤ I _O ≤ 1.5 A		_	_	0.5	
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz $\leq f \leq$ 100 kHz)	V _n	_	90	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	60	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	$V_I - V_O$	-	1.3	_	Vdc
Average Temperature Coefficient of Output Voltage	$\Delta V_{O}/\Delta T$				mV/°C
$I_{O} = 5.0 \text{ A, Tlow}^* \le T_{J} \le +125^{\circ}\text{C}$		_	-1.0	_	

MC7915AC

ELECTRICAL CHARACTERISTICS ($V_I = -23 \text{ V}$, $I_O = 500 \text{ mA}$, $Tlow^* < T_J < +125^{\circ}C$, unless otherwise noted.)

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	V _O	-14.7	-15	-15.3	Vdc
Line Regulation (Note 5) $-20 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -26 \text{ Vdc}, \text{ I}_{\text{O}} = 1.0 \text{ A}, \text{T}_{\text{J}} = +25 ^{\circ}\text{C}$ $-20 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -26 \text{ Vdc}, \text{ I}_{\text{O}} = 1.0 \text{ A},$ $-17.9 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -30 \text{ Vdc}, \text{ I}_{\text{O}} = 500 \text{ mA}$ $-17.5 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -30 \text{ Vdc}, \text{ I}_{\text{O}} = 1.0 \text{ A}, \text{T}_{\text{J}} = +25 ^{\circ}\text{C}$	Reg _{line}	- - - -	27 57 57 57	75 150 150 150	mV
Load Regulation (Note 5) 5.0 mA \leq I _O \leq 1.5 A, T _J = +25°C 250 mA \leq I _O \leq 750 mA 5.0 mA \leq I _O \leq 1.0 A	Reg _{load}	- - -	68 25 40	150 75 150	mV
Output Voltage $-17.9 \text{ Vdc} \ge V_l \ge -30 \text{ Vdc}, 5.0 \text{ mA} \le I_O \le 1.0 \text{ A}, \text{ P} \le 15 \text{ W}$	Vo	-14.4	_	-15.6	Vdc
Input Bias Current	I _{IB}	_	4.4	8.0	mA
Input Bias Current Change $-17.5 \text{ Vdc} \ge V_I \ge -30 \text{ Vdc}$ $5.0 \text{ mA} \le I_O \le 1.0 \text{ A}$ $5.0 \text{ mA} \le I_O \le 1.5 \text{ A}, T_J = +25^{\circ}\text{C}$	$\Delta I_{ m lB}$	- - -	- - -	0.8 0.5 0.5	mA
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz \leq f \leq 100 kHz)	V _n	_	90	_	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	-	60	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	V_I – V_O	_	1.3	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, Tlow* $\leq T_J \leq +125^{\circ}C$	$\Delta V_{O}/\Delta T$	_	-1.0	-	mV/°C

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

^{*}Tlow = -40°C for MC7915B and Tlow = 0°C for MC7915C.

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	V _O	-17.3	-18	-18.7	Vdc
Line Regulation (Note 6) (T _J = +25°C, I _O = 100 mA)	Reg _{line}				mV
$-21 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}$		_	25	180	
$-24 \text{ Vdc} \ge V_1 \ge -30 \text{ Vdc}$ (T _J = +25°C, I _O = 500 mA)		_	10	90	
$-21 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}$		_	90	360	
–24 Vdc ≥ V _I ≥ –30 Vdc		_	50	180	
Load Regulation, T _J = +25°C (Note 6)	Reg _{load}				mV
5.0 mA \leq I _O \leq 1.5 A 250 mA \leq I _O \leq 750 mA		_	110 55	360 180	
, and the second	V		00	100	\/-l-
Output Voltage -21 Vdc \geq V _I \geq -33 Vdc, 5.0 mA \leq I _O \leq 1.0 A, P \leq 15 W	Vo	-17.1	-	-18.9	Vdc
Input Bias Current (T _J = +25°C)	I _{IB}	_	4.5	8.0	mA
Input Bias Current Change	ΔI_{IB}				mA
$-21 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}$		_	_	1.0 0.5	
5.0 mA ≤ I _O ≤ 1.5 A		_	_	0.5	
Output Noise Voltage ($T_A = +25^{\circ}C$, 10 Hz \leq f \leq 100 kHz)	V _n	_	110	-	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	_	59	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	V _I –V _O	-	1.3	-	Vdc
Average Temperature Coefficient of Output Voltage	$\Delta V_{O}/\Delta T$				mV/°C
$I_{O} = 5.0 \text{ mA}, 0^{\circ}\text{C} \le T_{J} \le +125^{\circ}\text{C}$		_	-1.0	_	

MC7924B, MC7924C

 $\textbf{ELECTRICAL CHARACTERISTICS} \ (V_I = -33 \ V, \ I_O = 500 \ \text{mA}, \ Tlow^* < T_J < +125 ^{\circ}C, \ unless \ otherwise \ noted.)$

Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	-23	-24	-25	Vdc
Line Regulation (Note 6) $(T_J = +25^{\circ}C, I_O = 100 \text{ mA})$	Reg _{line}				mV
$ -27 \text{ Vdc} ≥ V_1 ≥ -38 \text{ Vdc} -30 \text{ Vdc} ≥ V_1 ≥ -36 \text{ Vdc} (T_1 = +25°C, I_0 = 500 \text{ mA}) $		_ _	31 14	240 120	
$-27 \text{ Vdc} \ge V_1 \ge -38 \text{ Vdc}$ $-30 \text{ Vdc} \ge V_1 \ge -36 \text{ Vdc}$		_ _	118 70	470 240	
Load Regulation, T_J = +25°C (Note 6) 5.0 mA \leq I _O \leq 1.5 A 250 mA \leq I _O \leq 750 mA	Reg _{load}	- -	150 85	480 240	mV
Output Voltage $-27 \text{ Vdc} \ge V_l \ge -38 \text{ Vdc}, 5.0 \text{ mA} \le I_O \le 1.0 \text{ A}, P \le 15 \text{ W}$	Vo	-22.8	_	-25.2	Vdc
Input Bias Current (T _J = +25°C)	I _{IB}	-	4.6	8.0	mA
Input Bias Current Change $-27 \text{ Vdc} \ge V_l \ge -38 \text{ Vdc}$ $5.0 \text{ mA} \le I_O \le 1.5 \text{ A}$	$\Delta l_{ m IB}$	_ _	_ _	1.0 0.5	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f ≤ 100 kHz)	V _n	-	170	-	μV
Ripple Rejection (I _O = 20 mA, f = 120 Hz)	RR	-	56	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = +25°C)	V _I –V _O	-	1.3	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, Tlow* $\leq T_J \leq +125^{\circ}C$	$\Delta V_{O}/\Delta T$	_	-1.0	_	mV/°C

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

^{*}Tlow = -40°C for MC7924B and Tlow = 0°C for MC7924C.

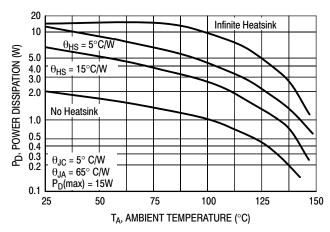


Figure 2. Worst Case Power Dissipation as a Function of Ambient Temperature

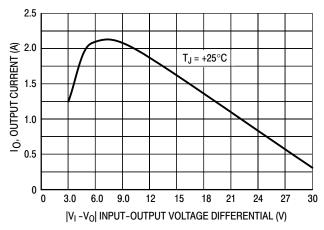


Figure 3. Peak Output Current as a Function of Input-Output Differential Voltage

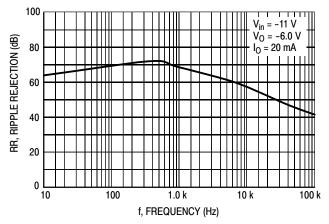


Figure 4. Ripple Rejection as a Function of Frequency

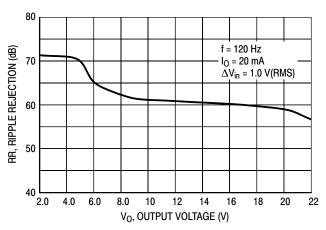


Figure 5. Ripple Rejection as a Function of Output Voltage

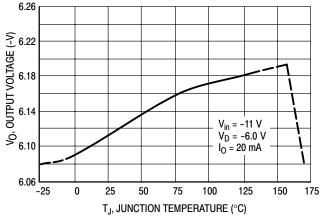


Figure 6. Output Voltage as a Function of Junction Temperature

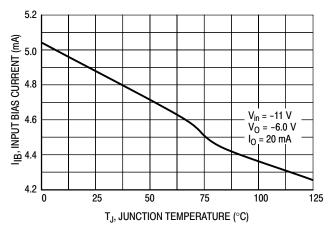


Figure 7. Quiescent Current as a Function of Temperature

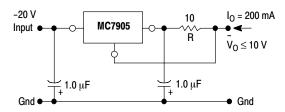
APPLICATIONS INFORMATION

Design Considerations

The MC7900 Series of fixed voltage regulators are designed with Thermal overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe–Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A 0.33 μ F or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The capacitor chosen should have an equivalent series resistance of less than 0.7 Ω . The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Normally good construction techniques should be

used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.

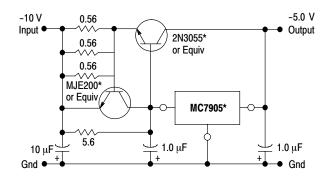


The MC7905, -5.0 V regulator can be used as a constant current source when connected as above. The output current is the sum of resistor R current and quiescent bias current as follows.

$$I_O = \frac{5.0 \text{ V}}{\text{B}} + I_B$$

The quiescent current for this regulator is typically 4.3 mA. The 5.0 V regulator was chosen to minimize dissipation and to allow the output voltage to operate to within 6.0 V below the input voltage.

Figure 8. Current Regulator

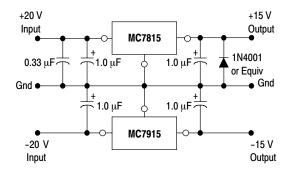


*Mounted on heatsink.

When a boost transistor is used, short circuit currents are equal to the sum of the series pass and regulator limits, which are measured at 3.2 A and 1.8 A respectively in this case. Series pass limiting is approximately equal to 0.6 V/R_{SC}. Operation beyond this point to the peak current capability of the MC7905C is possible if the regulator is mounted on a heatsink; otherwise thermal shutdown will occur when the additional load current is picked up by the regulator.

Figure 9. Current Boost Regulator

 $(-5.0\ V\ @\ 4.0\ A,\ with\ 5.0\ A\ Current\ Limiting)$



The MC7815 and MC7915 positive and negative regulators may be connected as shown to obtain a dual power supply for operational amplifiers. A clamp diode should be used at the output of the MC7815 to prevent potential latch-up problems whenever the output of the positive regulator (MC7815) is drawn below ground with an output current greater than 200 mA.

Figure 10. Operational Amplifier Supply (±15 @ 1.0 A)

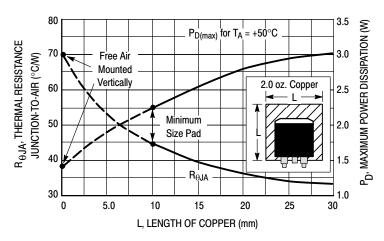


Figure 11. D²PAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

DEFINITIONS

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation – The change in output voltage for a change in load current at constant chip temperature.

Maximum Power Dissipation – The maximum total device dissipation for which the regulator will operate within specifications.

Input Bias Current – That part of the input current that is not delivered to the load.

Output Noise Voltage – The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Long Term Stability – Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices' electrical characteristics and maximum power dissipation.

ORDERING INFORMATION

Device	Nominal Output Voltage	Output Voltage Tolerance	Package	Operating Temperature Range	Shipping _†
MC7905ACD2T	−5.0 V	2%	D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7905ACD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7905ACD2TR4			D ² PAK		800 Tape & Reel
MC7905ACD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7905ACT			TO-220	_	50 Units/Rail
MC7905ACTG			TO-220 (Pb-Free)		50 Units/Rail
MC7905BD2T		4%	D ² PAK	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	50 Units/Rail
MC7905BD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7905BD2TR4			D ² PAK		800 Tape & Reel
MC7905BD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7905BT			TO-220		50 Units/Rail
MC7905BTG			TO-220 (Pb-Free)		50 Units/Rail
MC7905CD2T			D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7905CD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7905CD2TR4			D ² PAK	_	800 Tape & Reel
MC7905CD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7905CT			TO-220		50 Units/Rail
MC7905CTG			TO-220 (Pb-Free)		50 Units/Rail
MC7905.2CT	-5.2 V	4%	TO-220	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7905.2CTG			TO-220 (Pb-Free)		50 Units/Rail
MC7906CD2T	-6.0 V	4%	D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7906CD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7906CT			TO-220		50 Units/Rail
MC7906CTG			TO-220 (Pb-Free)		50 Units/Rail
MC7908ACT	-8.0 V	2%	TO-220	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7908ACTG			TO-220 (Pb-Free)		50 Units/Rail
MC7908CD2T		4%	D ² PAK		50 Units/Rail
MC7908CD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7908CD2TR4			D ² PAK		800 Tape & Reel
MC7908CD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7908CT			TO-220		50 Units/Rail
MC7908CTG			TO-220 (Pb-Free)		50 Units/Rail

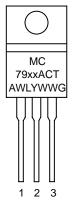
Device	Nominal Output Voltage	Output Voltage Tolerance	Package	Operating Temperature Range	Shipping _†
MC7912ACD2T	–12 V	2%	D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7912ACD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7912ACD2TR4			D ² PAK	-	800 Tape & Reel
MC7912ACD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7912ACT			TO-220		50 Units/Rail
MC7912ACTG			TO-220 (Pb-Free)		50 Units/Rail
MC7912BD2T		4%	D ² PAK	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	50 Units/Rail
MC7912BD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7912BD2TR4			D ² PAK		800 Tape & Reel
MC7912BD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7912BT			TO-220		50 Units/Rail
MC7912BTG			TO-220 (Pb-Free)		50 Units/Rail
MC7912CD2T			D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7912CD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7912CD2TR4			D ² PAK	1	800 Tape & Reel
MC7912CD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7912CT			TO-220	1	50 Units/Rail
MC7912CTG			TO-220 (Pb-Free)		50 Units/Rail
MC7915ACD2T	– 15 V	2%	D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7915ACD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7915ACT			TO-220		50 Units/Rail
MC7915ACTG			TO-220 (Pb-Free)		50 Units/Rail
MC7915BD2T		4%	D ² PAK	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	50 Units/Rail
MC7915BD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7915BT			TO-220		50 Units/Rail
MC7915BTG			TO-220 (Pb-Free)		50 Units/Rail
MC7915CD2T			D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7915CD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7915CD2TR4			D ² PAK		800 Tape & Reel
MC7915CD2TR4G			D ² PAK (Pb-Free)		800 Tape & Reel
MC7915CT			TO-220		50 Units/Rail
MC7915CTG			TO-220 (Pb-Free)		50 Units/Rail

Device	Nominal Output Voltage	Output Voltage Tolerance	Package	Operating Temperature Range	Shipping _†
MC7918CT	– 18 V	4%	TO-220	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7918CTG			TO-220 (Pb-Free)		50 Units/Rail
MC7924BT	– 24 V	4%	TO-220	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	50 Units/Rail
MC7924BTG			TO-220 (Pb-Free)		50 Units/Rail
MC7924CD2T			D ² PAK	$T_J = 0$ °C to +125°C	50 Units/Rail
MC7924CD2TG			D ² PAK (Pb-Free)		50 Units/Rail
MC7924CT			TO-220		50 Units/Rail
MC7924CTG			TO-220 (Pb-Free)		50 Units/Rail

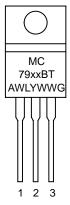
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

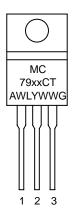
MARKING DIAGRAMS

TO-220 T SUFFIX CASE 221AB



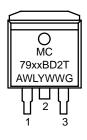






D²PAK D2T SUFFIX CASE 936







xx = Nominal Voltage

A = Assembly Location

WL = Wafer Lot

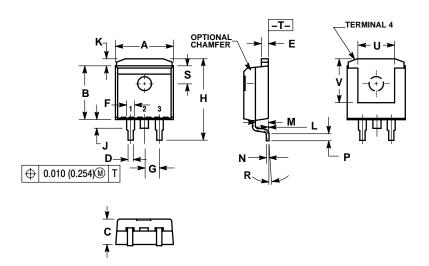
Y = Year

WW = Work Week

G = Pb-Free Device

PACKAGE DIMENSIONS

D²PAK **D2T SUFFIX** PLASTIC PACKAGE CASE 936-03 **ISSUE B**



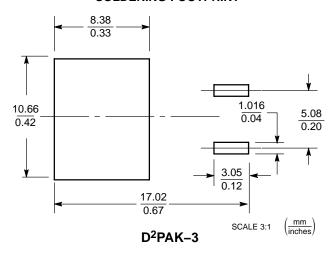
NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
 4. DIMENSIONS U AND V ESTABLISH A MINIMUM MOUNTING SURFACE FOR TERMINAL 4.
 5. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.386	0.403	9.804	10.236	
В	0.356	0.368	9.042	9.347	
С	0.170	0.180	4.318	4.572	
D	0.026	0.036	0.660	0.914	
Е	0.045	0.055	1.143	1.397	
F	0.051	REF	1.295 REF		
G	0.100	BSC	2.540 BSC		
Н	0.539	0.579	13.691	14.707	
J	0.125	MAX	3.175 MAX		
K	0.050 REF		1.270 REF		
L	0.000	0.010	0.000	0.254	
M	0.088	0.102	2.235	2.591	
N	0.018	0.026	0.457	0.660	
P	0.058	0.078	1.473	1.981	
R	5° REF		5° REF		
S	0.116 REF		2.946 REF		
5	0.200 MIN		5.080 MIN		
٧	0.250 MIN		6.350 MIN		

SOLDERING FOOTPRINT*

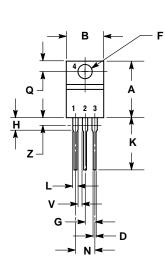


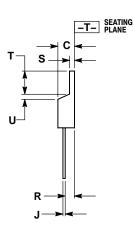
^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TO-220, SINGLE GAUGE **T SUFFIX**

CASE 221AB-01 **ISSUE O**





NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.020	0.055	0.508	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

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