<u>TOSHIBA</u>

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

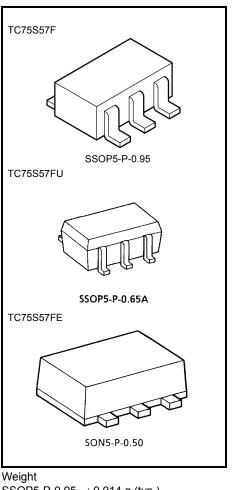
TC75S57F,TC75S57FU,TC75S57FE

Single Comparator

The TC75S57F/TC75S57FU/TC75S57FE is a CMOS generalpurpose single comparator. The device can operate off a single power supply and draws a lower supply current than a conventional bipolar general-purpose comparator. This device's push-pull output stage can be directly connected to TTL or CMOS logic ICs, among others.

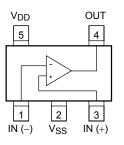
Features

- Low-current power supply $: I_{DD} = 100 \ \mu A \ (typ.)$
- Single power supply operation
- Wide common mode input voltage range $: VSS \sim VDD 0.9 V$
- Push-pull output circuit
- Low input bias current
- Small package

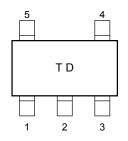


SSOP5-P-0.95 : 0.014 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.) SON5-P-0.50 : 0.003 g (typ.)

Pin Connection (top view)



Marking (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Supply voltage		V _{DD} , V _{SS}	±3.5 or 7	V	
Differential input voltage		DVIN	±7	V	
Input voltage		V _{IN}	V _{SS} ~V _{DD}	V	
Output Current		IOUT	±35	mA	
Power dissipation	TC75S57F/FU	PD	200	mW	
	TC75S57FE		100	IIIVV	
Operating temperature		T _{opr}	-40~85	°C	
Storage temperature		T _{stg}	-55~125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note: This device's CMOS structure makes it prone to latch-up. To prevent latch-up, please take the following precautions:
 - Ensure that no I/O pin's voltage level ever exceeds V_{DD} or drops below $V_{SS}.$ In addition, check the power-on timing.
 - Do not subject the device to excessive noise.

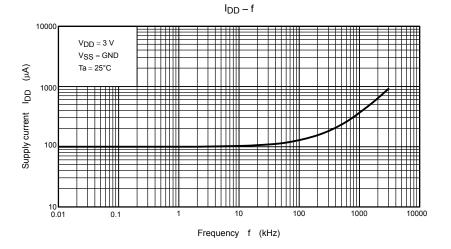
Electrical Characteristics (unless otherwise specified, $V_{DD} = 5 V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

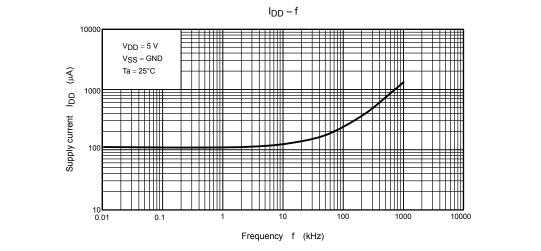
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V _{IO}	_	—	_	±1	±7	mV
Input offset current	I _{IO}	_	—	_	1	_	pА
Input bias current	lı	_	—	_	1	_	pА
Common mode input voltage	CMVIN		—	0		4.1	V
Supply current	I _{DD} (Note)		—	_	110	220	μΑ
Voltage gain	GV		—	_	94	_	dB
Sink current	I _{sink}	_	V _{OL} = 0.5 V	13	25	_	mA
Source current	I _{source}		V _{OH} = 4.5 V	9	21	_	mA
Output valtage	V _{OL}		I _{sink} = 5.0 mA	_	0.1	0.3	V
Output voltage	V _{OH}		$I_{source} = 5.0 \text{ mA}$	4.7	4.9	_	
Operating supply voltage	V _{DD}	_	—	1.8		7.0	V
Propagation delay time (turn on)	^t PLH (1)		Over drive = 100 mV	_	140	_	ns
	t _{PLH} (2)	_	TTL step input	_	90	_	
Propagation delay time (turn off)	^t PHL (1)		Over drive = 100 mV	_	90	_	ns
	t _{PHL (2)}	—	TTL step input		70		
Response time	t _{TLH}		Over drive = 100 mV		11	_	- ns
	t _{THL}		Over drive = 100 mV		7	_	

Electrical Characteristics (unless otherwise specified, $V_{DD} = 3 V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V _{IO}		—	_	±1	±7	mV
Input offset current	l _{IO}		—	_	1	_	pА
Input bias current	lı		—	_	1		pА
Common mode input voltage	CMVIN		—	0		2.1	V
Supply current	I _{DD} (Note)		—		100	200	μΑ
Sink current	I _{sink}		$V_{OL} = 0.5 V$	6	18	_	mA
Source current	I _{source}		V _{OH} = 2.5 V	3	15		mA
Output voltage	V _{OL}		I _{sink} = 5.0 mA	_	0.15	0.35	v
	V _{OH}		I _{source} = 5.0 mA	2.65	2.85		
Propagation delay time (turn on)	t _{PLH}		Over drive = 100 mV		110		ns
Propagation delay time (turn off)	t _{PHL}	_	Over drive = 100 mV	_	90	_	ns
Response time	tтLH	—	Over drive = 100 mV	_	7	_	ns
	t _{THL}		Over drive = 100 mV	_	8		

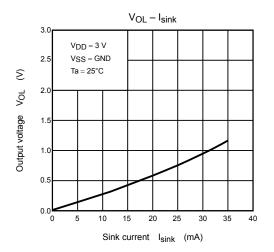
Note: This device's current consumption increases as its operating frequency increases. Note that the power dissipation should not exceed the allowable power dissipation.

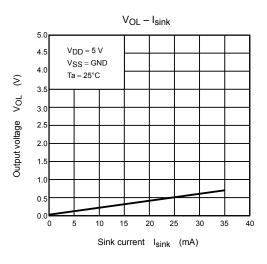


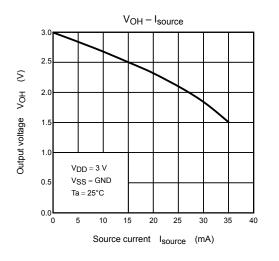


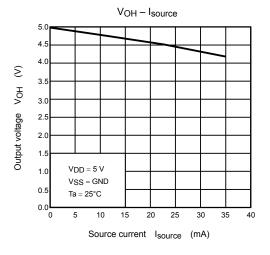
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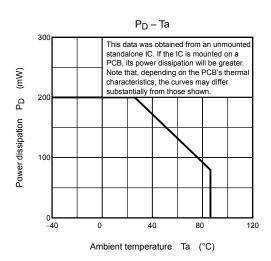
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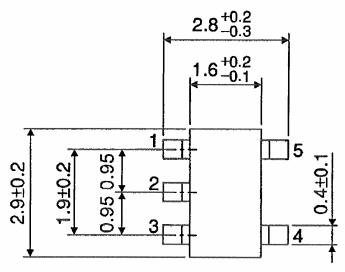
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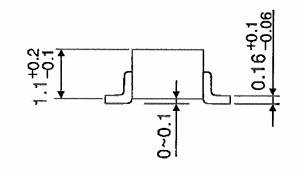
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Package Dimensions



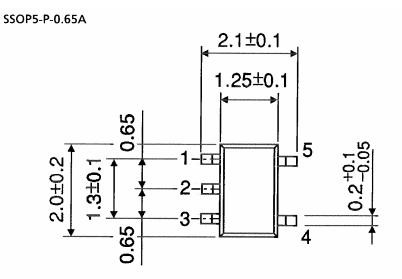
Unit : mm

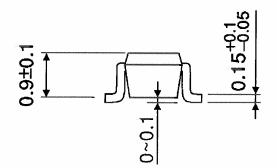




Weight: 0.014 g (typ.)

Package Dimensions





Weight: 0.006 g (typ.)

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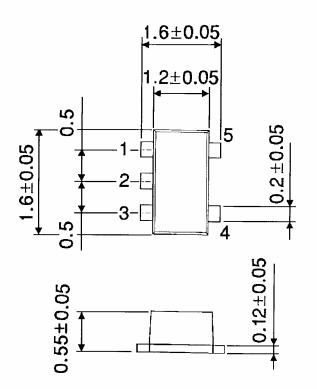
Unit : mm

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Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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