

Electrical Characteristics (Unless otherwise specified, condition shall be $V_{IN}=12V$, $I_o=0.2A$, $V_o=5V$, ON-OFF terminals is open, $T_a=25^\circ C$)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output saturation voltage	V_{SAT}	$I_{sw}=1A$	-	1.0	1.5	V
Reference voltage	V_{ref}	-	1.235	1.26	1.285	V
Reference voltage temperature fluctuation	ΔV_{ref}	$T_j=0$ to $125^\circ C$	-	± 0.5	-	%
Load regulation	$ R_{egL} $	$I_o=0.2$ to $1A$	-	0.2	1.5	%
Line regulation	$ R_{egI} $	$V_{IN}=8$ to $35V$	-	0.5	2.5	%
Efficiency	η	$I_o=1A$	-	83	-	%
Oscillation frequency	f_o	-	270	300	330	kHz
Oscillation frequency temperature fluctuation	Δf_o	$T_j=0$ to $125^\circ C$	-	± 3	-	%
Overcurrent detecting level	I_L	-	1.55	2.0	2.6	A
Charge current	I_{CHG}	②, ④ terminals is open, ⑤ terminal	-	-10	-	μA
Input threshold voltage	V_{THL}	Duty ratio=0%, ④ terminal=0V, ⑤ terminal	-	1.3	-	V
	V_{THH}	Duty ratio=100%, ④ terminals=1.1V, ⑤ terminal	-	2.3	-	V
ON threshold voltage	$V_{TH(ON)}$	④ terminal=0V, ⑤ terminal	0.7	0.8	0.9	V
Stand-by current	I_{SD}	$V_{IN}=40V$, ⑤ terminal=0V	-	140	400	μA
Output OFF-state dissipation current	I_{QS}	$V_{IN}=40V$, ④ terminal=0V, ⑤ terminal=0.9V	-	8	12	mA

Fig.1 Test Circuit

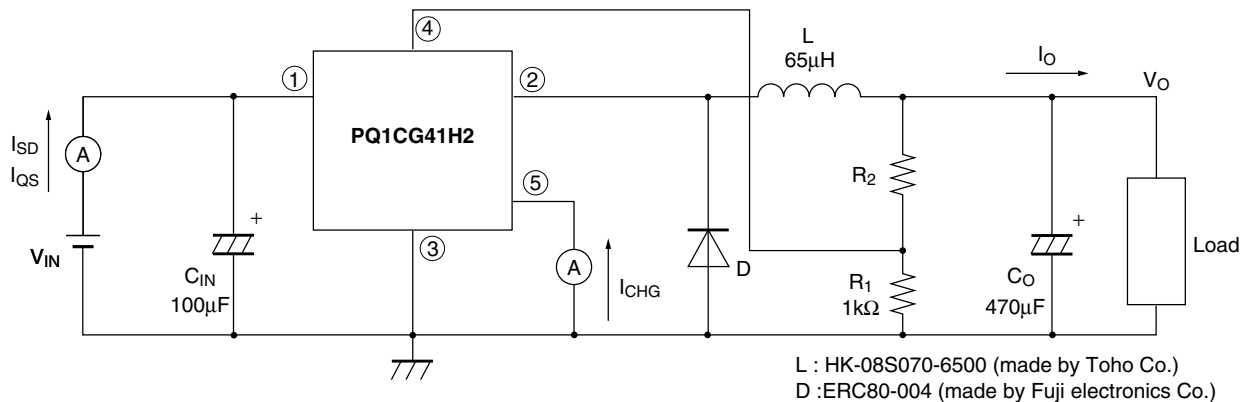
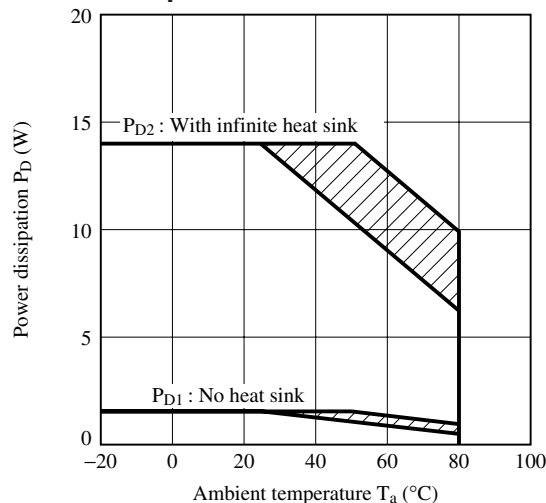


Fig.2 Power Dissipation vs. Ambient Temperature



Note) Oblique line portion: Overheat protection may operate in this area

Fig.3 Block Diagram

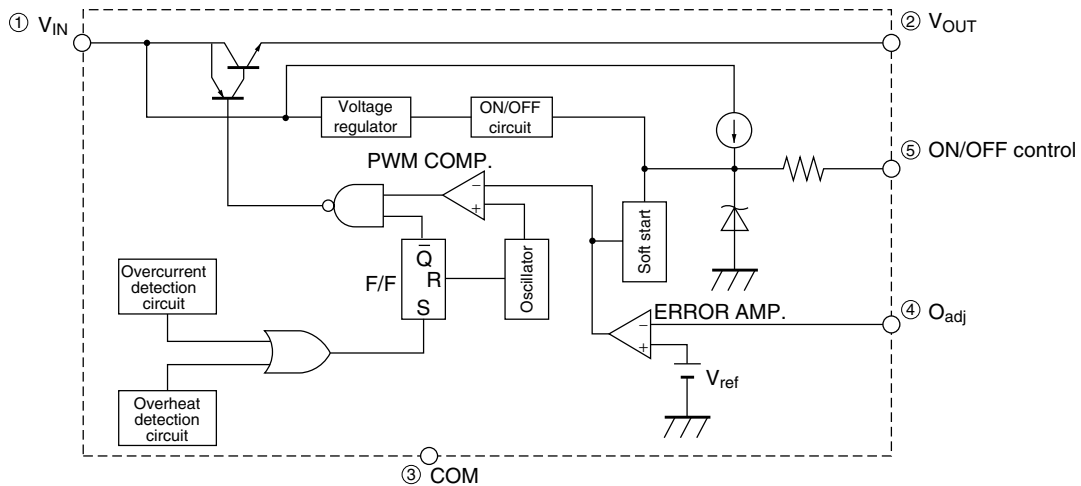


Fig.4 Step Down Type Circuit Diagram

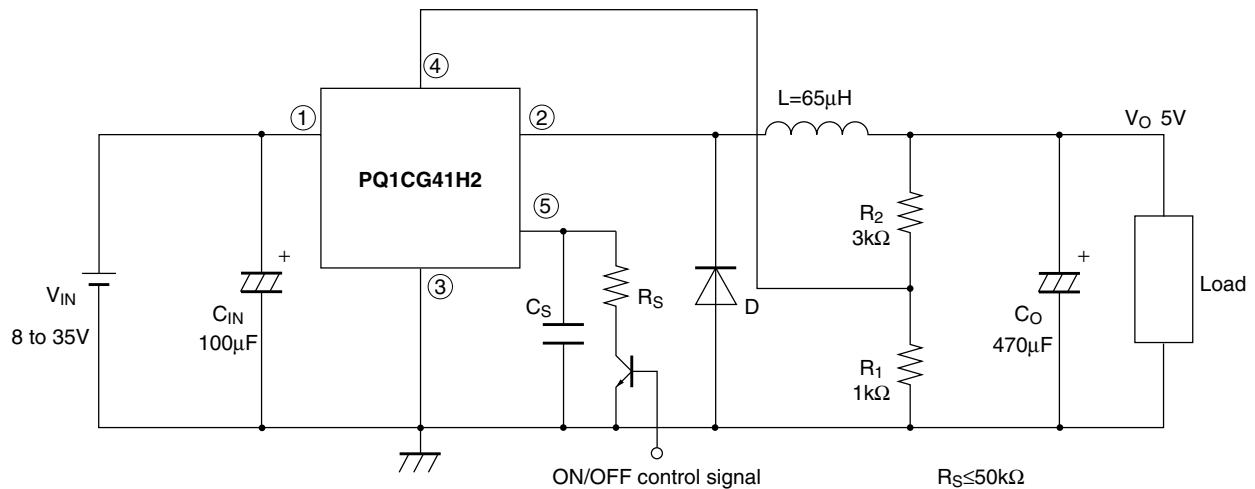
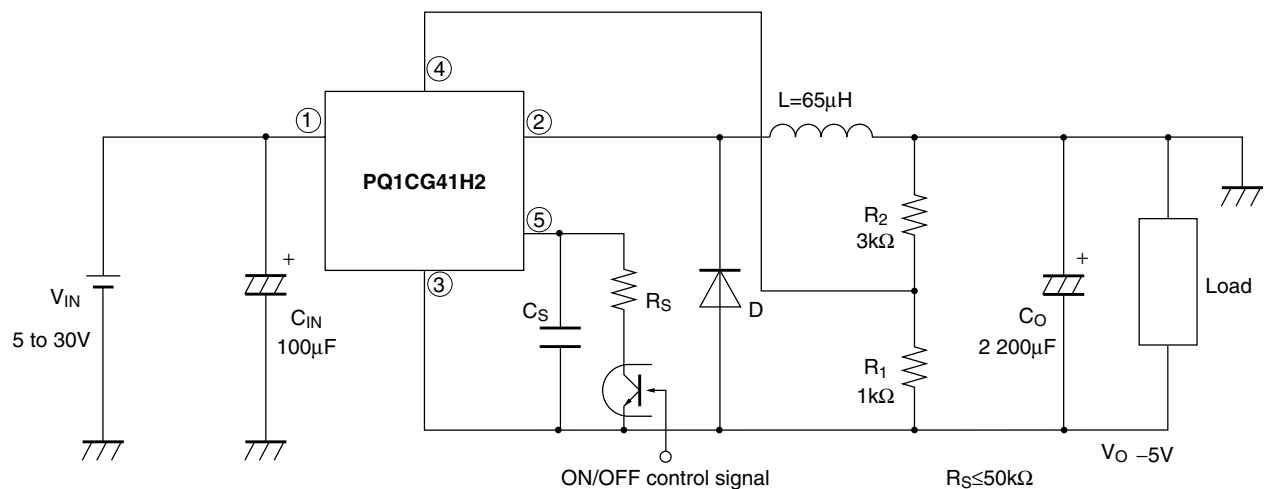


Fig.5 Polarity Inversion Type Circuit Diagram



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