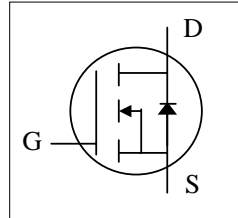




- ▼ Low On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic

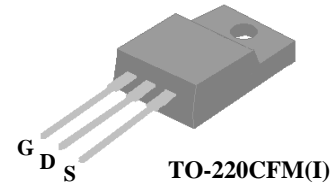


BV_{DSS}	500V
$R_{DS(ON)}$	0.4 Ω
I_D	16A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220CFM isolation package is widely preferred for commercial-industrial through hole applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	16	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	11	A
I_{DM}	Pulsed Drain Current ¹	60	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	52	W
	Linear Derating Factor	0.42	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	2.4	$^\circ C/W$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	65	$^\circ C/W$



Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =1mA	500	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =6.5A	-	-	0.4	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	3	-	5	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =8A	-	8	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =500V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =16A	-	33	53	nC
Q _{gs}	Gate-Source Charge	V _{DS} =400V	-	11	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	9	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DD} =200V	-	55	-	ns
t _r	Rise Time	I _D =8A	-	50	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =50Ω, V _{GS} =10V	-	141	-	ns
t _f	Fall Time	R _D =25Ω	-	40	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1950	3120	pF
C _{oss}	Output Capacitance	V _{DS} =15V	-	630	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	20	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =16A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =16A, V _{GS} =0V	-	495	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs	-	10	-	uC

Notes:

- 1.Pulse width limited by Max junction temperature.
- 2.Pulse test

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.

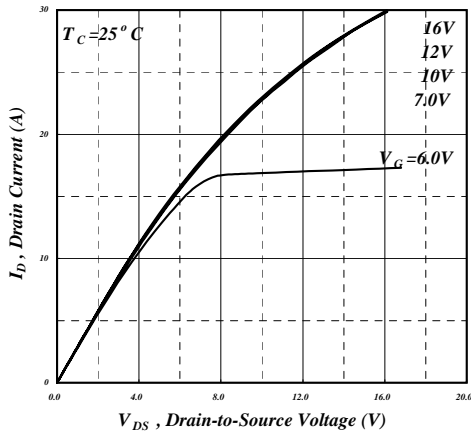


Fig 1. Typical Output Characteristics

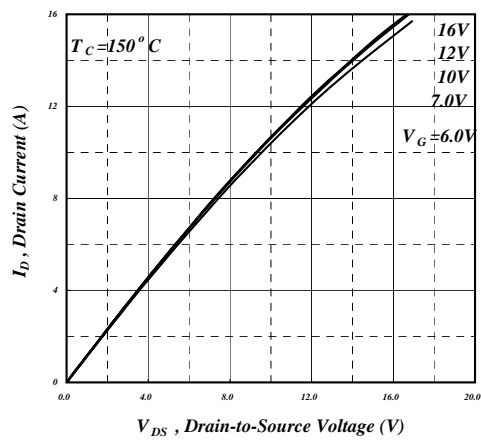


Fig 2. Typical Output Characteristics

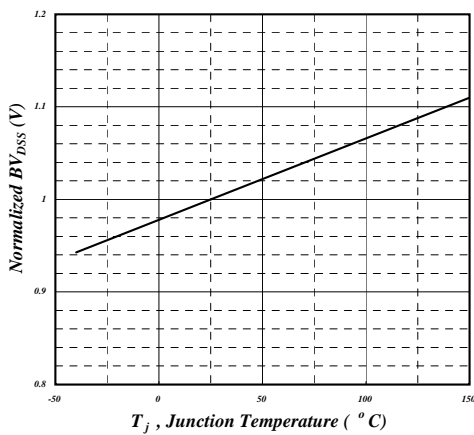


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

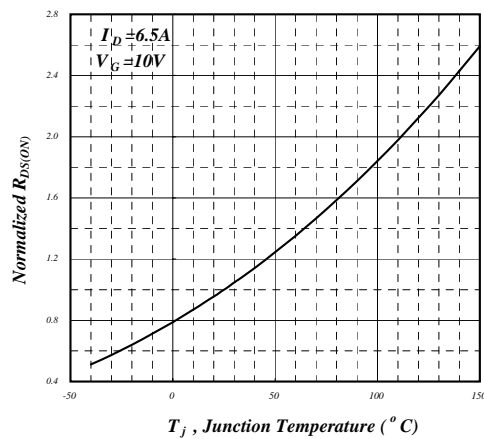


Fig 4. Normalized On-Resistance v.s. Junction Temperature

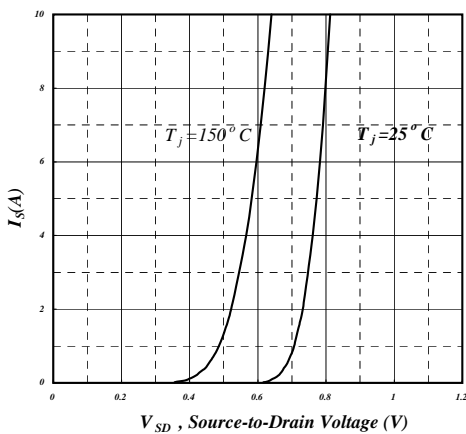


Fig 5. Forward Characteristic of Reverse Diode

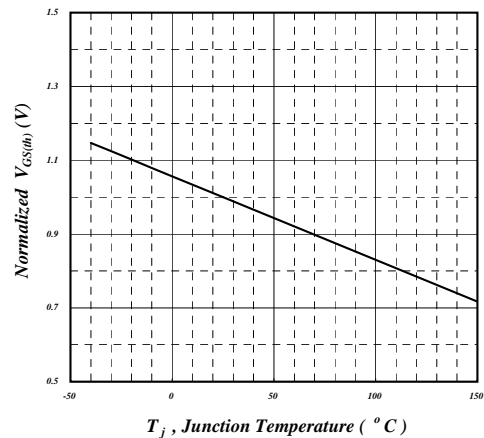


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

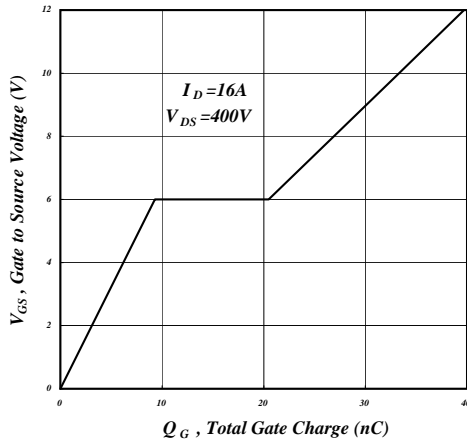


Fig 7. Gate Charge Characteristics

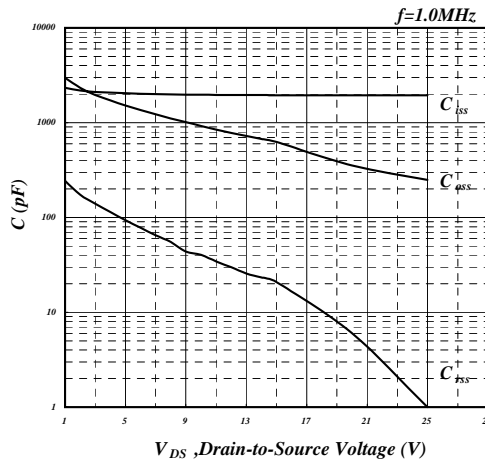


Fig 8. Typical Capacitance Characteristics

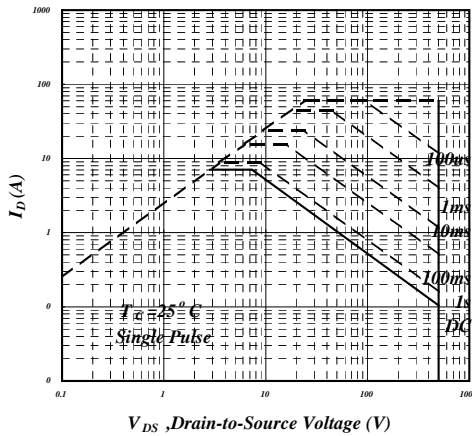


Fig 9. Maximum Safe Operating Area

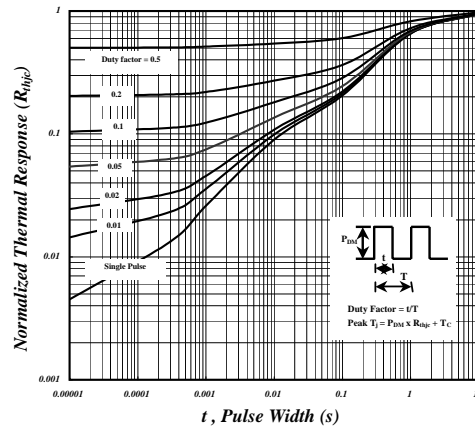


Fig 10. Effective Transient Thermal Impedance

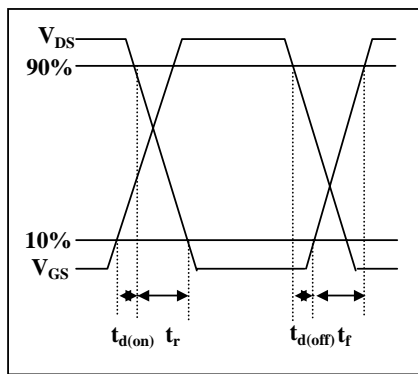


Fig 11. Switching Time Waveform

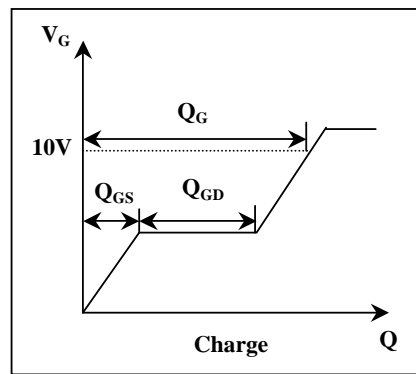
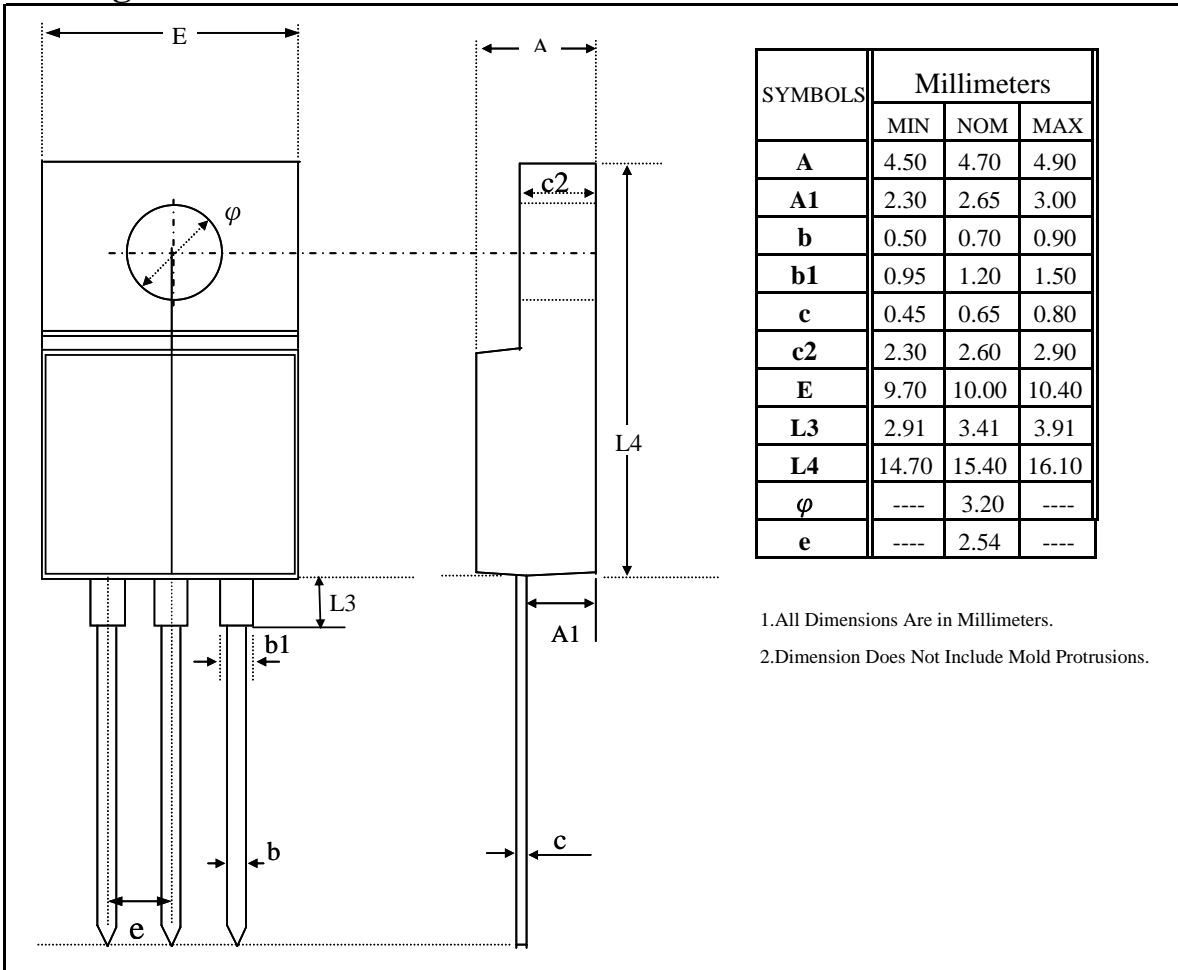


Fig 12. Gate Charge Waveform



Package Outline : TO-220CFM



Part Marking Information & Packing : TO-220CFM

