

# 1A LOW DROPOUT POSITIVE FIXED 3.3V REGULATOR

#### **FEATURES**

- Guaranteed < 1.3V Dropout at Full Load Current
- Fast Transient Response
- 1% Output Voltage Initial Accuracy
- Built-In Thermal Shutdown
- Available in SOT-223, D-Pak, Ultra Thin-Pak and 8-Pin SOIC Surface-Mount Packages

## **APPLICATIONS**

- VGA & Sound Card Applications
- Standard 3.3V Chip Set and Logic Applications

# **DESCRIPTION**

The IRU1010-33 is a low dropout three-terminal fixed output regulator with minimum of 1A output current capability. This product is specifically designed to provide well regulated supply for low voltage IC applications such as VGA, sound & DVD cards. The IRU1010-33 is guaranteed to have <1.3V dropout at full load current making it ideal to provide well regulated with 4.75V to 7V input supply. The IRU1010-33 is specifically designed to be stable with low cost aluminum capacitors while maintaining stability with low ESR tantalum caps.

#### TYPICAL APPLICATION

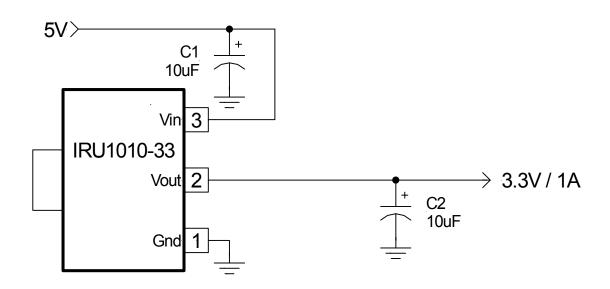


Figure 1 - Typical application of IRU1010-33 in a 5V to 3.3V regulator

# PACKAGE ORDER INFORMATION

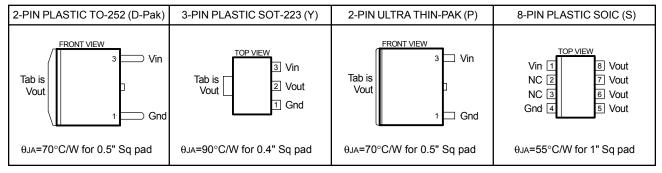
Tj (°C)	2-PIN PLASTIC	3-PIN PLASTIC	2-PIN PLASTIC	8-PIN PLASTIC
	TO-252 (D-Pak)	SOT-223 (Y)	Ultra Thin-Pak (P)	SOIC (S)
0 To 150	IRU1010-33CD	IRU1010-33CY	IRU1010-33CP	IRU1010-33CS

# IRU1010-33

## **ABSOLUTE MAXIMUM RATINGS**

Power Dissipation Internally Limited
Storage Temperature Range -65°C To 150°C
Operating Junction Temperature Range 0°C To 150°C

#### PACKAGE INFORMATION



## **ELECTRICAL SPECIFICATIONS**

Unless otherwise specified, these specifications apply over Cin=1 $\mu$ F, Vin=5V, Cout=10 $\mu$ F, and Tj=0 to 125°C. Typical values refer to Tj=25°C.

PARAMETER	SYM	TEST CONDITION	MIN	TYP	MAX	UNITS
Output Voltage	Vo	lo=10mA,Tj=25°C	3.267	3.300	3.333	V
		lo=10mA	3.235	3.300	3.365	
Line Regulation		Io=10mA, 4.75V <vin<7v< td=""><td></td><td></td><td>7</td><td>mV</td></vin<7v<>			7	mV
Load Regulation (Note 1)		10mA <lo<1a< td=""><td></td><td></td><td>17</td><td>mV</td></lo<1a<>			17	mV
Dropout Voltage (Note 2)		Io=1A			1.3	V
Current Limit		dVo=100mV	1.1			Α
Thermal Regulation		30ms Pulse, Io=1A		0.01	0.02	%/W
Ripple Rejection		f=120Hz, Co=25μF Tantalum,				
		Io=0.5A	60	70		dB
Temperature Stability		lo=10mA		0.5		%
Long Term Stability		Tj=125°C, 1000Hrs		0.3	1	%
RMS Output Noise		Tj=25°C, 10Hz <f<10khz< td=""><td></td><td>0.003</td><td></td><td>%Vo</td></f<10khz<>		0.003		%Vo

Note 1: Low duty cycle pulse testing with Kelvin connections is required in order to maintain accurate data.

**Note 2:** Dropout voltage is defined as the minimum differential voltage between Vin and Vout required to maintain regulation at Vout. It is measured when the output voltage drops 1% below its nominal value.

**2** Rev. 1.3 06/29/01

# **PIN DESCRIPTIONS**

PIN#	PIN SYMBOL	PIN DESCRIPTION
1	Gnd	Ground pin. This pin must be connected to ground plane using a low inductance short connection.
2	Vout	The output of the regulator. This pin is also connected to the tab of the package. An output capacitor must be connected to this pin to insure stability of the regulator.
3	Vin	Input pin of the regulator.

# **BLOCK DIAGRAM**

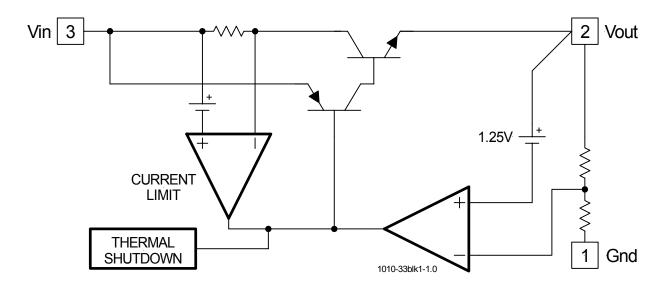


Figure 2 - Simplified block diagram of the IRU1010-33

IRU1010-33 Notes



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Data and specifications subject to change without notice. 02/01