

L6755

2/3/4 phase buck controller for VR10, VR11 and AM2 processor applications

Data Brief

Features

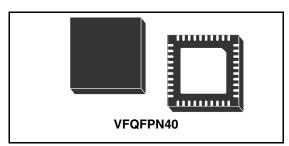
- LTB Technology[®] enhances load transient response
- 2 to 4 scalable phase operation
- Dual-edge asynchronous architecture
- Flexible driver support
- 7/8 bit programmable output VR10/11 DAC
- 6 bit programmable output AMD AM2 DAC
- 0.5 % output voltage accuracy
- Full-differential current sense across DCR
- Integrated remote sense buffer
- Feedback disconnection protection
- Adjustable oscillator from 100 kHz to 1 MHz
- LSLess startup to manage pre-biased ouptut
- Programmable soft-start
- Threshold sensitive enable pin for VTT sensing
- VFQFPN40 7x7 mm package

Applications

 High-Current VRM / VRD for desktop / server / workstation CPUs

For further information contact your local STMicroelectronics sales office.

- Graphic cards
- Low-voltage, high-current power supplies
- High-density DC-DC converters



Description

L6755 is a two-to-four phase controller designed to power Intel's most demanding Processors and, most in general, low-voltage, high-current power supplies. The device features LTB technology[®] to provide the fastest response to load transients thus minimizing the output filter composition.

L6755 embeds selectable DAC: Output Voltage is programmable up to 1.6000 V (Intel VR10 and VR11 DACs) or up to 1.550 V (AMD 6bit DAC) managing DVID transitions with ± 0.5 % output voltage accuracy over line, load and temperature variations.

The device assures fast protection against load over current and under / over voltage. Feedback disconnection prevents from damaging the load in case of misconnections in the system board.

Low-side-less start-up allows soft-start over prebiased output avoiding dangerous current return through the main inductors as well as negative spike at the load side.

L6755 is available in VFQFPN40 7x7 mm package.

Table 1. Device summary

Order code	Package	Packing
L6755	VFQFPN40	Tube
L6755TR	VFQFPN40	Tape and reel

February 2008

Rev 1

www.st.com

1 Revision history

Table 2.Document revision history

Date	Revision	Changes
15-Feb-2008	1	Initial release.



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3/3