TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# **TA76L431FB**

#### 2.495 V Adjustable High-Precision Shunt Regulators

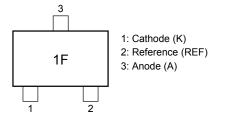
These devices are adjustable high-precision shunt regulators whose output voltage (VKA) can be set arbitrarily using two external resistors.

The devices have a precise internal reference voltage of 2.495 V, enabling them to operate at low voltage. In addition, they can be used as zener diodes to perform temperature compensation.

#### Features

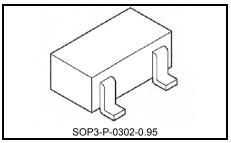
- Precision reference voltage :  $V_{REF} = 2.495 \text{ V} \pm 1.0\% \text{ (Ta} = 25^{\circ}\text{C)}$
- Adjustable output voltage:  $V_{REF} \le V_{OUT} \le 19 V$
- Minimum cathode current for regulation:  $I_{kmin} = 0.5 \text{ mA} (max)$
- Packages: Surface-mount S-Mini

#### Pin Assignment/Marking



#### How to Order

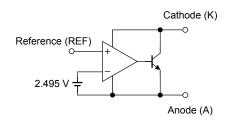
Product No.	Package	Packing Type and Capacity
TA76L431FB(TE85L,F)	S-Mini	Embossed tape: 3000 pcs/real

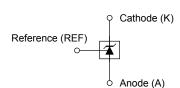


Weight: 0.012 g (typ.)

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## **Functional Block Diagram**

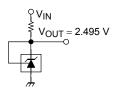


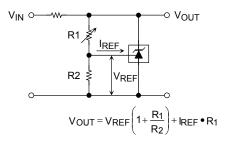


**Circuit Symbol** 

# **Typical Application Circuits**

(1) 2.495 V Reference ( $V_{KA} = V_{REF}$ )





(2) Shunt Regulator (V<sub>KA</sub> > V<sub>REF</sub>)

#### **Precautions During Use**

1. TA76L431FB

These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.

2. TA76L431FB

The oscillation frequency of these devices is determined by the value of the capacitor connected between the anode and the cathode.

When establishing maximum operating condition parameters, please derate the absolute maximum rating values specified in these datasheets so as to allow an operational safety margin.

Use of a laminated ceramic capacitor is recommended

# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Cathode voltage	V <sub>KA</sub>	20	V
Cathode current	١ <sub>K</sub>	50	mA
Cathode-anode reverse current	$-I_{K}$	50	mA
Reference voltage	V <sub>REF</sub>	7	V
Reference current	I <sub>REF</sub>	50	μA
Reference-anode reverse current	-I <sub>REF</sub>	10	mA
Power dissipation	PD	200 (Note 1)	mW
Thermal resistance	R <sub>th</sub>	625 (Note 1)	°C/W
Operating junction temperature	Tjopr	-40~150	°C
Junction temperature	Тј	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

Note 1: Glass epoxy substrate mounting:  $30 \text{ mm} \times 30 \text{ mm} \times 0.8 \text{ mmt}$  (Cu pad area  $35 \text{ mm}^2$ )

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Operating Ranges**

Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	V <sub>KA</sub>	$V_{REF}$	_	19	V
Cathode current	١ĸ	0.5		40	mA

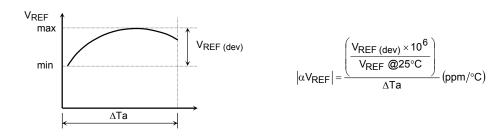
# Electrical Characteristics (Unless otherwise specified, $Ta = 25^{\circ}C$ , $I_{K} = 10 \text{ mA}$ )

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	V <sub>REF</sub>	$V_{KA} = V_{REF}$	2.470	2.495	2.520	V
Deviation of reference input voltage over temperature	V <sub>REF (dev)</sub>	$0^{\circ}C \leq Ta \leq 70^{\circ}C, V_{KA} = V_{REF}$		8	18	mV
Ratio of change in reference input voltage to the change in cathode voltage	$\Delta V_{REF} / \Delta V$	$V_{REF} \le V_{KA} \le 10 V$	_	0.8	2.4	mV/V
		$10 \text{ V} \leq \text{V}_{\text{KA}} \leq 19 \text{ V}$	_	0.8	2.0	
Reference input current	I <sub>REF</sub>	V <sub>KA</sub> = V <sub>REF</sub>	_	0.6	3	μA
Deviation of reference input current over temperature	IREF (dev)	$ \begin{array}{l} 0^{\circ}C \leq \text{Ta} \leq 70^{\circ}\text{C},  \text{V}_{KA} = \text{V}_{REF}, \\ \text{R}_{1} = 10  \text{k}\Omega,  \text{R}_{2} = \infty \end{array} $		0.3	1.2	μA
Minimum cathode current for regulation	I <sub>Kmin</sub>	VKA = VREF	_	0.2	0.5	mA
Off-State cathode current	I <sub>Koff</sub>	V <sub>KA</sub> = 19 V, V <sub>REF</sub> = 0 V			1.0	μA
Dynamic impedance	Z <sub>KA</sub>			0.2	0.5	Ω

#### **Precaution on Application**

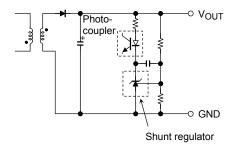
 $T_j = 25^{\circ}C$  in the measurement conditions of each item is a regulation for where a pulse test is carried out and any drift in the electrical characteristic due to a rise in the junction temperature of the chip may be disregarded.

The deviation parameters  $V_{REF}$  (dev) and  $I_{REF}$  (dev) are defined as the maximum variation of the  $V_{REF}$  and  $I_{REF}$  over the rated temperature range (Ta = 0 to 70°C). The average temperature coefficient of the  $V_{REF}$  is defined as:



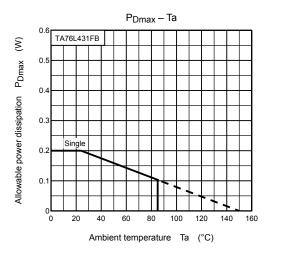
#### **Application Circuit Example**

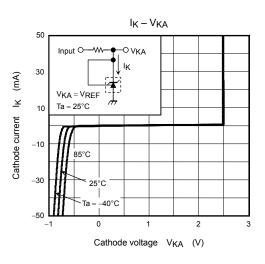
Error amplification circuit for switching power supply

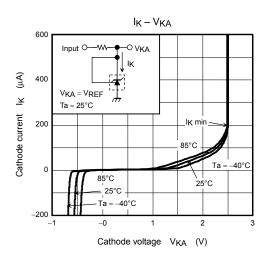


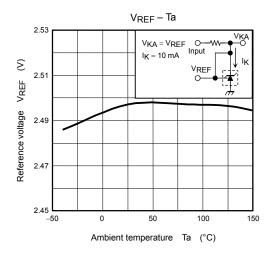
This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

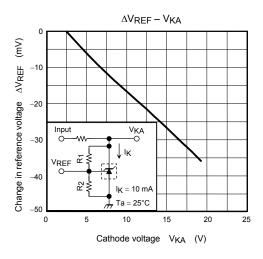
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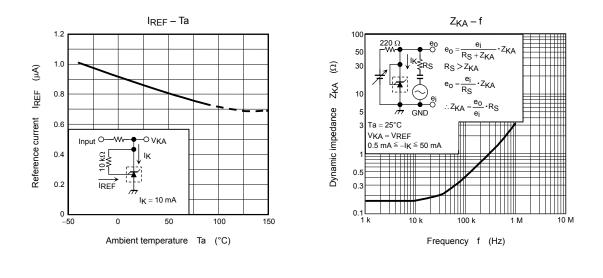


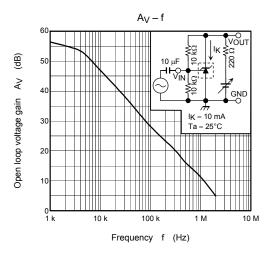


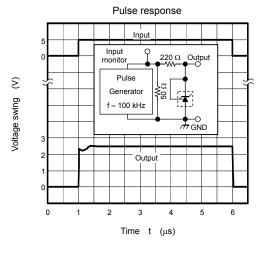


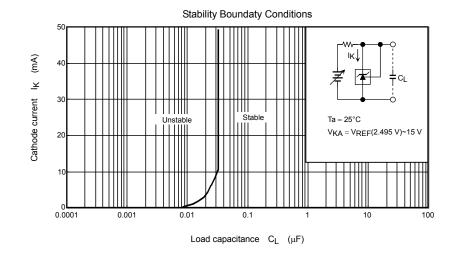


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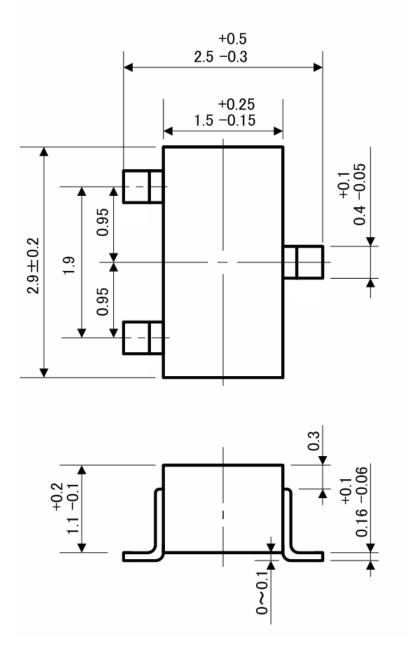


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# Package Dimensions

SOP3-P-0302-0.95

Unit:mm



Weight: 0.012 g (typ.)

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20070701-EN

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